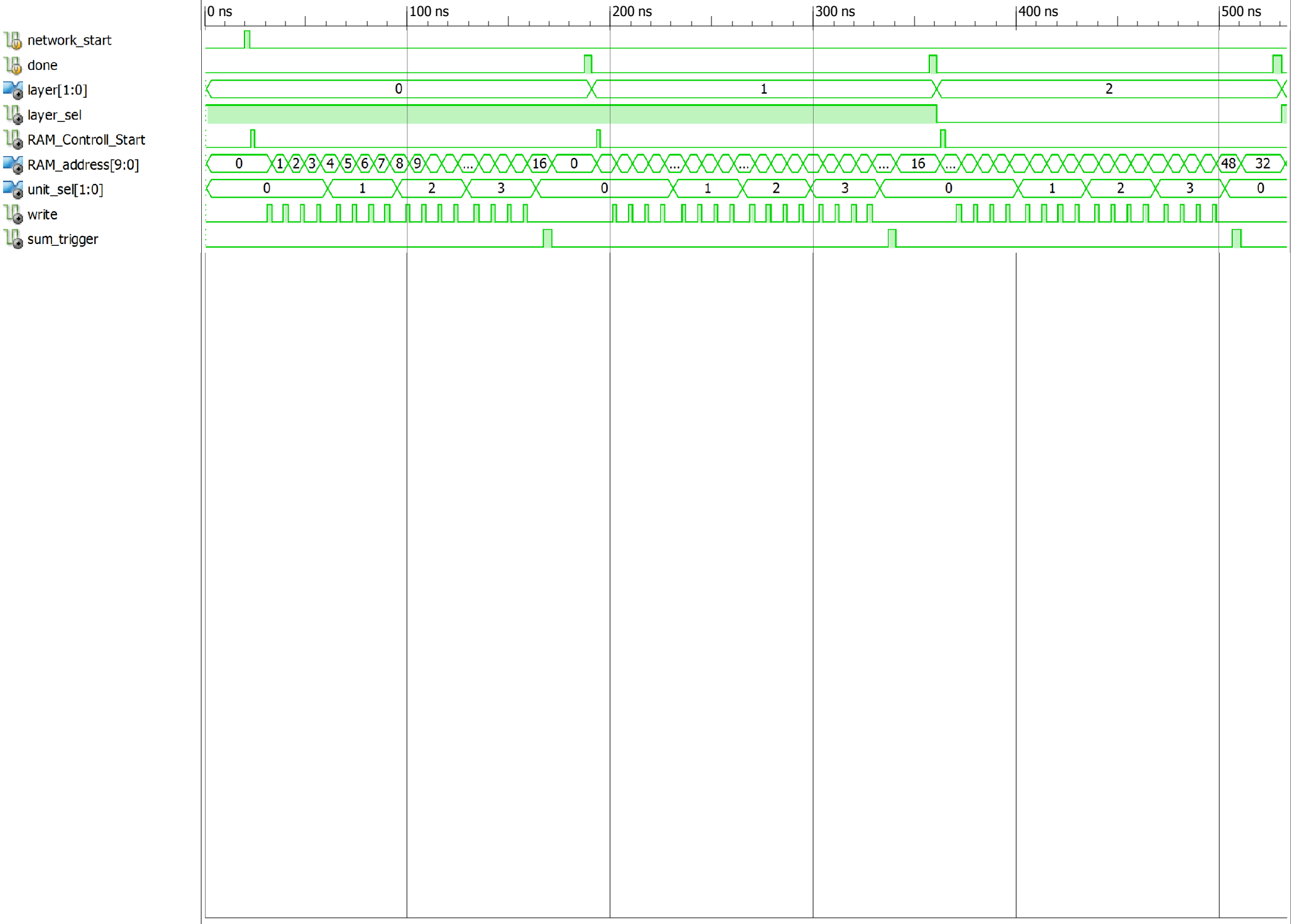
0 ns
100 ns
200 ns
300 ns
400 ns
500 ns
network_start
done
layer[1:0]
layer_sel
RAM_Controll_Start
RAM_address[9:0]
unit_sel[1:0]
write
sum_trigger
0
1
2
0 1 2 3 4 5 6 7 8 9 ... 16 0 ... ... 16 ... 48 32
0 1 2 3 0 1 2 3 0 1 2 3 0