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|  | Mind Games |

Memo

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| To: | Dr. Song |
| From: | Joseph Doherty, Robert Fendricks, Ian Kowalski |
| cc: |  |
| Date: | May 5, 2014 |
| Re: | Week 7 Progress Memo |
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**Summary of Last Meeting**

* Discussed data display
* Discussed decision to move away from embedded Linux

**Current Status**

* Single unit has almost been completed
  + Testing will be done by end of day

**Work Done**

* Elliot Activation function completed
* Divider Completed
* Layer Mux completed
* Multi input summation unit completed
* Python generation scripts completed as necessary
* Single unit completed but untested

**Current Objectives**

* Create test bench for single unit (JD)
* Determine number of clock cycles needed for unit to execute (JD)
* Figure out RAM communication (IK)
* Update paper (RF)

**Future Objectives**

* Implement unit controller
* Connect internal components for units
* Explore Programmable Logic and Processor communication

**Final Objectives**

* Units finished
* ARM programmed
* Network connected
* Network tested

**Red Flags**

* Difficult to understand Zynq documentation