KPK 8 BIT PROGRAMMABLE COMPUTER INSTRUCTION SET ARCHITECTURE (MULTIPLY)						
Instruction	**Memory Address (Binary)**	**Memory Address (Decimal)**	**CPU Op.Code/ Memory Address/ Value (Binary)**		**CPU Op.Code/ Memory Address/ Value (Decimal)**	
			CPU Op.Code	**Memory Address/ Value**	**CPU Op.Code**	**Memory Address/ Value**
LOAD-A	0000	0	0001	1110	1	14
LOAD-B & SUB	0001	1	0011	1100	3	12
JUMP-CARRY	0010	2	0111	0110	7	6
LOAD-A	0011	3	0001	1101	1	13
OUTPUT	0100	4	1110	0000	14	0
HALT	0101	5	1111	0000	15	0
STORE-A	0110	6	0100	1110	4	14
LOAD-A	0111	7	0001	1101	1	13
LOAD-B & ADD	1000	8	0010	1111	2	15
STORE-A	1001	9	0100	1101	4	13
JUMP	1010	10	0110	0000	6	0
-	1011	11	-	-	-	-
1	1100	12	0000	0001	VALUE	VALUE-1
PRODUCT	1101	13	0000	0000	0	0
X	1110	14	VALUE	VALUE	VALUE	VALUE
Y	1111	15	VALUE	VALUE	VALUE	VALUE