# One Clock To Rule Them All - Progress Report

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# Abstract

The purpose of this document is to discuss the progress of the project and what our team has done during the fall term. There will be a explanation of each document we wrote and a summary of what we did during each week of the term.

## I. PROJECT PURPOSES AND GOALS

For the fall term there were many documents to write that will help to layout and define what we will be working on during the winter and spring terms next year. Some of these documents will have changes made to them as we better understand out project, but the final product and goal will stay the same.

#### A. Problem Statement

The first of these documents to be written was the Problem Statement. In this document we were working to create a definition and description of what our research will be based around for the rest of the year.

# B. Requirements Document

The next document we wrote was the Requirements Document. In this document we were tasked with outlining the project so that both us and our client are clear on what the end product will be. This document is important because it will serve as a list to create user stories and as a road map of sorts on when we expect to have certain feature completed by.

# C. Technology Review

The next document assigned was the Technology Review. For this document many groups would research possible solutions to their requirements and then choose which one was best suited for the task. But out project differed from most for this document, mainly because 3 of our main features were chosen for us by our project owner Victor. The microcontroller, RTC, and LEDs that make up the main components of our clock were supplied by Victor. So instead of finding which products to use for our clock, we wrote a comparison of alternative components to look into the quality of Victor's decisions.

# D. Design Document

The final document, besides this one, that we wrote for the term was the Design Document. In this we document we created a road map for the rest of the year. The details of this map may change, but the big pictures and final destination will stay the same.

## II. CURRENT STAGE OF PROJECT

Currently in our project we are at a point where we are ready to start coding and designing features for the clock. We have the main 3 components, the microcontroller, LEDs, and RTC, and now are starting the process of learning to connect them and communicate between the components.

# III. PROBLEMS AND SOLUTIONS

The main problems that we have run into right now is learning the hardware side of our project. We have not researched the ways to connect all the hardware to the microcontroller. Once we figure out how to connect everything writing all the code for the clock should come quickly since it is in C and not a very complex system. Another problem we have had is time management on writing and planning out the documents outlined in the Project Purposes and Goals section. The main reason for our time management issues was that it usually took awhile for us to understand exactly what the assignment description was asking for. We found the assignment descriptions a little vague at times and we had to contact our TA or a professor to fully understand assignments. Meetings with our TA Jon Dodge were useful all term since we were able to ask questions and get a better understand of a document by asking him questions.

## IV. INTERESTING PIECES OF CODE

At this point of our project we have not written much code ourselves, but there are many hello world and demo programs that you can download for free on the MPLAB website. These basic programs will help us to learn the basics of how to interact and control different parts of the board. Also the online IDE for the microcontroller is similar to Eclipse in layout and has a user friendly interface. To have version control for our code we will need to download the MPLAB IDE so that we can push and pull from git easier.

# V. RETROSPECTIVE OF THE PAST 10 WEEKS

Positives Column	Deltas Column	Actions Column
test text test text test text text	test text test text text text text text	test text test text text text text