Complete 64-bit ALU Unit

A complete 64-bit ALU unit built using sixty-four 1-bit ALU units and an ALU Control Unit.

VERILOG CODE:

```
reg[63:0] A;
reg[63:0] B;
wlre[63:0] Result;
reg CarryIn;
wl test (op. ALU Op. funct7, funct3);
ALU alu1(Result, Zero, CarryOut, A, B, CarryIn, Op);
initial begin
for(j = 0; j < 4; j++) begin
#Z0 ALU Op = 2'bb01;
CarryIn = Op[2];
funct7 = 7'bb010111;
funct3 = 3'b111;
Sdisplay("case 0: Add(ALU_Op code)\n\n");
#Z0 ALU_Op = 2'bb1;
CarryIn = Op[2];
funct7 = 7'bb000000;
funct3 = 3'b111;
Sdisplay("case 1: Subtract(ALU_Op code)\n\n");
#Z0 ALU_Op = 2'bb1;
CarryIn = Op[2];
funct7 = 7'bb000000;
funct3 = 3'b1000;
CarryIn = Op[2];
funct7 = 7'bb000000;
funct3 = 3'b1000;
Sdisplay("case 2: Add(R-type)\n\n");
#Z0 ALU_Op = 2'b10;
CarryIn = Op[2];
funct7 = 7'bb000000;
funct3 = 3'b000;
funct3 = 3'b000;
funct3 = 3'b000;
funct3 = 3'b1000;
funct3 = 3'b1000;
funct3 = 3'b1000;
funct3 = 3'b111;
Sdisplay("case 3: Subtract(R-type)\n\n");
#Z0 ALU_Op = 2'b10;
CarryIn = Op[2];
funct7 = 7'b0000000;
funct3 = 3'b111;
Sdisplay("case 4: And(R-type)\n\n");
#Z0 ALU_Op = 2'b10;
CarryIn = Op[2];
funct7 = 7'b0000000;
funct7 = 7'b0000000;
funct3 = 3'b110;
Sdisplay("case 5: Or(R-type)\n\n");
#Z0 ALU_Op = 2'b10;
CarryIn = Op[2];
funct7 = 7'b0000000;
funct3 = 3'b110;
Sdisplay("case 5: Or(R-type)\n\n");
end
```

```
#20 ALU_Op = 2'b10;
   CarryIn = 0p[2];
   funct7 = 7'b0100000;
   funct3 = 3'b000;
   $display("Case 3: Subtract(R-type)\n\n");
#20 ALU_Op = 2'b10;
   CarryIn = 0p[2];
   funct7 = 7'b0000000;
   funct3 = 3'b111;
   Sdisplay("Case 4: And(R-type)\n\n");
      funct3 = 3'b111;
    Sdisplay("Case 4: And(R-type)\n\n");
#20 ALU_Op = 2'b10;
    CarryIn = Op[2];
    funct7 = 7'b00000000;
    funct3 = 3'b110;
    Sdisplay("Case 5: Or(R-type)\n\n");
       end
       initial begin
                                                                                //A= 1
//B = -1
            A = 1;
B = -64'h1;
       #120
                                                                                                        //A = max
//B = 1
             A = 64'h7fffffffffffff;
             B = 1;
      #120
            A = 64'h7fffffffffffff;
B = 64'h8000000000000000;
       #120
            A = 0;
B = 0;
       end
       initial begin
end
      end
```

OUTPUT:

```
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            funct7 = 0100111
ALU Op = 00
                                 funct3 = 111
Opcode = 0010
Time = 21
A = 00000000000000001 B = ffffffffffffff
                                     Carry In = x
Carry Out = 1
Case 1: Subtract(ALU_Op code)
            funct7 = 0000000 funct3 = 111
ALU_0p = 01
Opcode = 0110
Carry In = 0
Result = 00000000000000002
                      Zero = 0
                                     Carry Out = 0
Case 2: Add(R-type)
Opcode = 0010
Time = 61
A = 0000000000000001 B = fffffffffffff
                                    Carry In = 1
Result = 0000000000000000
                                      Carry Out = 1
Case 3: Subtract(R-type)
```

```
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Case 0: Add(ALU_Op code)
ALU_Op = 00 funct7 = 0100111 funct3 = 111
Opcode = 0010
Time = 141
A = 7fffffffffffff B = 000000000000001
                                           Carry In = 0
Result = 8000000000000000
                                           Carry Out = 0
Case 1: Subtract(ALU_Op code)
Opcode = 0110
Time = 161
A = 7fffffffffffff B = 0000000000000001
                                          Carry In = 0
Result = 7ffffffffffffe
                                           Carry Out = 1
Case 2: Add(R-type)
Opcode = 0010
Time = 181
A = 7fffffffffffff B = 000000000000001
                                         Carry In = 1
Result = 8000000000000000
                                            Carry Out = 0
```

```
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Case 3: Subtract(R-type)
ALU_Op = 10
             funct7 = 0100000
                                             funct3 = 000
Opcode = 0110
Time = 321
A = 7ffffffffffffff
                           B = 8000000000000000
                                                      Carry In = 0
Result = ffffffffffffff
                                Zero = 0
                                                      Carry Out = 0
Case 4: And(R-type)
Opcode = 0000
Time = 341
A = 7ffffffffffffff
                         B = 8000000000000000
                                                      Carry In = 1
Result = 0000000000000000
                                                      Carry Out = 0
Case 5: Or(R-type)
ALU_Op = 10
                  funct7 = 0000000
                                             funct3 = 110
Opcode = 0001
Time = 361
A = 00000000000000000
                           B = 0000000000000000
                                                      Carry In = 0
                                                      Carry Out = 0
Result = 00000000000000000
```

Case 0: Add(ALU_Op code) ALU Op = 00 funct7 = 0100111 funct3 = 111 Opcode = 0010 Time = 381 A = 000000000000000000 B = 0000000000000000 Carry In = 0 Result = 0000000000000000 Carry Out = 0 Case 1: Subtract(ALU_Op code) ALU_Op = 01 funct7 = 0000000 funct3 = 111 Opcode = 0110 Time = 401 A = 000000000000000000 B = 0000000000000000 Carry In = 0 Zero = 1 Result = 0000000000000000 Carry Out = 1 Case 2: Add(R-type) ALU_Op = 10 funct7 = 0000000 funct3 = 000 Opcode = 0010 Time = 421 A = 000000000000000000 B = 0000000000000000 Carry In = 1 Result = 00000000000000000 Carry Out = 0

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