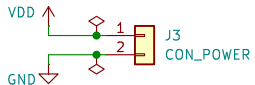
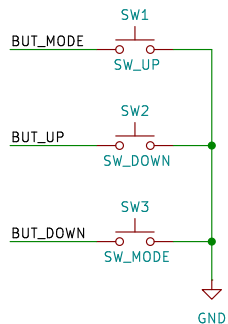
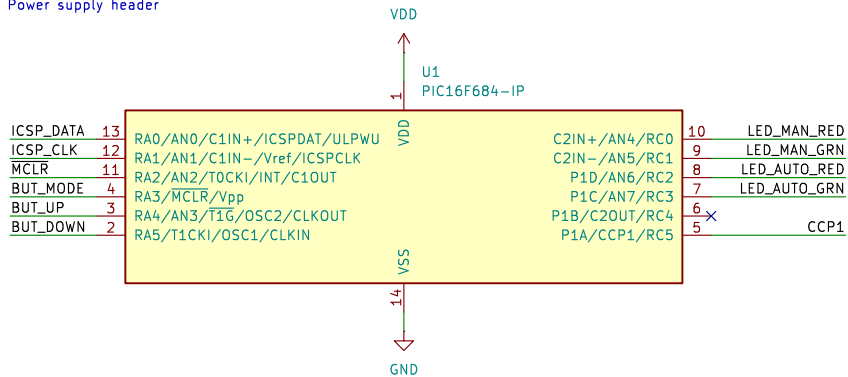


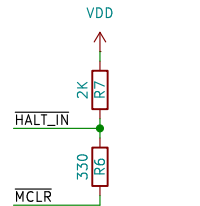
Programming header



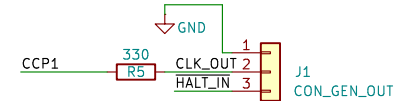
Power supply header



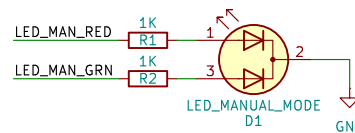
User interface buttons:  
\* MODE: AUTO / MANUAL  
\* UP / set HIGH  
\* DOWN /set LOW



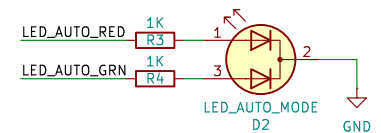
External pull up resistors  
for MCLR / HALT signals



Generator signals header



LED Manual MODE:  
\* when RED – out is LOW  
\* when GREEN – out is HIGH



LED Auto MODE :  
\* when RED – out is LOW  
\* when GREEN – out is HIGH  
\* when YELLOW – out is switching fast

K. Yosifov

**KrasYo Technologies**

Sheet: /

File: dev-clock-generator.kicad\_sch

**Title: Dev Clock Generator**

Size: A4 Date: 2024-12-06

KiCad E.D.A. 8.0.6

**Rev: 1.0**

Id: 1/1