

Ans(i) An operating system is a construct that allows the user application programs to interact with the system hardware. Operating system itself does not provide any function but it provides an atmosphere in which different applications and programs can do useful work.

The operating system can be observed from the point of view of the user or the system. This is known as the user view and the system view respectively.

### ~~USER VIEW~~ USER VIEW:

- Depends on system interface by user
- Different view experiences—
  - If user is using a personal computer the operating system is largely designed to make interaction easy. personal computer uses all the resources ~~are~~ available and no sharing.
  - OS is largely concerned to resource utilization.
  - If user is on workstation then OS need to focus on both individual usage and sharing of resource.

### SYSTEM VIEW:

- Bridge between applications and hardware
- It is most intimate with hardware used to control.
- Different type of system view for OS.
- System view OS as resource allocator.
  - as a control program
  - as a way for easier hardware
- To easy communication
- Considered to run all time in background.

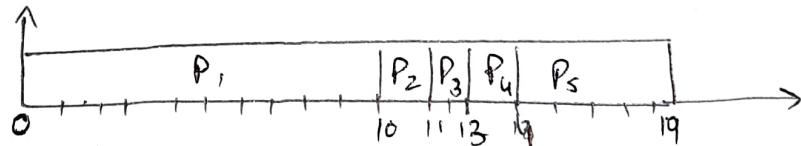
(2)

## Types of operating System.

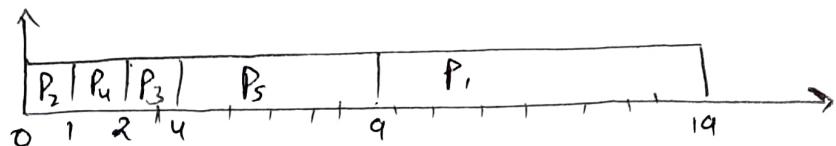
- 1) Batch operating System → Does not interact with computer directly. There is an operator, which takes similar jobs having the same requirement and group them into batches.
- 2) Time sharing operating → Time given for execution, then OS switches over to next task. These are also known Multi-tasking system.
- 3) Multi programming System: Various autonomous - inter-connected computers communicate using a shared communication network.
- 4) Multi processing System: These system run on a server and provide the capability to manage data, user groups, security, application and other network function.

Q-1(i)

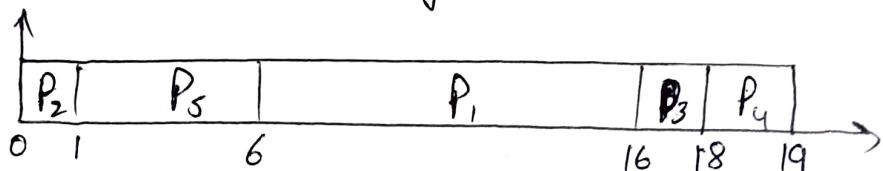
Ans 1(ii) • a) FCFS - Gantt chart



SJF - Gantt chart



non-preemptive priority - Gantt chart.



(3)

## b) Turn Around Time (TAT)

P Jf<sub>s</sub>

Process

P<sub>1</sub>P<sub>2</sub>P<sub>3</sub>P<sub>4</sub>P<sub>5</sub>

TAT

$$10 - 0 = 10$$

$$11 - 0 = 11$$

$$13 - 0 = 13$$

$$14 - 0 = 14$$

$$19 - 0 = 19$$

$$\text{Total TAT} = 67$$

S J f

Process

P<sub>1</sub>P<sub>2</sub>P<sub>3</sub>P<sub>4</sub>P<sub>5</sub>

TAT

$$19 - 0 = 19$$

$$1 - 0 = 1$$

$$4 - 0 = 4$$

$$2 - 0 = 2$$

$$9 - 0 = 9$$

$$\text{Total TAT} = 35$$

## Non Preemptive Priority

Process

P<sub>1</sub>

TAT

$$16 - 0 = 16$$

P<sub>2</sub>

$$1 - 0 = 1$$

P<sub>3</sub>

$$18 - 0 = 18$$

P<sub>4</sub>

$$19 - 0 = 19$$

P<sub>5</sub>

$$6 - 0 = 6$$

$$\text{Total TAT} = 60$$

(4)

c) Waiting Time

Process	FIFO	SJF	NP Priority
	WT	WT	WT
P <sub>1</sub>	0	9	6
P <sub>2</sub>	10	0	0
P <sub>3</sub>	11	2	16
P <sub>4</sub>	13	1	18
P <sub>5</sub>	14	4	1
Total	<u>48</u>	<u>16</u>	<u>41</u>

d) Average TAT =  $\frac{\text{Total TAT}}{\text{no. of Process}}$       Average WT =  $\frac{\text{Total WT}}{\text{no. of Process}}$

FIFO	13.6	9.6
SJF	7	3.2
NP priority	12	8.2

Q-2(i)

Ans 2(i) (i) Race condition  $\rightarrow$  Potential IPC problem when two or ~~more~~ more process interact via shared data. Final outcome depends on exact instruction sequence.

Depends on which process "wins" the race.

(ii) Critical Section  $\rightarrow$  Critical section of program is where global shared memory is being accessed. Process has exclusive access to share modified data while in critical region.

For efficient operation -

- No two processes can be inside their critical section.
- No assumption needed about relative process speed.
- No process can be indefinitely postponed from entering its critical section.
- No process stopped outside its critical section can block other process.

(S)

(P17) Sleep and Wake up  $\Rightarrow$  Essentially, when a process P is not permitted to access its critical section, it uses a system call known as sleep, which causes that process to block. The process will not be scheduled to run again, until another process uses to wake up system call. In most cases, wake up P is called by a process when it leaves its critical section or any other process have blocked.

(Q8) Sleeping Barber Problem  $\Rightarrow$  It has one barber, one barber chair, and w chairs for waiting customers, if any to sit on. If there is no customer present, Barber sits down on chair and falls asleep. Wakes up when customer arrives. Customer leaves shop if all chairs are full. The problem is to program the the barber and the customer without getting into race condition.

O-2(iii)

Ans Q8(iii) Process of life cycle can be defined by a state diagram which has states representing the execution states of process at various time and transition. That shows changes in execution status. To maintain the information about a process, the OS uses process control block or Task control block.

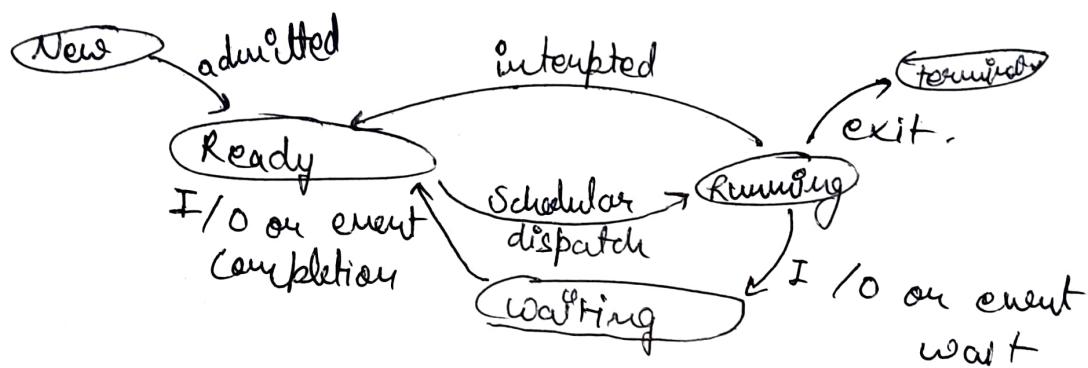
Process State
Process number
Program counter
Register
Memory limit
List of open files
...

Process control Block (PCB)  
Task control Block.

(6)

Process life cycle consists of five stages which are -

- New → The process which is being created.
- Running → Instruction being executed.
- Waiting → The process is waiting for an event to occur.
- Ready → Waiting to assigned to a process.
- Terminated → Process completed its execution.

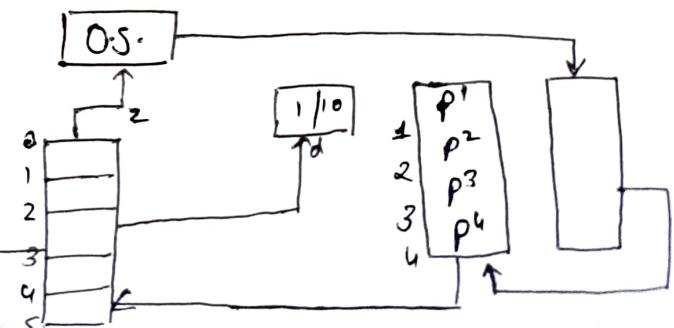
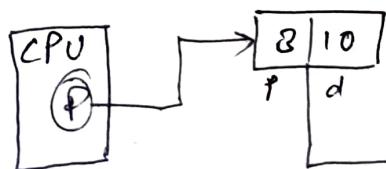


Q-3(i),

Ans 3(i) Virtual memory is a storage mechanism which offers user an illusion of having a very big main memory. It is done by treating a part of secondary memory as the main memory. In virtual memory, the user can store processes with a bigger size than the available main memory.

Various ways for implementing it are:-

- a) Demand Paging → The process of loading the page into memory on demand (whenever page fault occurs) is known as demand paging.



- If the CPU tries to refer to page that is currently ~~not~~<sup>⑦</sup> available in the main memory, it generates an interrupt indicating a memory access fault.
- OS puts the interrupted process in a blocking state.

b) Swapping:- So it is a process out means removing all of its pages from memory or making them so, that they will be removed by the normal page replacement process.

c) Thrashing → At a given time, only a few pages of any process are in the main memory and therefore more processes can be maintained in memory. Pages not swapped in and out of memory.

Q3(5%)  
Ans 2.11)

### Segment table

Segment	Base	Length
0	219	600
1	2300	14
2	90	100
3	1327	580
4	1952	96

i) 0, 430 → Base = 219 ; offset = 430 ; length = 60  
 offset < length, ∴ It is valid physical address  
 $\text{Address} = 219 + 430 = 649$

ii) 1, 10 → Base = 2300 ; offset = 10 ; length = 14  
 offset < length, ∴ It is valid physical address  
 $\text{Address} = 2300 + 10 = 2310$

(8)

118) 4, 12 → Base = 1952; offset = 12; length = 96  
 offset < length, ∴ It is valid physical address

$$P_V) \quad \text{Address} = 1952 + 12 = 1964$$

Q, 500 : Illegal address since size is 100 and offset 500. ∴ It is INVALID.

Q-41(i)

Ans 4(i),

### Need Matrix.

	A	B	C	D
P <sub>0</sub>	0	0	0	0
P <sub>1</sub>	0	7	5	0
P <sub>2</sub>	1	0	0	2
P <sub>3</sub>	0	0	2	0
P <sub>4</sub>	0	6	4	2

- To check if system is in a safe state, we need to find safety sequence such that it satisfy the criteria needs.

Need ≤ Available

① P<sub>0</sub>

as Need(P<sub>0</sub>) ≤ Available, we select P<sub>0</sub> [0000] ≤ [1520]

After Completion [Available] =

[1532]

② P<sub>1</sub> Need(P<sub>1</sub>) ≥ Available = [07 50] > [1532]  
 \* Not fulfilling criteria.

③ P<sub>2</sub> Need(P<sub>2</sub>) ≤ Available

[1002] [1532]

After Completion [Available] = [2886]

④ P<sub>3</sub> Need(P<sub>3</sub>) ≤ Available  
 [0020] [2886]

After [Available] = [214118]

⑤ P<sub>4</sub> Need(P<sub>4</sub>) ≤ Available  
 [0642] [214118]

After [Available] = [214122]

④

Repeat.

⑥  $\text{Need}(P_i) \leq \text{Available}$ 

Process completed.  $\hookrightarrow [3 \quad 14 \quad 12 \quad 12]$

$\text{Safe Seq} = \langle P_0, P_2, P_3, P_4, P_1 \rangle$

System is in safe state.

19) A request for process  $P_2$  arrives for  $(0, 4, 2, 0)$

$\text{Need}(P_2) > \text{Req}(P_2)$

$[0 \ 7 \ 50] > [0, 4, 2, 0] \rightarrow \text{true.}$

$\text{Available} > \text{Req}(P_2)$

$[15 \ 20] > [0 \ 4 \ 2 \ 0] - \text{true.}$

Updating values.

	Allocation	Max	Need	Available
$P_1$	14 2 0	1750	0330	1100

on verification we can see that still the safe sequence remains the same.

Q-4 (i)

Ans 4 (i)

(1) FCFS scheduling algorithm.

- Total seek distance:

$$(152 - 86) + (1470 - 86) + (1470 - 913) + (1774 - 913) + \\ (1850 - 1774) + (1850 - 850) + (1525 - 850) + (1728 - 1525) + (728 - 1000) \\ = 6423 \text{ cylinders.}$$

(2) SSTF scheduling algorithm.

Total distance = 1830 cylinders.

10

### (3) SCAN Algorithm

Total distance  $\rightarrow$  9760 cylinders.

## (4) C-scan Algorithm

Total distance  $\rightarrow$  9973 cylinders

## (5) LOOK Algorithm

Total distance  $\rightarrow$  3462 cylinders

## (6) C - LOOK Algorithm

Total distance = 3503 cylinders

Ans: i) FIFO (First in First out) page trace analysis.

Page Request	a	b	a	c	a	b	d	d	b	a	c	d
Page fault	*	*	*			*		*		*		
Page frame 1	a	a	a	a	a	a	d	d	d	d	d	d
Page frame 2	b	b	b	t	b	b	b	a	a	a		
Page frame 3			c	c	c	c	c	c	c	c	c	c
Stack 1 (Top)	a	b	b	c	c	c	d	d	a	a	a	
Stack 2	a	a	b	b	b	c	c	d	d	d	d	
Stack 3 (oldest)			a	a	a	b	b	c	c	c	c	
Stack 4 (Swapped) out			a	a	b	b	c	c	b	b	b	

11.

5 page faults in 11 steps yields:

$$\text{Success rate } \Rightarrow 6/11 \Rightarrow \frac{6}{11} \times 100 = 55\%.$$

$$\text{Failure rate } \Rightarrow 5/11 \Rightarrow \frac{5}{11} \times 100 = 45\%.$$

(ii) LRU (Least Recently Used) page trace analysis.

Page Request	a	b	a	c	a	b	d	b	a	c	d
Page Fault	*	*		*			*		*		*
Page Frame 1	a	a	a	a	a	a	a	a	a	a	a
Page Frame 2	b	b	b	b	b	b	b	b	b	b	d
Page Frame 3	.	c	c	c	d	d	d	d	c	c	
Stack 1 (Top)	a	b	a	c	a	b	d	b	a	c	d
Stack 2	a	b	a	c	a	d	d	b	a	c	
Stack 3 (Old)	.	b	b	c	b	a	a	d	b	b	
Stack 4 (Swapped Out)	.	b	b	c	c	c	c	a	b	b	

6 faults in 11 steps yields:

$$\text{Success rate} = \frac{5}{11} \times 100 = 45\%.$$

$$\text{Failure rate} = \frac{6}{11} \times 100 = 55\%.$$

The FIFO (first in first out) yielded a more favorable success rate than the LRU (Least Recently Used) method, but we cannot find the optimal method by taking just one sample.

iii) Optimal Method for page trace analysis.

Page Request	a	b	a	c	a	b	d	t	a	c	d
Page Fault	*	*		*			*	*			*
Page Frame 1	a	a	a	a	a	a	a	a	a	a	a
Page Frame 2	b	b	b	b	b	b	d	b	b	b	b
Page Frame 3	.	c	c	c	c	c	c	c	c	c	c
Stack 1 (Top)	a	b	a	c	a	b	d	b	a	c	d
Stack 2	a	b	a	c	a	a	t	b	a	a	
Stack 3 (Old)	.	b	b	c	c	c	c	a	b	b	
Stack 4 (Swapped Out)	.	b	b	c	c	d	d	d	c		

## a) Semaphore and Monitor :

Monitor : High level synchronization construct. abstract data type. The monitor type contains shared variable and set of procedures.

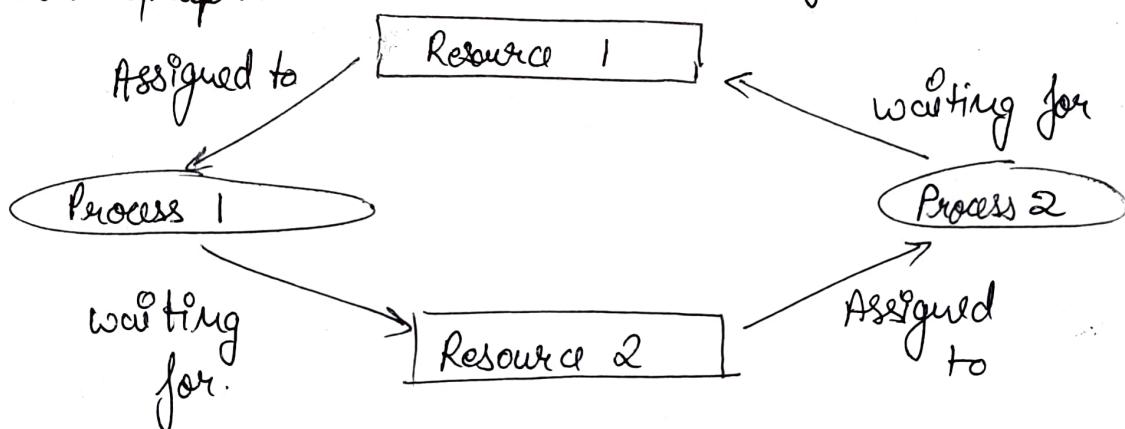
Any process wishes to access the shared variables in monitor, need to access through procedures. Process line up in a queue and are only provided access.

Semaphore  $\rightarrow$  lower level object. Semaphore is a non-negative integer variables. The value indicated by the numbers to resource available in system. Modified by wait() and signal() operations.

$\begin{cases} \rightarrow & \text{Binary Semaphore} \\ \rightarrow & \text{Counting Semaphore} \end{cases}$

b) Deadlock Detection  $\rightarrow$ 

\* If 0 single instance  $\rightarrow$  Here, for detection, we can run an algorithm to check for the cycle in the Resource Allocation Graph.



\* If multiple instances  $\rightarrow$  Detection is necessary but not sufficient condition for Deadlock detection.

## Deadlock Recovery :-

A traditional operating system such as windows doesn't deal with deadlock recovery as it is a time and space consuming process. Real-time operating systems use Deadlock recovery.