

## **COL215: LAB ASSIGNMENT 1**

**Student\_1: Pavas Goyal (2018CS10363)**

**Student\_2: Dipen Kumar (2018CS50098)**

### **Status Of Implementation: (Completed)**

We have done the whole job of logic designing (schematic file) , simulation , installing and testing it on the FPGA board.

### **Verification:**

We have verified all the 22 test cases taken during stimulation on the FPGA board.

Our logic design is following all priority orders mentioned in question.

### **Testcases:**

We have taken all the same test cases as taken during stimulation in assignment 1 and we got same results on the FPGA board.