

A

A

B

B

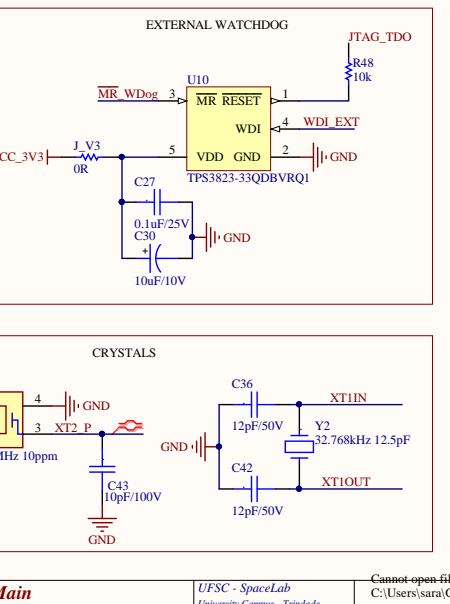
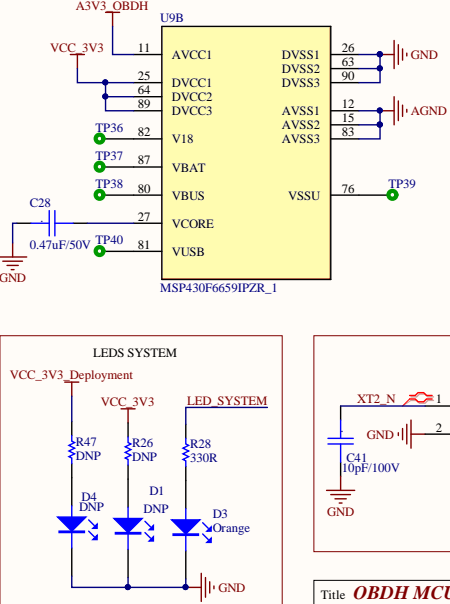
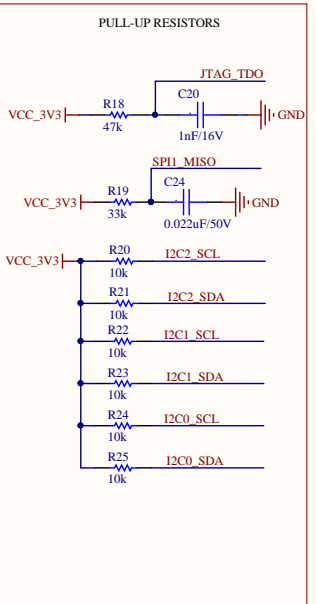
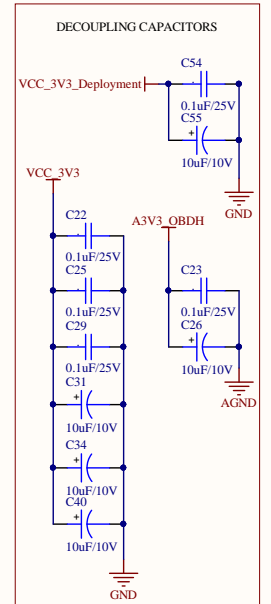
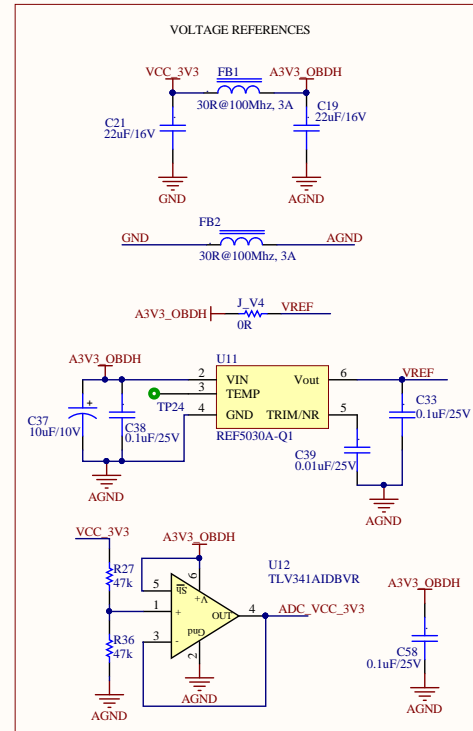
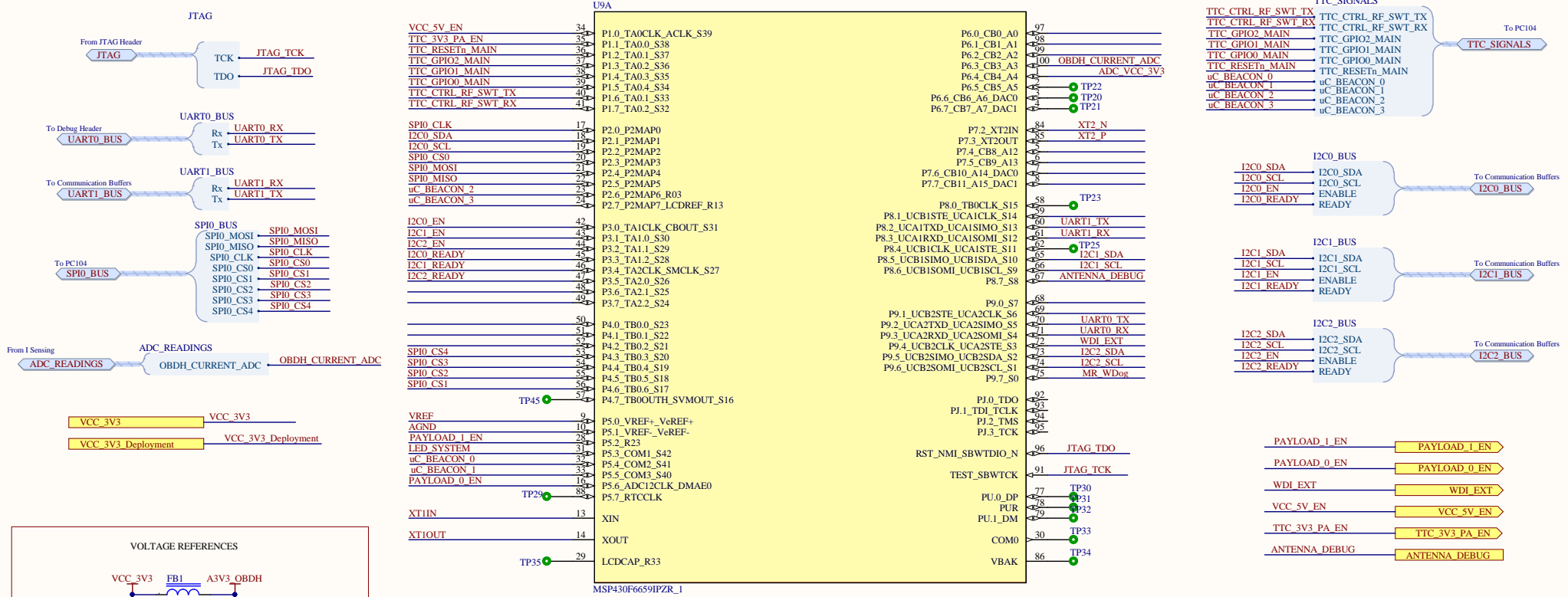
C

C

D

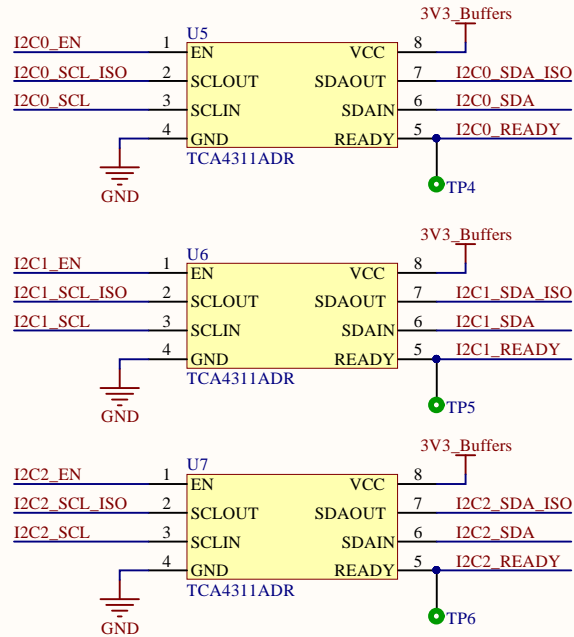
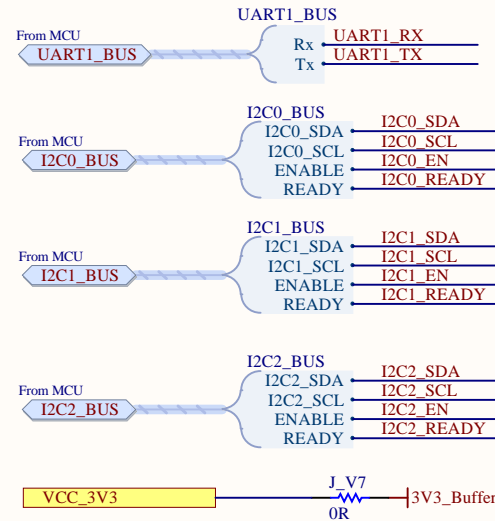
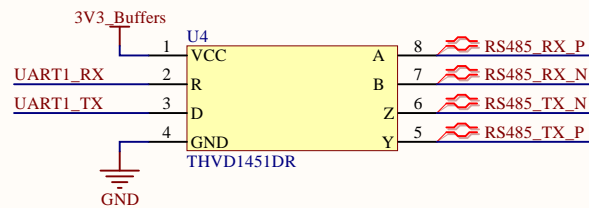
D

OBDH

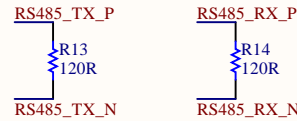


Title <i>OBDH MCU Main</i>			UFSC - SpaceLab University Campus - Trindade Dep. of Electrical Engineering - CTC Florianópolis, Santa Catarina, Brazil CEP: 88040 - 900		Cannot open file C:\Users\sara\Google Drive\PCB\patch-2 56.png
Size: A3	Project: FloripaSat	Revision: V0.0			
Date: 13/12/2019	Time: 23:08:50	Sheet 3 of 7			
Drawn by: André M. P. Mattos		Model: Eng 2. ALT			

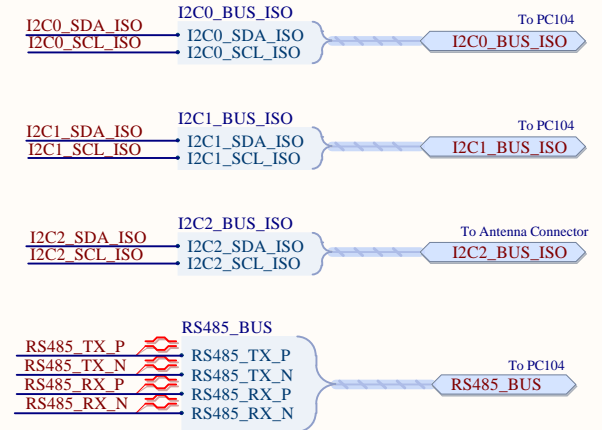
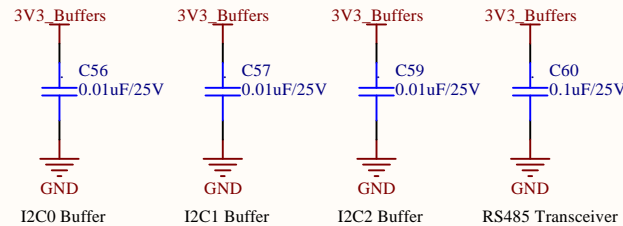
I2C BUFFERS

RS485 TRANSCEIVER
(Full-Duplex)

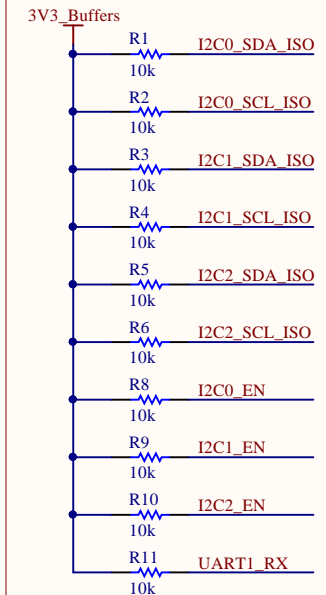
TERMINATION RESISTORS



DECOUPLING CAPACITORS

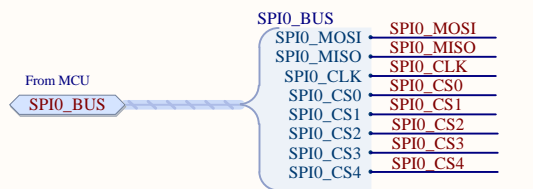


PULL-UP RESISTORS

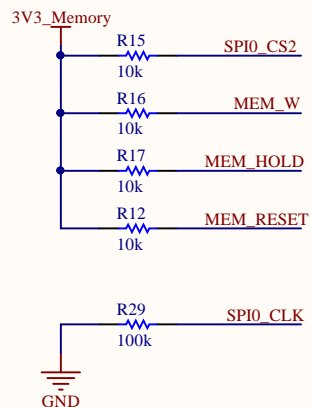
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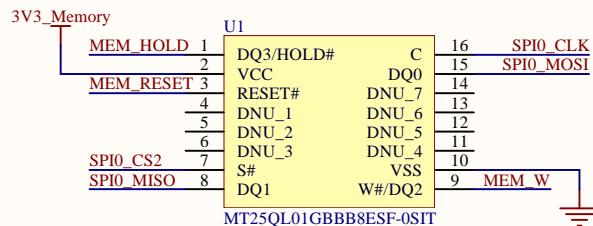




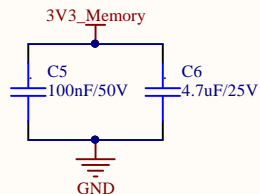
PULL-UP AND PULL-DOWN RESISTORS



NON-VOLATILE MEMORY



DECOUPLING CAPACITORS



Title **Non Volatile Memory**

Size: **A4**

Project: **FloripaSat**

Revision: **V0.0**

Date: **13/12/2019**

Time: **23:08:50**

Sheet **5** of **7**

Drawn By: **André M. P. Mattos**

Model: **Eng 2 ALT**

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