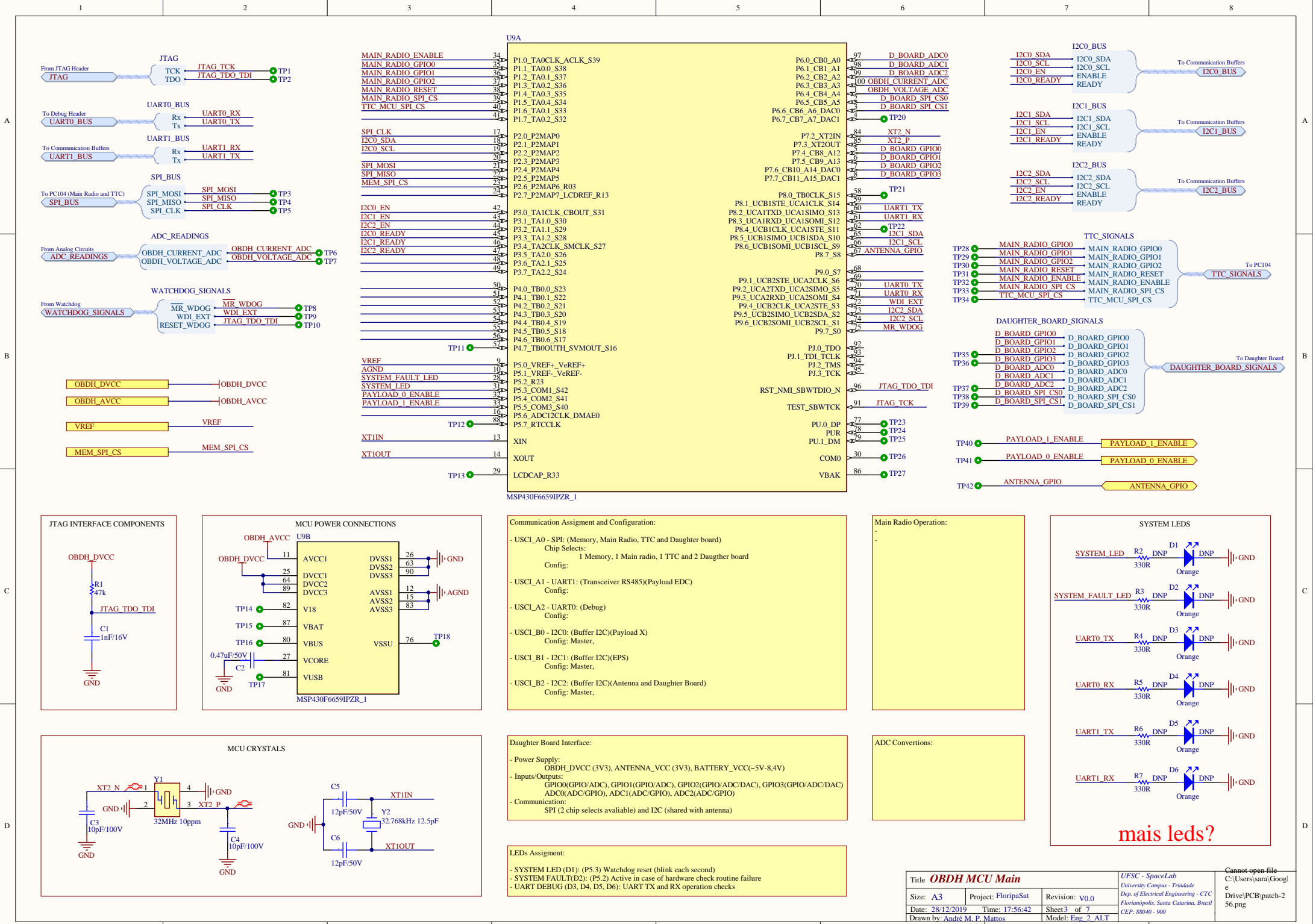
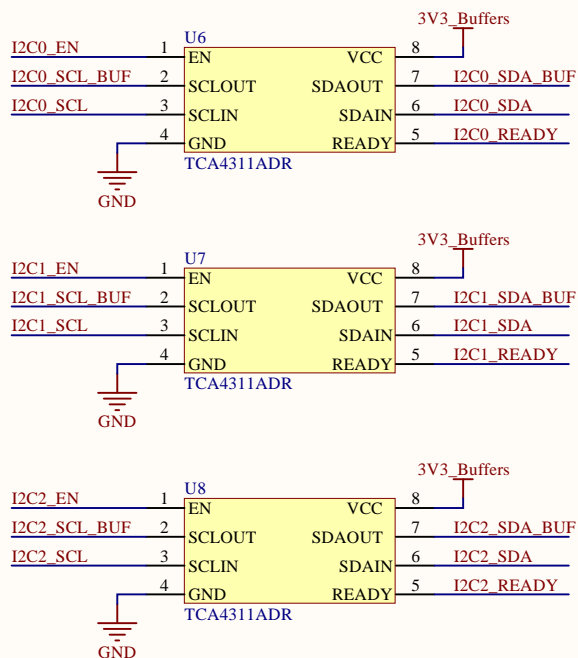
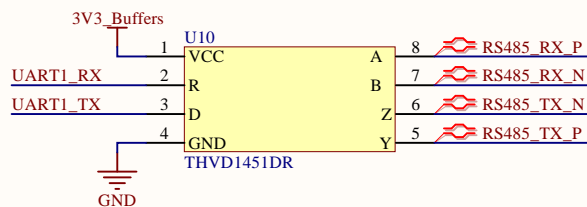


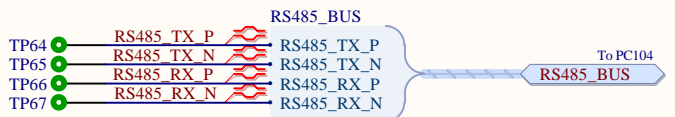
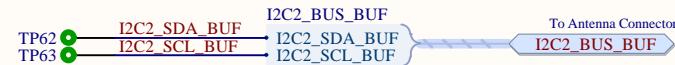
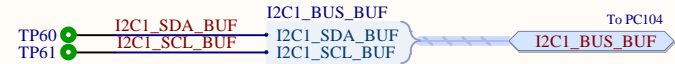
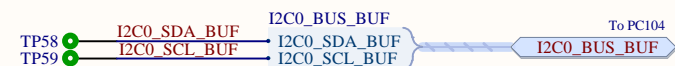
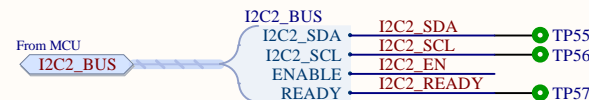
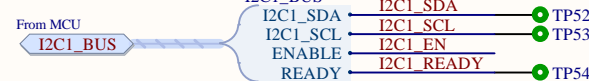
Title OBDH Topology			UFSC - SpaceLab University Campus - Trindade Dep. of Electrical Engineering - CTC Florianópolis, Santa Catarina, Brazil CEP: 88040 - 900		Cannot open file C:\Users\sara\Google Drive\PCB\patch-256.png
Size: A3	Project: FloripaSat	Revision: V0.0			
Date: 28/12/2019	Time: 17:56:42	Sheet 2 of 7			
Drawn by: André M. P. Mattos		Model: Eng 2_ALT			



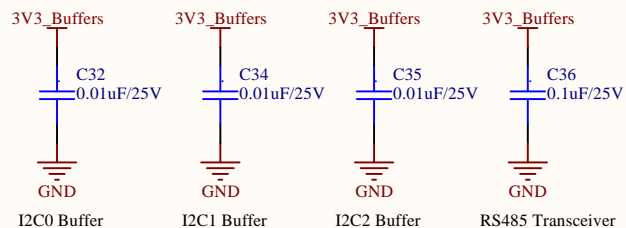
I2C BUFFERS

RS485 TRANSCEIVER
(Full-Duplex)

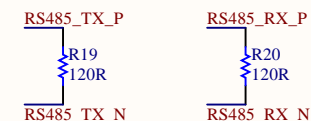
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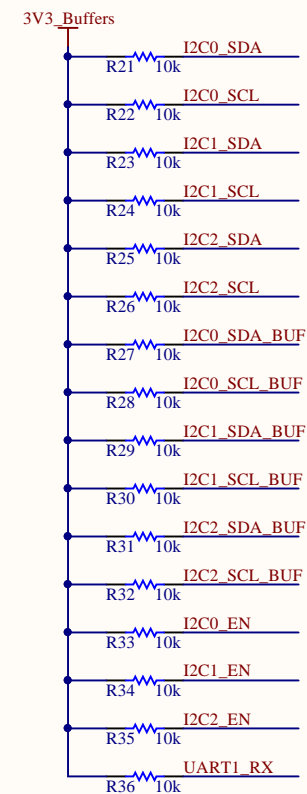
DECOUPLING CAPACITORS



TERMINATION RESISTORS



PULL-UP RESISTORS

Title **Non Volatile Memory**

Size: A4

Project: FloripaSat

Revision: V0.0

Date: 28/12/2019 Time: 17:56:43

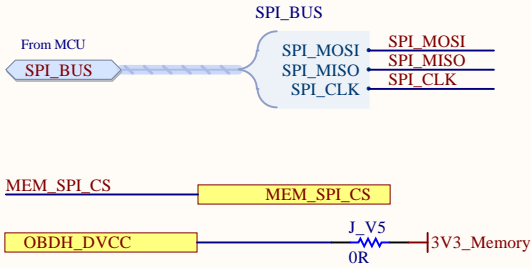
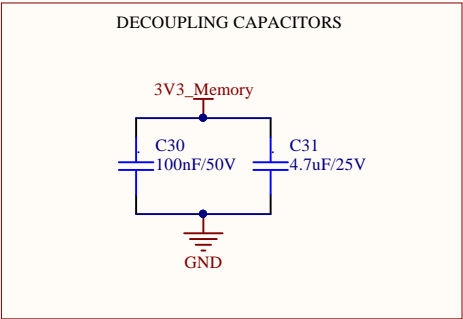
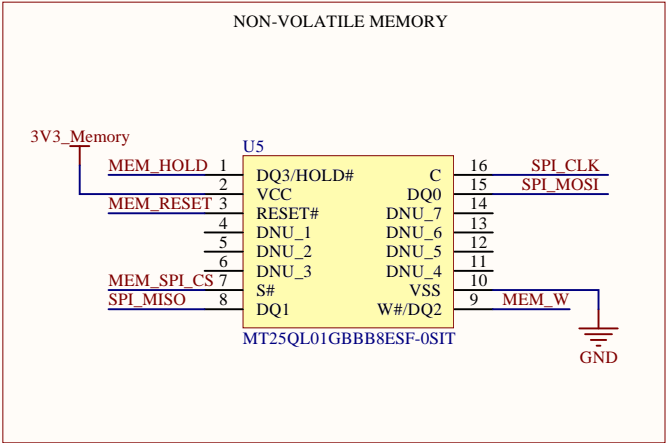
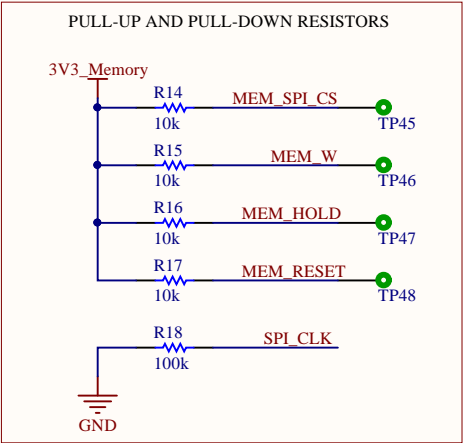
Sheet 5 of 7

Drawn By: André M. P. Mattos

Model: Eng 2 ALT

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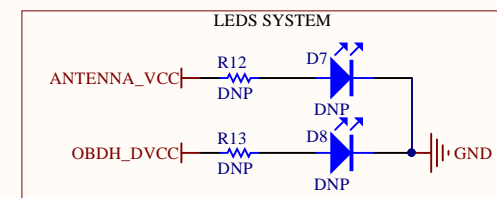
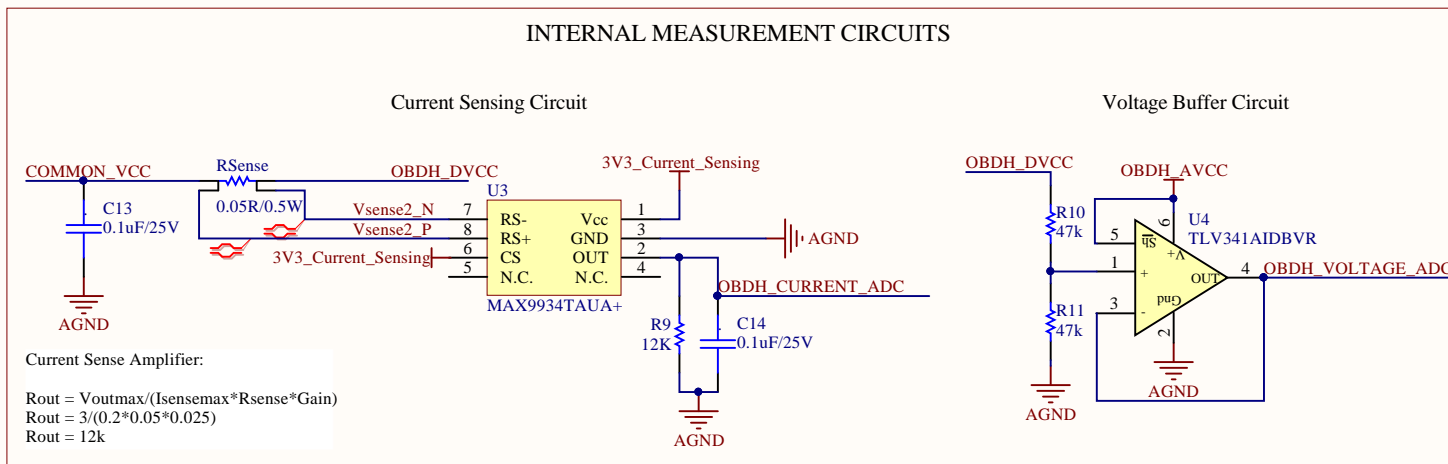
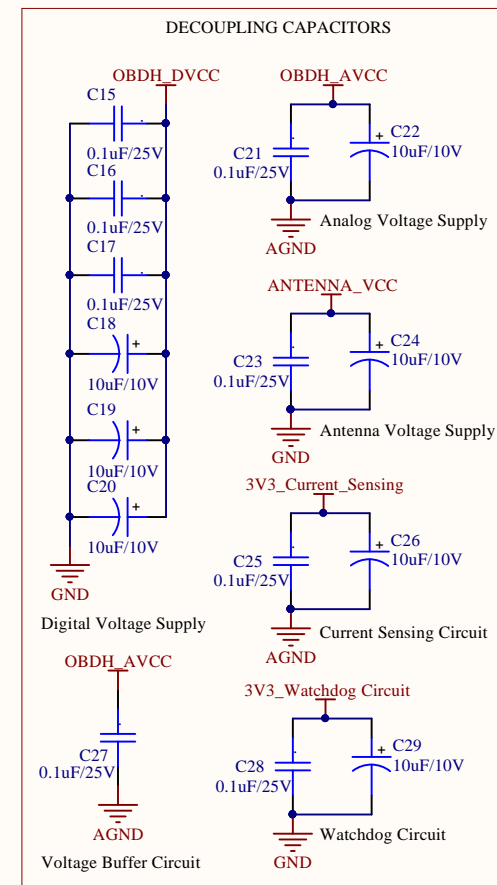
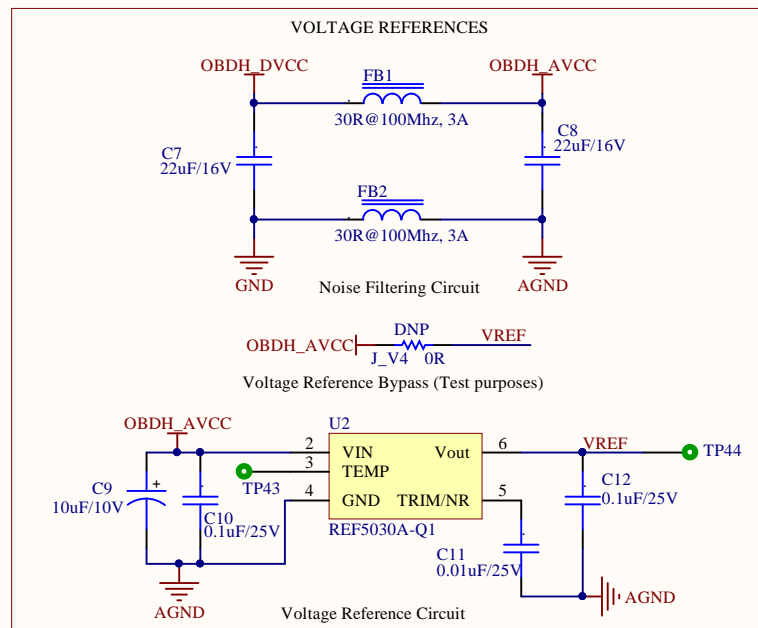
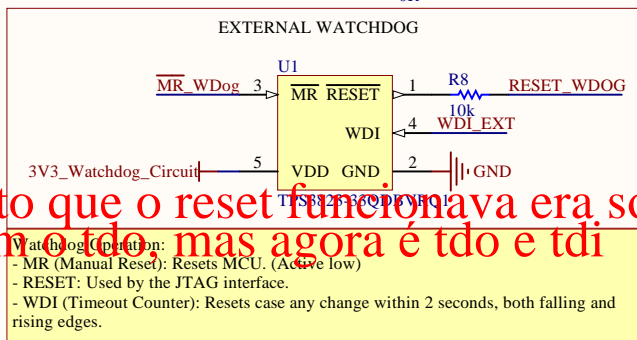
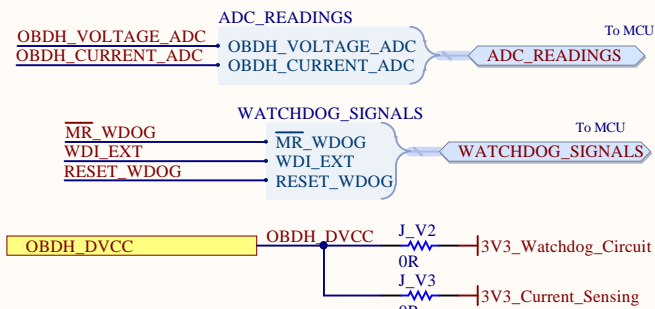
Ver se a memória precisa disso

SPI_MISO

33k

0.022uF/50V

GND



Title Current Sensing		
Size: A4	Project: FloripaSat	Revision: V0.0
Date: 28/12/2019	Time: 17:56:43	Sheet 7 of 7
Drawn By: André M. P. Mattos		Model: Eng 2 ALT

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