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FloripaSat-II OBDH 2.0 ALT Hardware
Based on the FloripaSat-I OBDH 2.0

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FloripaSat-II OBDH 2.0 ALT Hardware:

- Drawn by: André M. P. Mattos
- Reviewers: Cezar A. Rigo, Kleber Gouveia and Yan C. Azeredo
- Based on FloripaSat-I OBDH designed by: Sara V. Martinez
- Support: Gabriel M. Marcelino

Title **OBDH Hardware Architecture**

Size: **A4**

Project: **FloripaSat**

Revision: **V0.3**

Date: **4/30/2020**

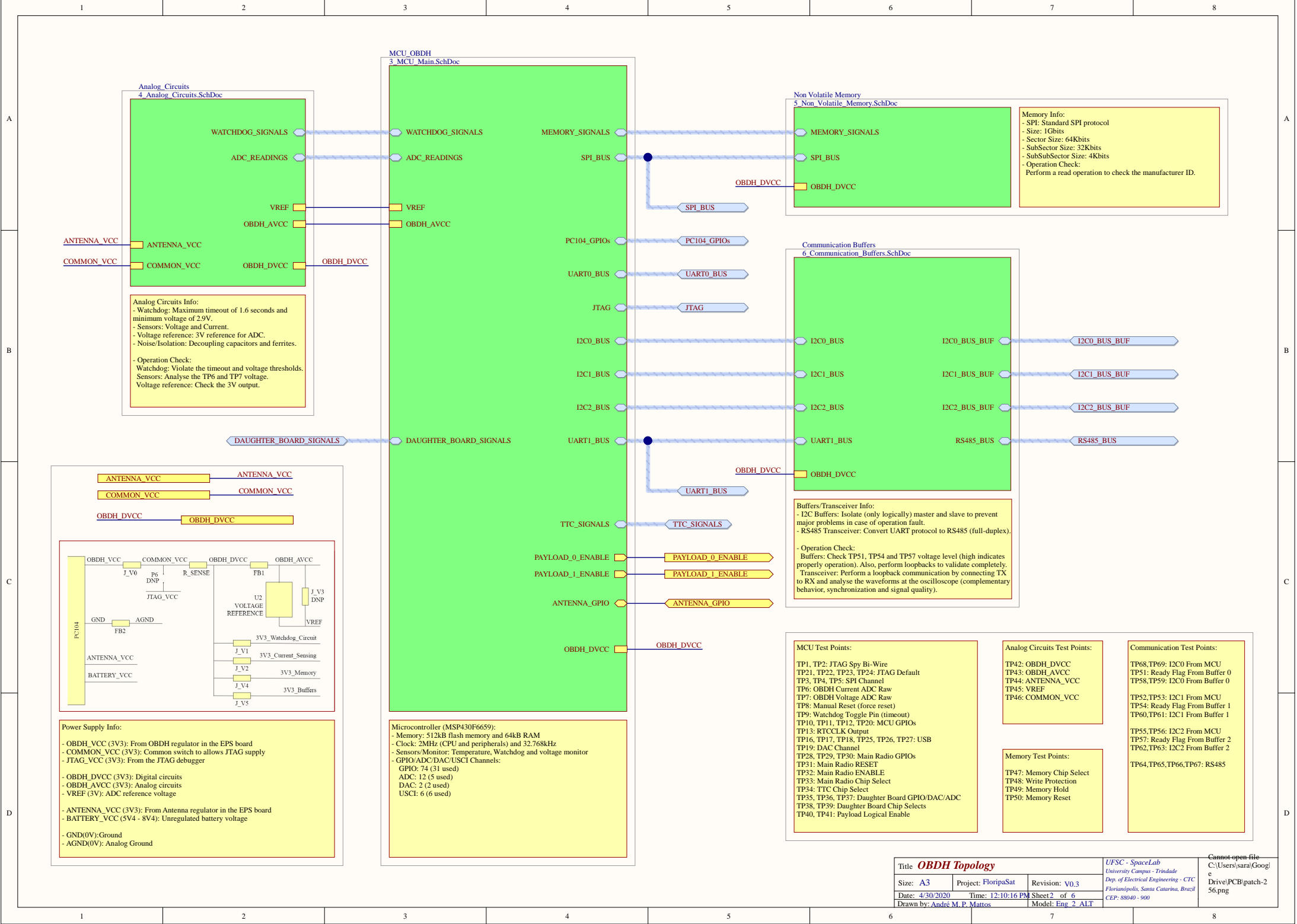
Time: **12:10:16 PM** Sheet **0** of **6**

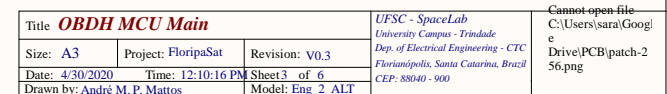
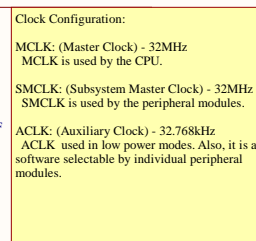
Drawn By: **André M. P. Mattos**

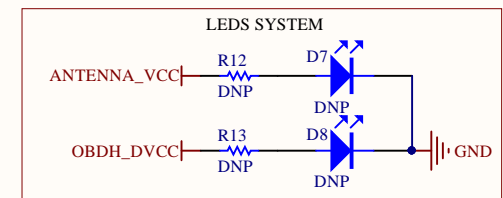
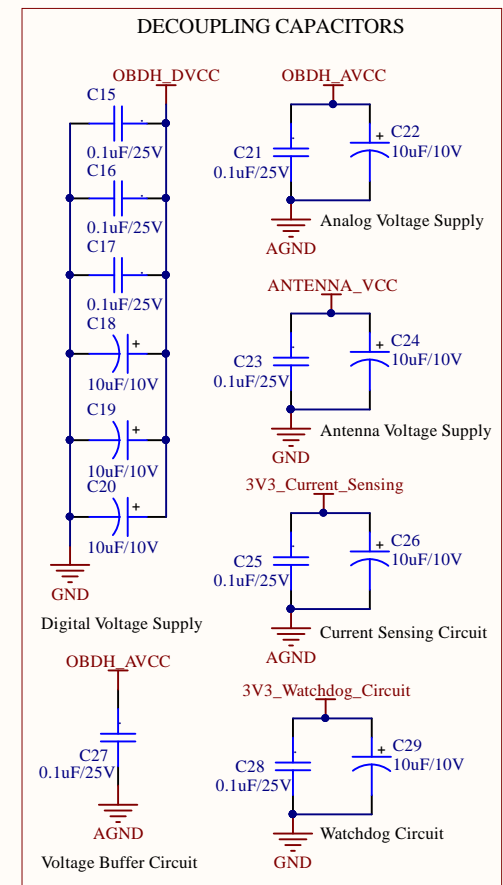
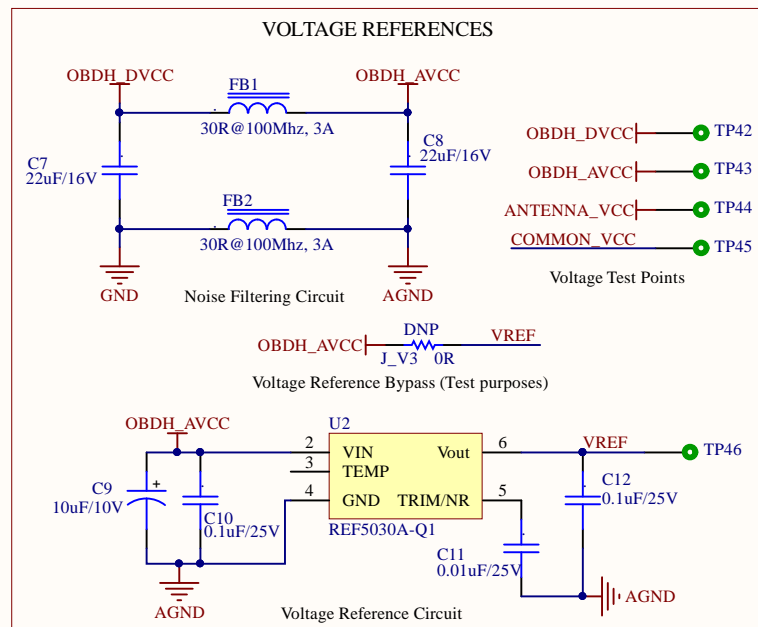
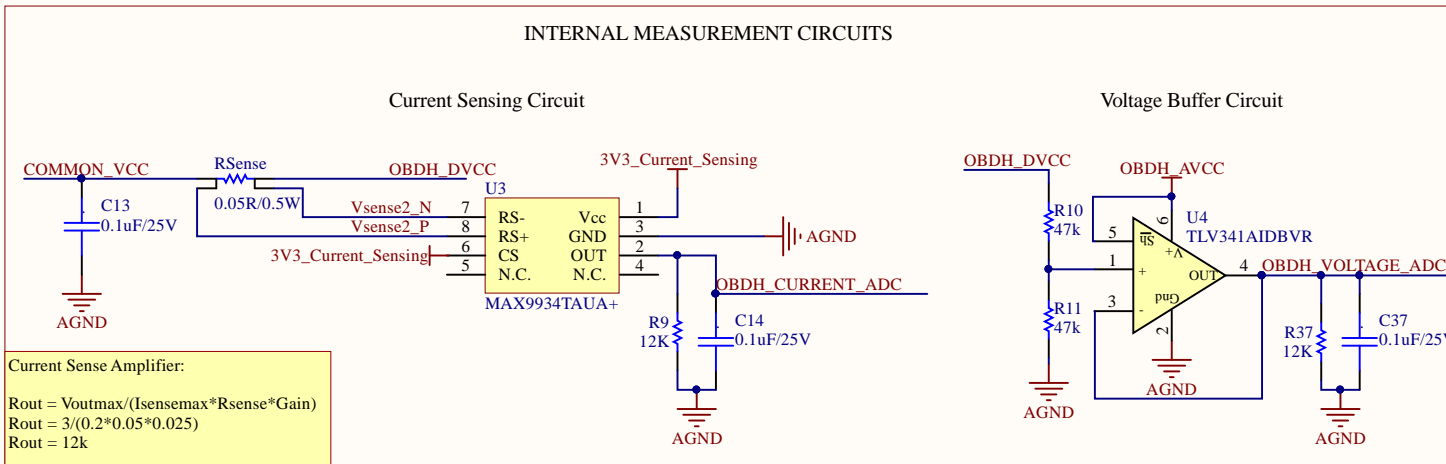
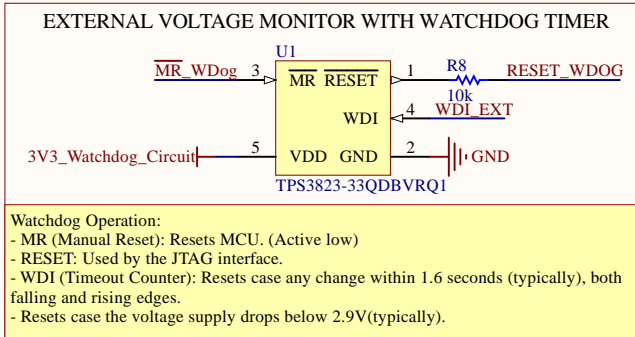
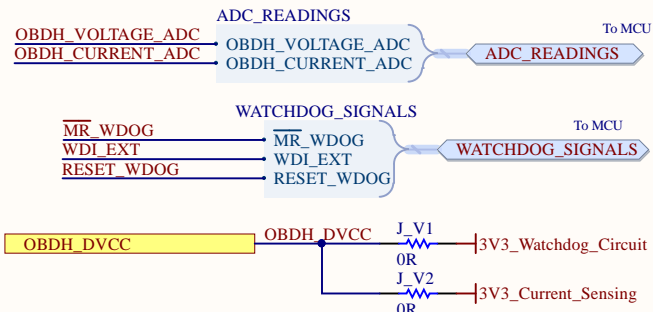
Model: **Eng_2_ALT**

UFSC - SpaceLab
University Campus - Trindade
Dep. of Electrical Engineering - CTC
Florianópolis, Santa Catarina, Brazil
CEP: 88040 - 900



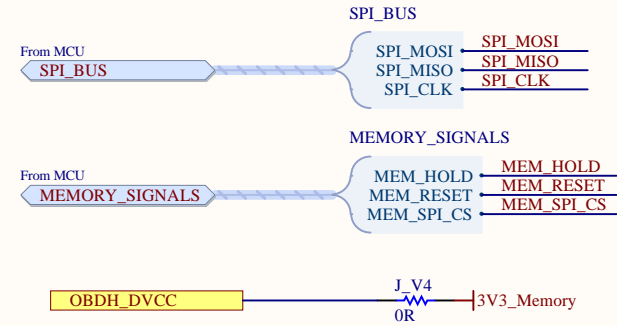
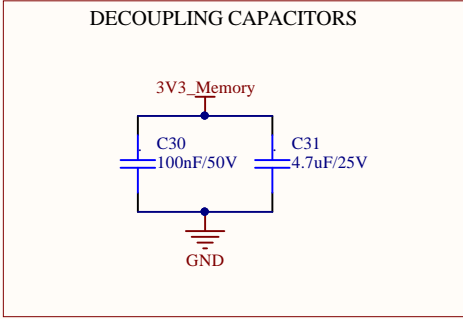
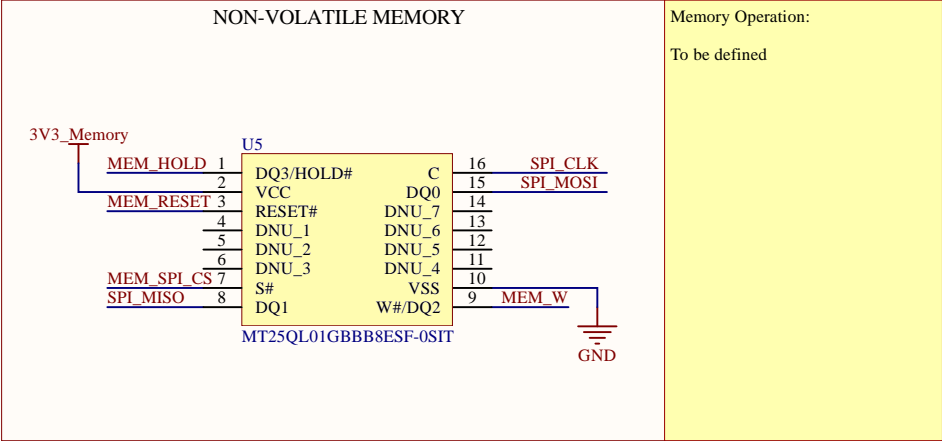
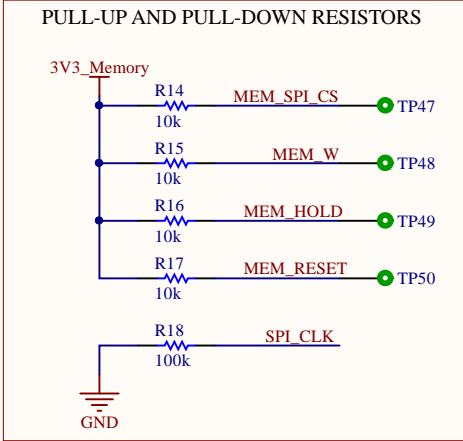




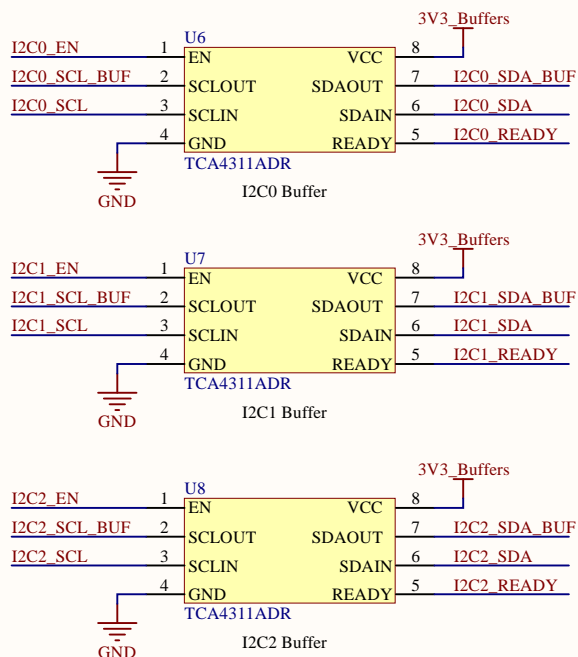
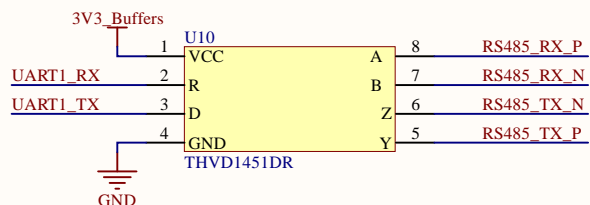


Title Analog Circuits			UFSC - SpaceLab
Size: A4			University Campus - Trindade
Project: FloripaSat			Dep. of Electrical Engineering - CTC
Revision: V0.3			Florianópolis, Santa Catarina, Brazil
Date: 4/30/2020	Time: 12:10:17 PM	Sheet 4 of 6	CEP: 88040 - 900
Drawn By: André M. P. Mattos			Model: Eng_2_ALT

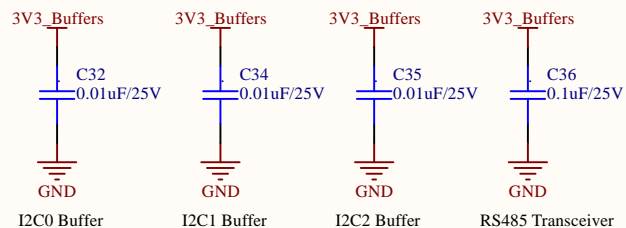




I2C BUFFERS

RS485 TRANSCEIVER
(Full-Duplex)

DECOUPLING CAPACITORS



UART1_BUS



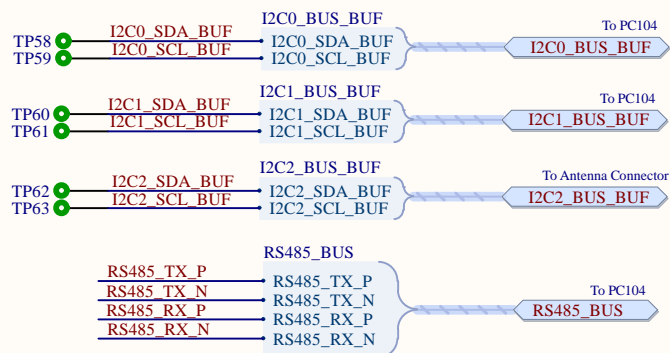
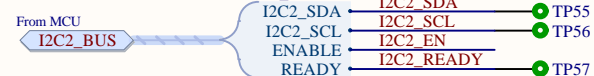
I2C0_BUS



I2C1_BUS



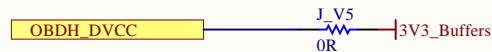
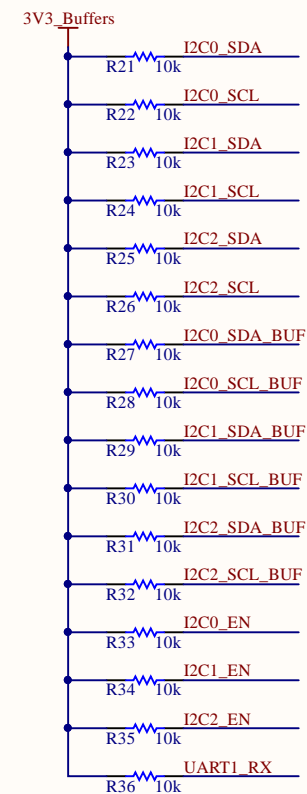
I2C2_BUS



TERMINATION RESISTORS



PULL-UP RESISTORS

Title **Communication Buffers**

Size: A4

Project: FloripaSat

Revision: V0.3

Date: 4/30/2020

Time: 12:10:17 PM

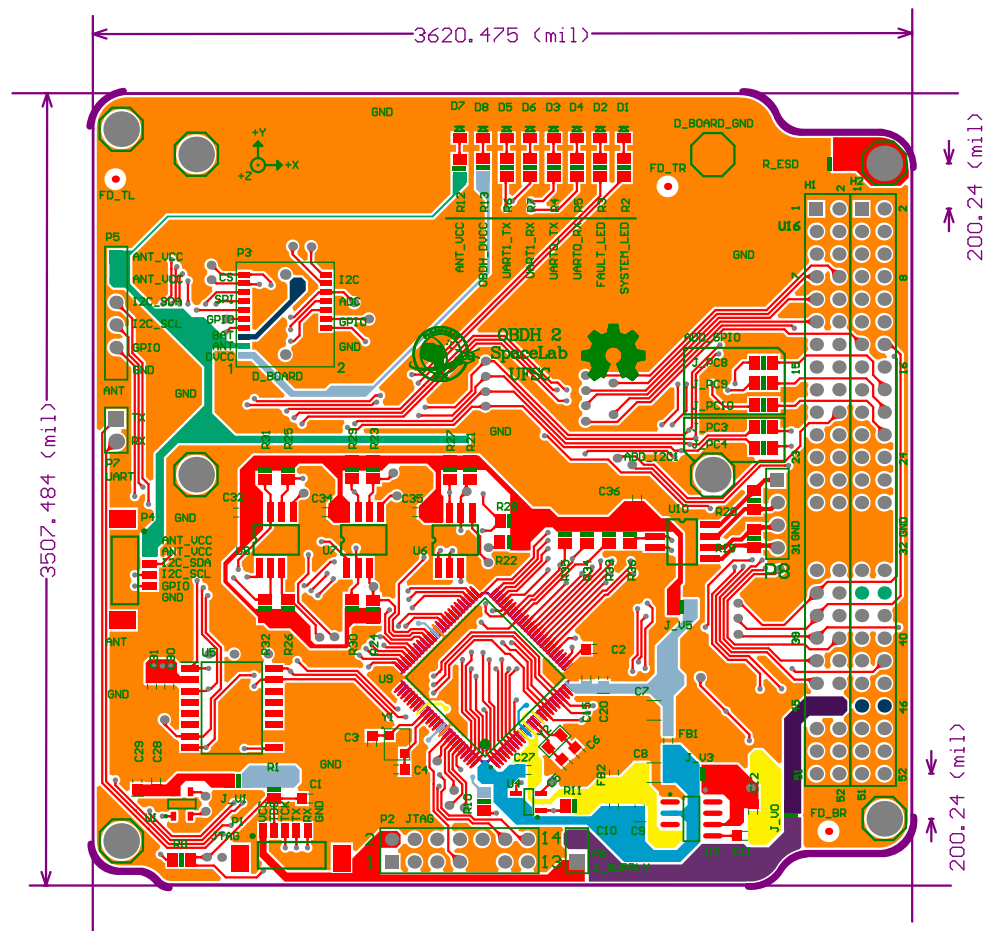
Sheet 6 of 6

Drawn By: André M. P. Mattos

Model: Eng_2_ALT

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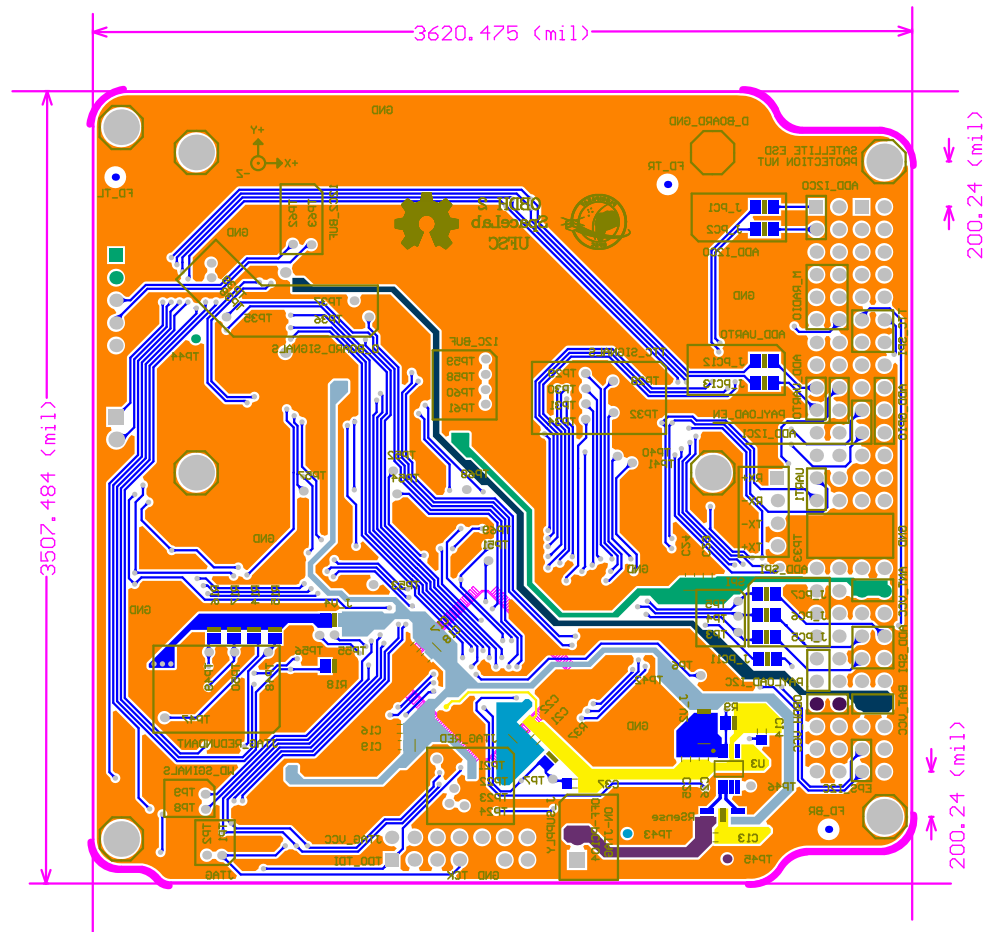




General SPEC:

Copper base 10Z:
 PCB Material: FR4
 PCB Thickness: 1.6mm
 PCB Surface: ENIG
 Vias: Force Complete Tenting

TITULO: OBDH 2018		REV.	VER.
UFSC		02	Eng_2_ALT
MATERIAL: FR4	Silkscreen color: green		
Board Thickness: 1.6mm	Layers: 02	Drawing	DATE
PCB Surface: ENIG		Andre M. P. Mattos	30/04/2020



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Copper base 10Z:
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UFSC		02	Eng_2_ALT
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