Imp topics



	Imp topics
01	Explain limit cycle. Define the termy zero input limit cycle,
	Overflow hast and the terry zero input limit cycle,
2	Rounding & Twocorting - Custon
3)	problem & detination
	WATER 13 DSP
5	Types of architecture in DSP
6	Super Harvard Architecture (SHARC)
7.)	DSP NS Micro Controller
8)	VLIW.
9)	What is multisate sampling. Give examples
10	explain method of multirate sampling
11)	teatures of DSP
12	· Applications of DSP (Radar)
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DSP

Dechum of discreate time signal
Assume upsampling by factor-2

q. Radon & Speaker Relogination.

of Deadband 2-6,101