ISNK_COARSE and ISNK_FINE Resistor Options Table

V3P3 RATIO	0/6	1/6	2/6	3/6	4/6	→5/6
ISNK_COARSE	0A	1A	2A	3A	4A	5A
ISNK_FINE	0mA	250mA	500mA	750mA	900mA	
PULLUP (R15, R13)	none	5k	5k	5k	5k	0*
PULLDOWN (R12, R6)	0*	1k	2.4k	5k	10k	none

* When pullup-pulldown combination is 0-none or none-0, use any resistor instead of 0-ohm

VBUS_MIN and VBUS_MAX Resistor Options Table

V3P3 RATIO	0/6	1/6	2/6	3/6	4/6	5/6	6/6
VBUS_MAX, VBUS_MIN	5V	9V	12V	15V	19V	20V	
PULLUP (R3, R11)	none	5k	5k	5k	5k	0*	
PULLDOWN (R6, R14)	0*	1k	2.4k	5k	10k	none	

PIXXEL SPACE
2JHJ+756, Swami Narayani Clinic Rd,
3rd Block, HBR Layout, Bengaluru,
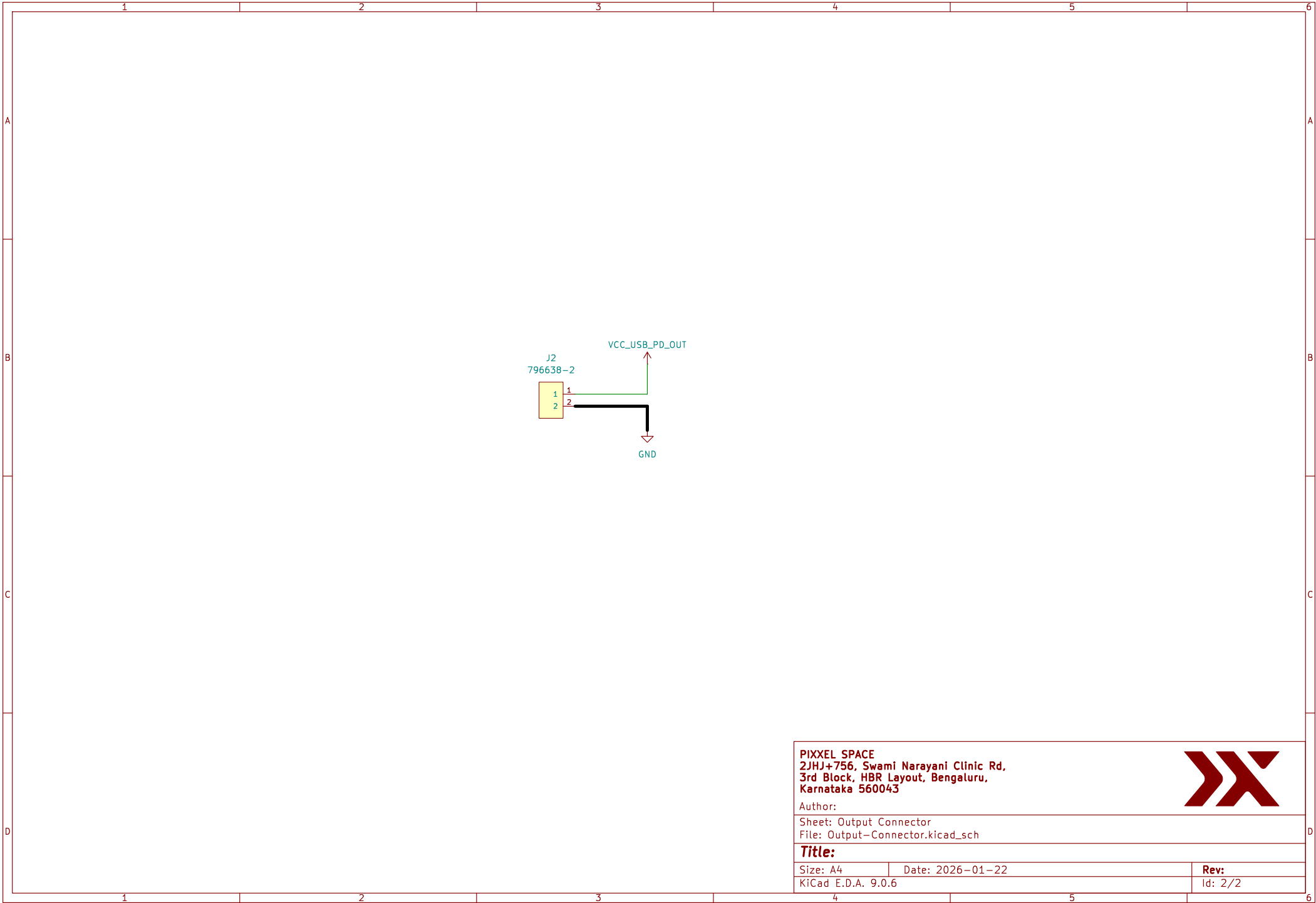Karnataka 560043
Author: Krishna Swaroop Dhulipalla

Sheet: Root
File: USB-PD-Trigger-Board.kicad_sch

Title: USB-PD Trigger Board

Size: A3	Date: 2026-01-22	Rev: E.1.0.0
KiCad E.D.A. 9.0.6		Id: 1/2

VCC_USB_PD_OUT

J2
796638-2

1 1
2 2

GND