INSTRUCTIONS TO RUN THE CODE:

1. Extract all the files from zip file with name ECE-571 TEAM15
2. Make sure the folder has all the files - 8088-1.svp, 8088if.svp, interface.sv, Memory_IO.sv, top.sv, busops.txt, dummy.txt, IO1.txt, IO2.txt, MEM1.txt, MEM2.txt, run.do, wave.do
3. Change the busops.txt if needed with valid addresses – Valid Address for MEMORY 1 – {20'h00000 - 20'h7FFFF}, MEMORY 2 – {20'h80000 - 20'hFFFFF}, IO1 - {16'hFF00 - 16'hFF0F}, IO2 – {16'h1C00 - 16'h1DFF}
4. Open QuestaSim in your machine
5. On the terminal window change the directory of working to the directory where all the above files reside including the text files. Command to change directory – **cd {Directory path}**
6. Execute the project using the following command – **do run.do - run.do** contains all the files needed for simulating the project.
7. Run.do file contains a default wave.do which when used in simulation gives the output of signals needed excluding all internal signals. If there is a need to generate all the internal signals comment the line do.wave do (#do wave.do) and remove comments on the line add wave sim:/top/* , add wave -r *- this will generate waveform which include all internals signals.