

## Gopalakrishnan, Krishnakumar

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**From:** Gregory Plett <[REDACTED]>  
**Sent:** 28 September 2018 20:28  
**To:** Gopalakrishnan, Krishnakumar  
**Subject:** Re: Request for permission to reuse a schematic from your lecture notes in my PhD thesis

Yes, you are welcome to use/adapt the figure. I'm sure I stole it from somewhere myself.

Congratulations for reaching this point!

Dr. Gregory L. Plett  
Professor, Department of Electrical and Computer Engineering  
Director, GATE Center of Excellence in Innovative Drivetrains in Electric Automotive Technology Education  
University of Colorado Colorado Springs  
1420 Austin Bluffs Parkway, Colorado Springs, CO 80918  
p: [REDACTED]; email: [REDACTED]

On Sep 28, 2018, at 10:13 AM, Gopalakrishnan, Krishnakumar <[REDACTED]@imperial.ac.uk> wrote:

Dear Prof Plett,

Greetings! How are you doing? I am about to submit the thesis of my thesis (hopefully early next week).

For use in my PhD thesis (non-profit, non-commercial CC-BY-NC-ND use), I'd like to adapt a schematic from Lecture 2 of ECE5540 notes that depicts a cartoon representation of the timing diagram of a real-time control/software loop. I shall of course cite the source of the original schematic in the caption.

I have attached the relevant schematic herewith for your perusal. I hope you can accord me with your permissions for thesis reuse.

Best Regards,  
Krishna

<timing\_diagram\_simplified.PNG>