

Technical Annex: Visual Sealer Tool (VST)

High-Resiliency Forensic Authentication Suite

Patent Pending (IPO): 202641004867

Forensic Verification & Silicon Anchor Report

February 13, 2026

Abstract

This document validates the Visual Sealer Tool (VST), supporting claims filed under Indian Patent Application No. 202641004867. The system demonstrates 100% recoulement across 423 images, designed in compliance with the Patents (Amendment) Rules, 2024 and the Jan Vishwas Act.

1 Executive Summary

The Visual Sealer Tool (VST) provides a high-resiliency forensic authentication suite for digital assets.

- **Objective:** Forensic authentication resilient to extreme data degradation.
- **Compliance:** Fully aligned with the Jan Vishwas (Amendment of Provisions) Act, 2023 and updated Patents Rules, 2024.
- **Performance:** Verified survivability against 90% data reduction (JPEG Quality 10).

2 System Specifications

- **Structural Redundancy:** Implements a 4×4 spatial lattice (16 redundant sectors) for “Survivor Logic” recoulement.
- **Expansion Protocol:** Utilizes amorphous binary signature conversion from high structural energy to low structural energy.
- **Silicon Anchor Telemetry:** Hardware-locked witness generation using GPS (tentative), MAC ID, and Unix Epoch witnesses.

3 Operational Evidence

3.1 3.1 Telemetry Log (Sample 2026-02-13)

Device ID: DESKTOP-28N4EQF

Geospatial Witness: xx.xxxx, yy.yyyy

Temporal Witness: 1770970470

Hardware Witness: 38:f3:ab:bc:13:2b

3.2 3.2 Stress Test Results

Table 1: Forensic verification audit for Application 202641004867.

Parameter	Value
Algorithm Input	elephant.png
Post-Attack Score	1.0 (100.00% Recoupment)
Status	PASS (Verification of S-Seed Integrity)

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