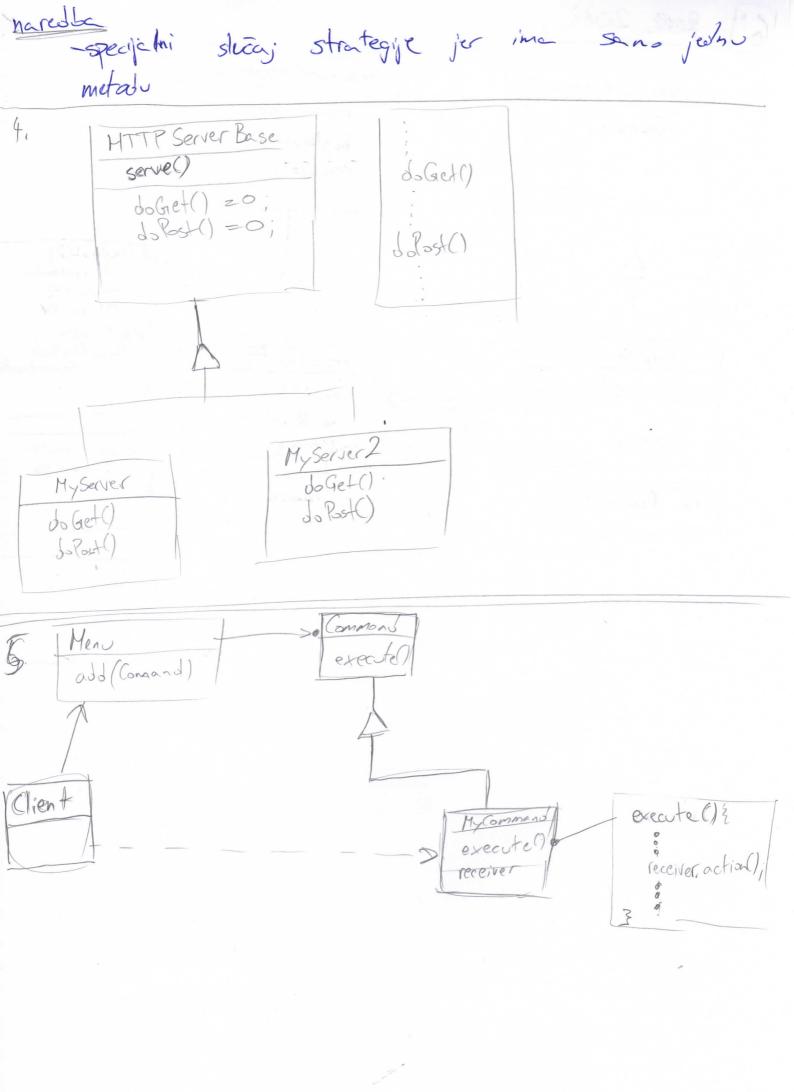
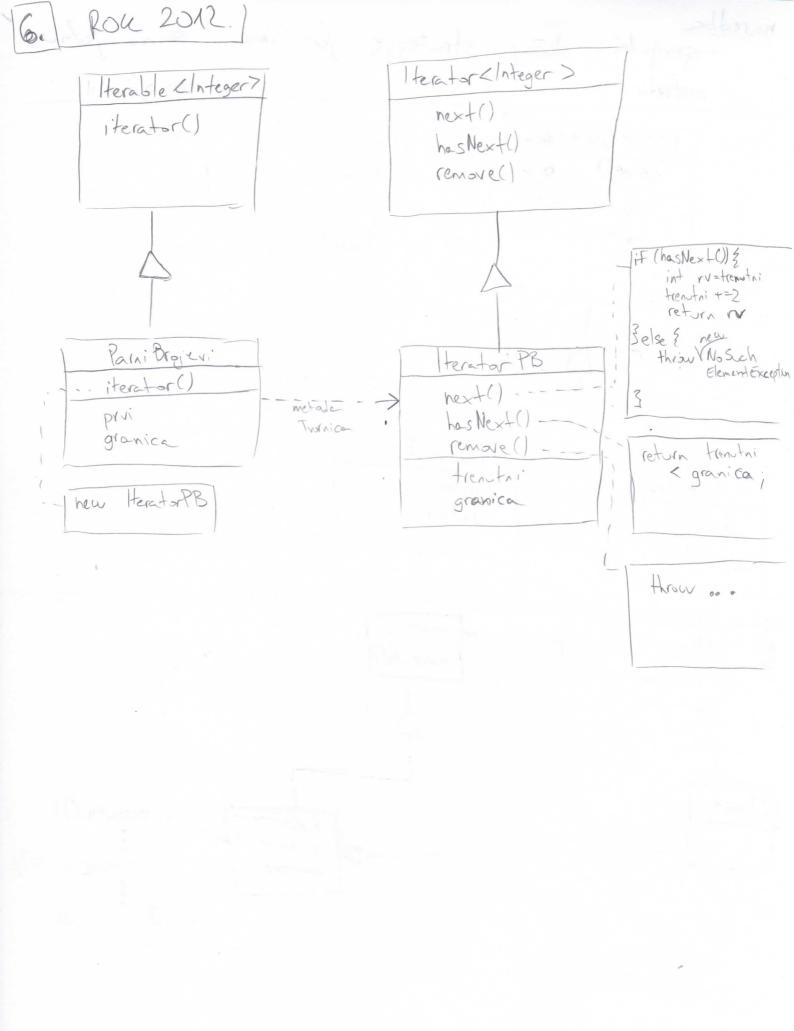


Component class Bundle : public Camponent & price() 20 std: vector « Component + > perts, clone()=0 virtual double rui. Price () { for (arts c: parts) MoBo return 1w Plice() Virtual Component done () { clone done() Boundle pl= new Boundles class NoBo & pullic Component () for (autof p: parts) return pl; po double price; String socheti price () { (etil pline) clove//2 MoBo mobo = non MoBo price, sochet) Problem sto get it Fibb ne vioi cached map z leturn mobo; class, Caches Seavence qualic: Scavence Fib(0) Seavence Sea. map Lint, int > caches: f:3/5(1) F; 56(2) fibl(3) if (+ = = cachel end()) & (Fache insert (maille per (n, rw)); 4:36(4) F:66(5) Seavence ? virtual int get (int n)= 0; Seavence get (n) 20 class Fibb & public Seavence ? return get(n-1) +get(n-2) Fibliset -souduéi de je overiscan Cache & map





1.MI 2013. 2. a) class Comparator & virtual bas compere (int, int) 20 Void paredaj (st.): vector (int) f. L, Comparator & cmp) } if (cmp. compale(L(j), L[min-j]) } b) det poledaj (L, cmp): if cmp(L[j], L[min-j]): c) template < typename T, typename Comparator> void paredaj (std::vector <TZF L, Compare for & cmp) { ORGANIZACIJA: prehisero nacelo supstitucie 3. (obt of hutaj Gare ()=0 Robot Robot Humansidni hvata Gole () =0 R202 Robotx R202 hizta Gures) huntaj Gord)

