.file "3zad.cpp"

.section .text.\_ZN9CoolClass3setEi,"axG",@progbits,\_ZN9CoolClass3setEi,comdat

.align 2

.weak \_ZN9CoolClass3setEi

.type \_ZN9CoolClass3setEi, @function

\_ZN9CoolClass3setEi:

.LFB0:

.cfi\_startproc

pushq %rbp

.cfi\_def\_cfa\_offset 16

.cfi\_offset 6, -16

movq %rsp, %rbp

.cfi\_def\_cfa\_register 6

movq %rdi, -8(%rbp)

movl %esi, -12(%rbp)

movq -8(%rbp), %rax

movl -12(%rbp), %edx

movl %edx, 8(%rax)

popq %rbp

.cfi\_def\_cfa 7, 8

ret

.cfi\_endproc

.LFE0:

.size \_ZN9CoolClass3setEi, .-\_ZN9CoolClass3setEi

.section .text.\_ZN9CoolClass3getEv,"axG",@progbits,\_ZN9CoolClass3getEv,comdat

.align 2

.weak \_ZN9CoolClass3getEv

.type \_ZN9CoolClass3getEv, @function

\_ZN9CoolClass3getEv:

.LFB1:

.cfi\_startproc

pushq %rbp

.cfi\_def\_cfa\_offset 16

.cfi\_offset 6, -16

movq %rsp, %rbp

.cfi\_def\_cfa\_register 6

movq %rdi, -8(%rbp)

movq -8(%rbp), %rax

movl 8(%rax), %eax

popq %rbp

.cfi\_def\_cfa 7, 8

ret

.cfi\_endproc

.LFE1:

.size \_ZN9CoolClass3getEv, .-\_ZN9CoolClass3getEv

.section .text.\_ZN13PlainOldClass3setEi,"axG",@progbits,\_ZN13PlainOldClass3setEi,comdat

.align 2

.weak \_ZN13PlainOldClass3setEi

.type \_ZN13PlainOldClass3setEi, @function

\_ZN13PlainOldClass3setEi:

.LFB2:

.cfi\_startproc

pushq %rbp

.cfi\_def\_cfa\_offset 16

.cfi\_offset 6, -16

movq %rsp, %rbp

.cfi\_def\_cfa\_register 6

movq %rdi, -8(%rbp)

movl %esi, -12(%rbp)

movq -8(%rbp), %rax

movl -12(%rbp), %edx

movl %edx, (%rax)

popq %rbp

.cfi\_def\_cfa 7, 8

ret

.cfi\_endproc

.LFE2:

.size \_ZN13PlainOldClass3setEi, .-\_ZN13PlainOldClass3setEi

.section .text.\_ZN4BaseC2Ev,"axG",@progbits,\_ZN4BaseC5Ev,comdat

.align 2

.weak \_ZN4BaseC2Ev

.type \_ZN4BaseC2Ev, @function

\_ZN4BaseC2Ev:

.LFB7:

.cfi\_startproc

pushq %rbp

.cfi\_def\_cfa\_offset 16

.cfi\_offset 6, -16

movq %rsp, %rbp

.cfi\_def\_cfa\_register 6

movq %rdi, -8(%rbp)

movq -8(%rbp), %rax

movq $\_ZTV4Base+16, (%rax)

popq %rbp

.cfi\_def\_cfa 7, 8

ret

.cfi\_endproc

.LFE7:

.size \_ZN4BaseC2Ev, .-\_ZN4BaseC2Ev

.section .text.\_ZN9CoolClassC2Ev,"axG",@progbits,\_ZN9CoolClassC5Ev,comdat

.align 2

.weak \_ZN9CoolClassC2Ev

.type \_ZN9CoolClassC2Ev, @function

\_ZN9CoolClassC2Ev:

.LFB9:

.cfi\_startproc

pushq %rbp

.cfi\_def\_cfa\_offset 16

.cfi\_offset 6, -16

movq %rsp, %rbp

.cfi\_def\_cfa\_register 6

subq $16, %rsp

movq %rdi, -8(%rbp)

movq -8(%rbp), %rax

movq %rax, %rdi

call \_ZN4BaseC2Ev

movq -8(%rbp), %rax

movq $\_ZTV9CoolClass+16, (%rax)

leave

.cfi\_def\_cfa 7, 8

ret

.cfi\_endproc

.LFE9:

.size \_ZN9CoolClassC2Ev, .-\_ZN9CoolClassC2Ev

.text

.globl main

.type main, @function

main:

.LFB4:

.cfi\_startproc

pushq %rbp

.cfi\_def\_cfa\_offset 16

.cfi\_offset 6, -16

movq %rsp, %rbp

.cfi\_def\_cfa\_register 6

pushq %rbx

subq $24, %rsp

movl $4, %edi

.cfi\_offset 3, -24

call \_Znwm

movq %rax, -32(%rbp)

movl $16, %edi

call \_Znwm

movq %rax, %rbx

movq %rbx, %rdi

call \_ZN9CoolClassC1Ev

movq %rbx, -24(%rbp)

movq -32(%rbp), %rax

movl $42, %esi

movq %rax, %rdi

call \_ZN13PlainOldClass3setEi

movq -24(%rbp), %rax

movq (%rax), %rax

movq (%rax), %rdx

movq -24(%rbp), %rax

movl $42, %esi

movq %rax, %rdi

call \*%rdx

movl $0, %eax

addq $24, %rsp

popq %rbx

popq %rbp

.cfi\_def\_cfa 7, 8

ret

.cfi\_endproc

.LFE4:

.size main, .-main

.weak \_ZTV9CoolClass

.section .rodata.\_ZTV9CoolClass,"aG",@progbits,\_ZTV9CoolClass,comdat

.align 32

.type \_ZTV9CoolClass, @object

.size \_ZTV9CoolClass, 32

\_ZTV9CoolClass:

.quad 0

.quad \_ZTI9CoolClass

.quad \_ZN9CoolClass3setEi

.quad \_ZN9CoolClass3getEv

.weak \_ZTV4Base

.section .rodata.\_ZTV4Base,"aG",@progbits,\_ZTV4Base,comdat

.align 32

.type \_ZTV4Base, @object

.size \_ZTV4Base, 32

\_ZTV4Base:

.quad 0

.quad \_ZTI4Base

.quad \_\_cxa\_pure\_virtual

.quad \_\_cxa\_pure\_virtual

.weak \_ZTS9CoolClass

.section .rodata.\_ZTS9CoolClass,"aG",@progbits,\_ZTS9CoolClass,comdat

.type \_ZTS9CoolClass, @object

.size \_ZTS9CoolClass, 11

\_ZTS9CoolClass:

.string "9CoolClass"

.weak \_ZTI9CoolClass

.section .rodata.\_ZTI9CoolClass,"aG",@progbits,\_ZTI9CoolClass,comdat

.align 16

.type \_ZTI9CoolClass, @object

.size \_ZTI9CoolClass, 24

\_ZTI9CoolClass:

.quad \_ZTVN10\_\_cxxabiv120\_\_si\_class\_type\_infoE+16

.quad \_ZTS9CoolClass

.quad \_ZTI4Base

.weak \_ZTS4Base

.section .rodata.\_ZTS4Base,"aG",@progbits,\_ZTS4Base,comdat

.type \_ZTS4Base, @object

.size \_ZTS4Base, 6

\_ZTS4Base:

.string "4Base"

.weak \_ZTI4Base

.section .rodata.\_ZTI4Base,"aG",@progbits,\_ZTI4Base,comdat

.align 16

.type \_ZTI4Base, @object

.size \_ZTI4Base, 16

\_ZTI4Base:

.quad \_ZTVN10\_\_cxxabiv117\_\_class\_type\_infoE+16

.quad \_ZTS4Base

.weak \_ZN4BaseC1Ev

.set \_ZN4BaseC1Ev,\_ZN4BaseC2Ev

.weak \_ZN9CoolClassC1Ev

.set \_ZN9CoolClassC1Ev,\_ZN9CoolClassC2Ev

.ident "GCC: (Ubuntu/Linaro 4.6.1-9ubuntu3) 4.6.1"

.section .note.GNU-stack,"",@progbits