.file "4zad.cpp"

.section .text.\_ZN4BaseC2Ev,"axG",@progbits,\_ZN4BaseC5Ev,comdat

.align 2

.weak \_ZN4BaseC2Ev

.type \_ZN4BaseC2Ev, @function

\_ZN4BaseC2Ev:

.LFB1:

.cfi\_startproc

pushq %rbp

.cfi\_def\_cfa\_offset 16

.cfi\_offset 6, -16

movq %rsp, %rbp

.cfi\_def\_cfa\_register 6

subq $16, %rsp

movq %rdi, -8(%rbp)

movq -8(%rbp), %rax

movq $\_ZTV4Base+16, (%rax)

movq -8(%rbp), %rax

movq %rax, %rdi

call \_ZN4Base6metodaEv

leave

.cfi\_def\_cfa 7, 8

ret

.cfi\_endproc

.LFE1:

.size \_ZN4BaseC2Ev, .-\_ZN4BaseC2Ev

.section .rodata

.LC0:

.string "ja sam bazna implementacija!"

.section .text.\_ZN4Base15virtualnaMetodaEv,"axG",@progbits,\_ZN4Base15virtualnaMetodaEv,comdat

.align 2

.weak \_ZN4Base15virtualnaMetodaEv

.type \_ZN4Base15virtualnaMetodaEv, @function

\_ZN4Base15virtualnaMetodaEv:

.LFB3:

.cfi\_startproc

pushq %rbp

.cfi\_def\_cfa\_offset 16

.cfi\_offset 6, -16

movq %rsp, %rbp

.cfi\_def\_cfa\_register 6

subq $16, %rsp

movq %rdi, -8(%rbp)

movl $.LC0, %edi

call puts

leave

.cfi\_def\_cfa 7, 8

ret

.cfi\_endproc

.LFE3:

.size \_ZN4Base15virtualnaMetodaEv, .-\_ZN4Base15virtualnaMetodaEv

.section .rodata

.LC1:

.string "Metoda kaze: "

.section .text.\_ZN4Base6metodaEv,"axG",@progbits,\_ZN4Base6metodaEv,comdat

.align 2

.weak \_ZN4Base6metodaEv

.type \_ZN4Base6metodaEv, @function

\_ZN4Base6metodaEv:

.LFB4:

.cfi\_startproc

pushq %rbp

.cfi\_def\_cfa\_offset 16

.cfi\_offset 6, -16

movq %rsp, %rbp

.cfi\_def\_cfa\_register 6

subq $16, %rsp

movq %rdi, -8(%rbp)

movl $.LC1, %edi

movl $0, %eax

call printf

movq -8(%rbp), %rax

movq (%rax), %rax

movq (%rax), %rdx

movq -8(%rbp), %rax

movq %rax, %rdi

call \*%rdx

leave

.cfi\_def\_cfa 7, 8

ret

.cfi\_endproc

.LFE4:

.size \_ZN4Base6metodaEv, .-\_ZN4Base6metodaEv

.section .text.\_ZN7DerivedC2Ev,"axG",@progbits,\_ZN7DerivedC5Ev,comdat

.align 2

.weak \_ZN7DerivedC2Ev

.type \_ZN7DerivedC2Ev, @function

\_ZN7DerivedC2Ev:

.LFB6:

.cfi\_startproc

pushq %rbp

.cfi\_def\_cfa\_offset 16

.cfi\_offset 6, -16

movq %rsp, %rbp

.cfi\_def\_cfa\_register 6

subq $16, %rsp

movq %rdi, -8(%rbp)

movq -8(%rbp), %rax

movq %rax, %rdi

call \_ZN4BaseC2Ev

movq -8(%rbp), %rax

movq $\_ZTV7Derived+16, (%rax)

movq -8(%rbp), %rax

movq %rax, %rdi

call \_ZN4Base6metodaEv

leave

.cfi\_def\_cfa 7, 8

ret

.cfi\_endproc

.LFE6:

.size \_ZN7DerivedC2Ev, .-\_ZN7DerivedC2Ev

.section .rodata

.align 8

.LC2:

.string "ja sam izvedena implementacija!"

.section .text.\_ZN7Derived15virtualnaMetodaEv,"axG",@progbits,\_ZN7Derived15virtualnaMetodaEv,comdat

.align 2

.weak \_ZN7Derived15virtualnaMetodaEv

.type \_ZN7Derived15virtualnaMetodaEv, @function

\_ZN7Derived15virtualnaMetodaEv:

.LFB8:

.cfi\_startproc

pushq %rbp

.cfi\_def\_cfa\_offset 16

.cfi\_offset 6, -16

movq %rsp, %rbp

.cfi\_def\_cfa\_register 6

subq $16, %rsp

movq %rdi, -8(%rbp)

movl $.LC2, %edi

call puts

leave

.cfi\_def\_cfa 7, 8

ret

.cfi\_endproc

.LFE8:

.size \_ZN7Derived15virtualnaMetodaEv, .-\_ZN7Derived15virtualnaMetodaEv

.text

.globl main

.type main, @function

main:

.LFB9:

.cfi\_startproc

.cfi\_personality 0x3,\_\_gxx\_personality\_v0

.cfi\_lsda 0x3,.LLSDA9

pushq %rbp

.cfi\_def\_cfa\_offset 16

.cfi\_offset 6, -16

movq %rsp, %rbp

.cfi\_def\_cfa\_register 6

pushq %r12

pushq %rbx

subq $16, %rsp

movl $8, %edi

.LEHB0:

.cfi\_offset 3, -32

.cfi\_offset 12, -24

call \_Znwm

.LEHE0:

movq %rax, %rbx

movq %rbx, %rdi

.LEHB1:

call \_ZN7DerivedC1Ev

.LEHE1:

movq %rbx, -24(%rbp)

movq -24(%rbp), %rax

movq %rax, %rdi

.LEHB2:

call \_ZN4Base6metodaEv

movl $0, %eax

addq $16, %rsp

popq %rbx

popq %r12

popq %rbp

.cfi\_remember\_state

.cfi\_def\_cfa 7, 8

ret

.L8:

.cfi\_restore\_state

movq %rax, %r12

movq %rbx, %rdi

call \_ZdlPv

movq %r12, %rax

movq %rax, %rdi

call \_Unwind\_Resume

.LEHE2:

.cfi\_endproc

.LFE9:

.globl \_\_gxx\_personality\_v0

.section .gcc\_except\_table,"a",@progbits

.LLSDA9:

.byte 0xff

.byte 0xff

.byte 0x1

.uleb128 .LLSDACSE9-.LLSDACSB9

.LLSDACSB9:

.uleb128 .LEHB0-.LFB9

.uleb128 .LEHE0-.LEHB0

.uleb128 0

.uleb128 0

.uleb128 .LEHB1-.LFB9

.uleb128 .LEHE1-.LEHB1

.uleb128 .L8-.LFB9

.uleb128 0

.uleb128 .LEHB2-.LFB9

.uleb128 .LEHE2-.LEHB2

.uleb128 0

.uleb128 0

.LLSDACSE9:

.text

.size main, .-main

.weak \_ZTV7Derived

.section .rodata.\_ZTV7Derived,"aG",@progbits,\_ZTV7Derived,comdat

.align 16

.type \_ZTV7Derived, @object

.size \_ZTV7Derived, 24

\_ZTV7Derived:

.quad 0

.quad \_ZTI7Derived

.quad \_ZN7Derived15virtualnaMetodaEv

.weak \_ZTV4Base

.section .rodata.\_ZTV4Base,"aG",@progbits,\_ZTV4Base,comdat

.align 16

.type \_ZTV4Base, @object

.size \_ZTV4Base, 24

\_ZTV4Base:

.quad 0

.quad \_ZTI4Base

.quad \_ZN4Base15virtualnaMetodaEv

.weak \_ZTS7Derived

.section .rodata.\_ZTS7Derived,"aG",@progbits,\_ZTS7Derived,comdat

.type \_ZTS7Derived, @object

.size \_ZTS7Derived, 9

\_ZTS7Derived:

.string "7Derived"

.weak \_ZTI7Derived

.section .rodata.\_ZTI7Derived,"aG",@progbits,\_ZTI7Derived,comdat

.align 16

.type \_ZTI7Derived, @object

.size \_ZTI7Derived, 24

\_ZTI7Derived:

.quad \_ZTVN10\_\_cxxabiv120\_\_si\_class\_type\_infoE+16

.quad \_ZTS7Derived

.quad \_ZTI4Base

.weak \_ZTS4Base

.section .rodata.\_ZTS4Base,"aG",@progbits,\_ZTS4Base,comdat

.type \_ZTS4Base, @object

.size \_ZTS4Base, 6

\_ZTS4Base:

.string "4Base"

.weak \_ZTI4Base

.section .rodata.\_ZTI4Base,"aG",@progbits,\_ZTI4Base,comdat

.align 16

.type \_ZTI4Base, @object

.size \_ZTI4Base, 16

\_ZTI4Base:

.quad \_ZTVN10\_\_cxxabiv117\_\_class\_type\_infoE+16

.quad \_ZTS4Base

.weak \_ZN4BaseC1Ev

.set \_ZN4BaseC1Ev,\_ZN4BaseC2Ev

.weak \_ZN7DerivedC1Ev

.set \_ZN7DerivedC1Ev,\_ZN7DerivedC2Ev

.ident "GCC: (Ubuntu/Linaro 4.6.1-9ubuntu3) 4.6.1"

.section .note.GNU-stack,"",@progbits