

The image displays a digital logic simulation interface. The top section lists various signals and their values:

- second_tick: 0
- count: 27'h0000000
- tb_input: 5'h00
- tb_output: 7'h7E
- tb_mclk: 0
- tb_reset: 0
- tb_c: 0
- tb_abcdefg: 7'h00
- tb_d0: 5'h12
- tb_d1: 5'h11
- datahei: 5'h11
- datahei2: 5'h12
- HALF_PERIOD: 5 ns
- my_rom: {10'h251} {10'h...

The main area shows a timing diagram with waveforms for these signals. The bottom section displays a hex dump of the data for 'my_rom'.

Address	Hex Data
0x00000000	251 293 214 210 2D5 277 210 2D8 219 210 37A 213 210 2DC 2B6 210