

IN3160 Lab 9

Task 1:

b) 17 bits

Task 2:

b) 32 bits

Task 3:

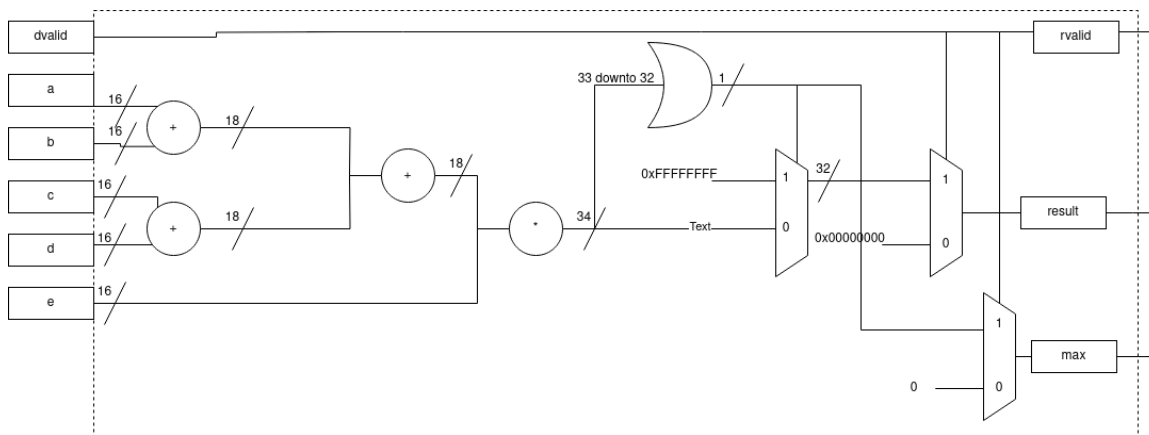
c) 18 bits

Task 4:

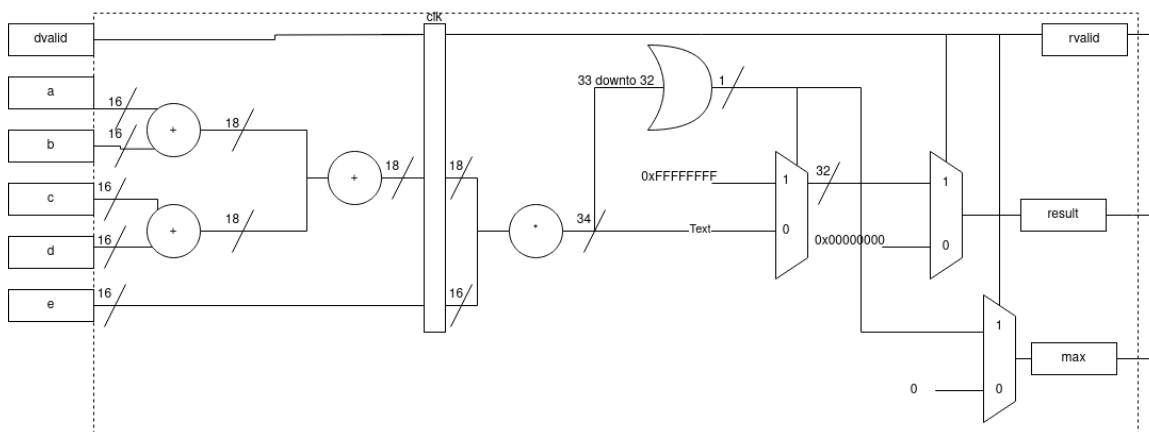
d) 34 bits

Task 5:

a)



b)



Task 6: se vedlagt VHDL-kode

Task 7: c) 34 flip flops

Task 8: c) 69 flip flops