tretton37

8 bit assembly in 20 minutes

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hello @simon.oskarsson!

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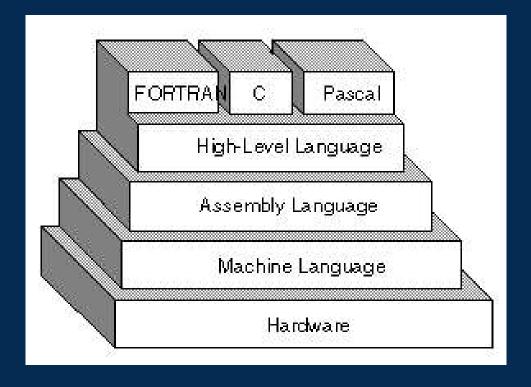
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_assembly language

- A low-level programming language
- Specific to a particular computer architecture
- Converted into executable machine code
- Uses a mnemonic to represent each lowlevel machine instruction or opcode

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ARM, MIPS, x86, Z80, 68000, 6502 6510

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_commodore 64?

- 8 bit computer
- Introduced in 1982
- Highest-selling computer model ever



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_what did it look like?

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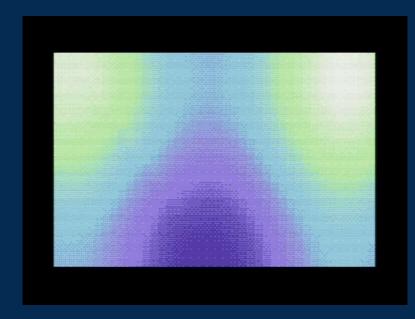


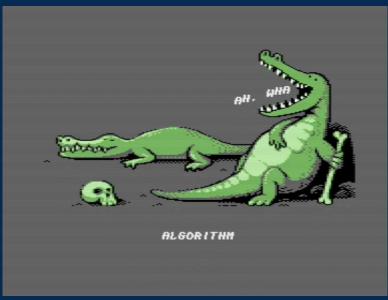




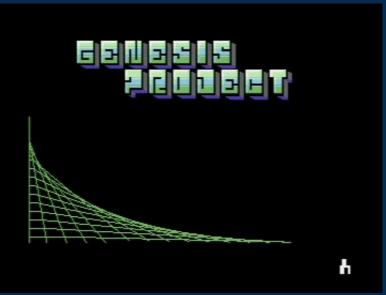


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_memory-mapped i/o

- Each memory location corresponds to a feature
- Access features by read/write to memory addresses
- 64 kB RAM (\$0000 \$ffff)

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_memory-mapped i/o

```
$0400 Char at y=1,x=1

$0401 Char at y=1,x=2

$0428 Char at y=2,x=1

... ...

$d020 Border color ($00-$0f)
```

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the task

Output Hello, world! to screen

solution

- Screen coord = one memory location
- Write data to memory locations

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instructions

- Three characters codes, e.g LDA, JMP
- Approx. 60 instructions
- Store data in memory and registers
- Increment/decrement
- Add/subtract
- Shift bits
- Jump and branch
- Compare

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numeral system

One byte:

```
Decimal 0 - 255

Hexadecimal $00 - $ff

Binary $0000000 - $1111111
```

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registers

A (Accumulator)	Arithmetic and logic
X and Y	General purpose
S	Stack pointer
Р	Processor status

Each register can hold one byte

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store data

STA \$d020	Store A in \$d020
LDA #\$0f	Load A with \$0f
STX \$d020	Store X in \$d020
LDX #\$0f	Load X with \$0f
STY \$d020	Store Y in \$d020
LDY #\$0f	Load Y with \$0f

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increment/decrement

INC \$d020	Increase value in \$d020 by 1
DEC \$d020	Decrease value in \$d020 by 1
INX	Increase value in X by 1
DEX	Decrease value in X by 1
INY	Increase value in Y by 1
DEY	Decrease value in Y by 1

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addressing modes

```
LDA #$0f Immediate addressing

LDA $0f Absolute addressing
```

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indexed absolute addressing

Clear the screen

Screen is 40x25 chars (\$0400-\$07FF)

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add and subtract

ADC Add with carry

SBC Subtract with carry

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bit shifting

Shift bits to the left = multiply by two Shift bits to the right = divide by two

ASL Bit shift left

LSR Bit shift right

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bit masking

Set bits to 1

Set bits to 0

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branching

CMP Compare memory and A

BEQ / BNE Branch if equal / not equal

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_rendering text

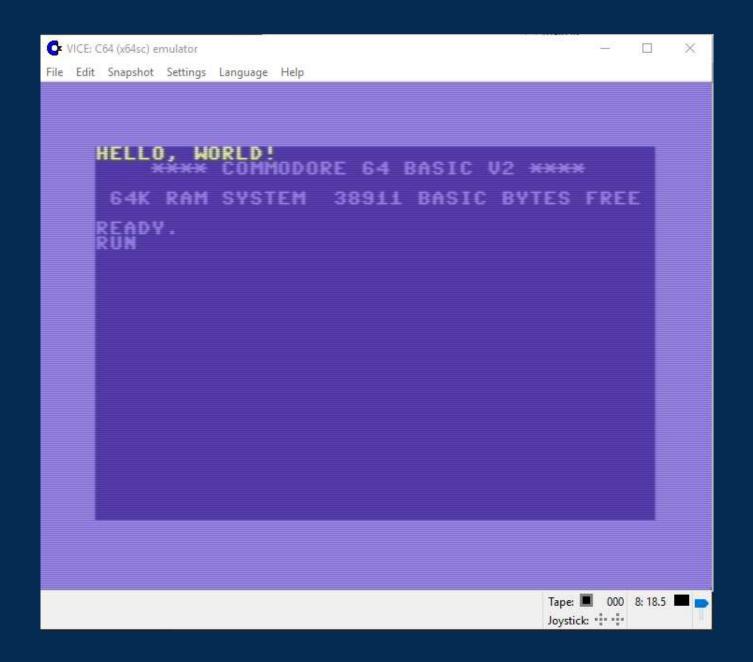
```
$0400 - $07e8 Screen memory
```

\$d800 - \$dbe7 Color memory

```
ldx #$00
loop:
   lda message, x
   sta $0400,x  // Screen memory is at $0400-$07ff
   sta $d800,x // Color memory is at $d800-$dbff
   inx
   cpx #13
   bne loop
                // Infinite loop
   †mp *
message:
   .text "hello, world!"
```

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