ENCODER WITH PRIORITY

```
library IEEE;
use IEEE.STD LOGIC 1164.all;
entity kris ewp is
         port (
              a: in STD LOGIC VECTOR(3 downto 0);
              b: out STD LOGIC VECTOR(1 downto 0)
end kris ewp;
architecture bhy of kris ewp is
begin
process(a)
begin
         if (a = (not "1000")) then
            b <= "00";
         elseif (a = (not "0100")) then
            b \le "01";
                                                                           图 66 元 深深 图 67 名 8 图 2 图 2 1 library IEEE; use IEEE.STD_LOGIC_1164.all;
         elseif (a = (not "0010")) then
            b <= "10";
                                                                             Eentity kris_ewp is

b port(
    a : in STD_LOGIC_VECTOR(3 downto 0);
    b : out STD_LOGIC_VECTOR(1 downto 0)
         elseif (a = (not "0001")) then
            b <= "11";
                                                                             else
            b <= "ZZ";
         end if;
end process;
end bhv;
                                                         All O A A V <<Filte
                                                            ID Message
293000 Quartus Prime Full Compilation was successful. 0 errors, 13 warning
oort - kris_ewp 🗶 🤍 🏋 L Viewer 🗶
wp:1 +
                                                                         b[0]~1
                              Equal1
         a[3..0]
                                                   b[0]~2
                                                                         b[1]~0
                                                   Equal0
```