STEPPER MOTOR INTERFACE

```
library IEEE;
use IEEE.STD LOGIC 1164.ALL;
use IEEE.NUMERIC STD.ALL;
entity STEPPER MOTOR INTERFACE is
Port (
        : in STD LOGIC;
  clk
  reset n: in STD LOGIC;
  enable n:in STD LOGIC;
  dir n : in STD LOGIC;
  coil n : out STD LOGIC VECTOR(3 downto 0)
);
end STEPPER MOTOR INTERFACE;
architecture Behavioral of STEPPER MOTOR INTERFACE is
  signal step counter : unsigned(1 downto 0) := "00";
                  : unsigned(20 downto 0) := (others => '0');
  signal clk div
  signal step_clk : STD LOGIC := '0';
  signal enabled
                  : STD LOGIC := '0';
  signal direction : STD LOGIC := '0';
  type step sequence is array (0 to 3) of std logic vector(3 downto 0);
  constant full step : step_sequence := (
    "1100",
    "0110".
    "0011",
    "1001"
  );
begin
  process(clk)
  begin
    if rising edge(clk) then
      clk div \le clk div + 1;
      step clk \le clk \ div(20);
    end if;
  end process;
 process(step clk, reset n)
  begin
    if reset n = 0 then
      step counter <= "00";
```

```
coil_n <= "1111";
     elsif rising_edge(step_clk) then
       if enabled = '1' then
          if direction = '1' then
             step_counter <= step_counter + 1;</pre>
          else
             step_counter <= step_counter - 1;</pre>
          end if;
          coil n <= full step(to integer(step counter));</pre>
        else
          coil n <= "1111";
       end if;
     end if;
  end process;
  enabled <= not enable n;</pre>
  direction <= not dir_n;
end Behavioral;
```

