

U19CS076 MIT ASSIGNMENT 6

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(1) The following block of data is stored in memory locations from 3055H to 305AH. Write a program to transfer the block of data in reverse order at same memory location.

DATA (HEX): 22, A5, B2, 99, 7F, 37

```
lxi b,3055h
lxi d,305Ah
loop: ldax b
mov m,a
ldax d
stax b
mov a,m
stax d
inx b
dcx d
mov a,e
cmp c
jnc loop
hlt
```

;BEFORE EXECUTION

Data Stack KeyPad Memory I/O Ports			
Start	3055h		OK
Address (Hex)	Address	Data	
3055	12373	9	
3056	12374	31	
3057	12375	23	
3058	12376	2	
3059	12377	3	
305A	12378	5	
305B	12379	0	
305C	12380	0	
305D	12381	0	

AFTER EXECUTION

Registers			Flag	
<i>A</i>	57		<i>S</i>	1
<i>BC</i>	30	58	<i>Z</i>	0
<i>DE</i>	30	57	<i>AC</i>	0
<i>HL</i>	00	00	<i>P</i>	1
<i>PSW</i>	00	00	<i>C</i>	1
<i>PC</i>	42	14		
<i>SP</i>	FF	FF		
<i>Int-Reg</i>	00			

Data Stack Keypad <u>Memory</u> I/O Ports			
Start	3055h		OK
Address (Hex)	Address	Data	
3055	12373	5	
3056	12374	3	
3057	12375	2	
3058	12376	23	
3059	12377	31	
305A	12378	9	
305B	12379	0	

(2) Find the square of the given numbers from memory location 6100H and store the result from memory location 7000H.

LXI H,6100H ;source address

LXI D,7000H ;destination address

MVI C,0AH ;for 10 counts

loop: MVI A,00H

MOV B,M

lp2: ADD M

DCR B

JNZ lp2

STAX D

INX H

INX D

DCR C

JNZ loop

HLT

Registers			Flag	
<i>A</i>	40		<i>S</i>	0
<i>BC</i>	00	00	<i>Z</i>	1
<i>DE</i>	70	0A	<i>AC</i>	0
<i>HL</i>	61	0A	<i>P</i>	1
<i>PSW</i>	00	00	<i>C</i>	0
<i>PC</i>	42	18		
<i>SP</i>	FF	FF		
<i>Int-Reg</i>	00			

(3) WAP to find Factorial of a given number using Call and Subroutine.

LDA 3000H

CPI 2H

JC SKIP

MOV E,A

MVI D,00H

DCR A

CALL Factorial

SHLD 3001H

JMP END

SKIP: LXI H,0001H

SHLD 2001H

END: HLT

Factorial: LXI H,0000H

MOV C,A

loop: DAD D

DCR C

JNZ loop

XCHG

DCR A

CNZ Factorial

RET

BEFORE EXECUTION

Address (Hex)	Address	Data
3000	12288	5

Input at-3000H

Output at -3001H

AFTER EXECUTION

Data	Stack	KeyPad	Memory	I/O Ports
Start 3000h OK				
Address (Hex)	Address	Data		
3000	12288	5		
3001	12289	120		

Registers			Flag	
A		00	S	0
BC	00	00		
DE	00	78	Z	1
HL	00	78		
PSW	00	00	AC	0
PC	42	1C	P	1
SP	FF	FF		
Int-Reg		00	C	0

(4) WAP for Fibonacci Series using Call and Subroutine.

LXI H,3000H

MVI C,0AH ;length of series(till 10 places)

DCR C

MVI B,00H

MVI D,01H

MOV M,B

INX H

CALL FIB

HLT

FIB: MOV M,D

loop: MOV A,B

ADD D

MOV B,D

MOV D,A

INX H

MOV M,A

DCR C

JNZ loop

RET


```
LDA 3000H
MOV B,A
LDA 3001H
MOV D,A
CALL MULT
STA 3002H
MOV A,C
STA 3003H
HLT
MULT: MVI C,00H
MVI A,00H
LOOP: ADD D
JNC Ip2
INR C
Ip2: DCR B
JNZ LOOP
RET
```

BEFORE EXECUTION

Start	3000h	OK
Address (Hex)	Address	Data
3000	12288	6
3001	12289	3

AFTER EXECUTION

Start	3000h		OK
Address (Hex)	Address	Data	
3000	12288	6	
3001	12289	3	
3002	12290	18	

Registers			Flag
<i>A</i>		00	<i>S</i> 0
<i>BC</i>	00	00	
<i>DE</i>	03	00	<i>Z</i> 1
<i>HL</i>	00	00	
<i>PSW</i>	00	00	<i>AC</i> 0
<i>PC</i>	42	13	<i>P</i> 1
<i>SP</i>	FF	FF	
<i>Int-Reg</i>		00	<i>C</i> 0