

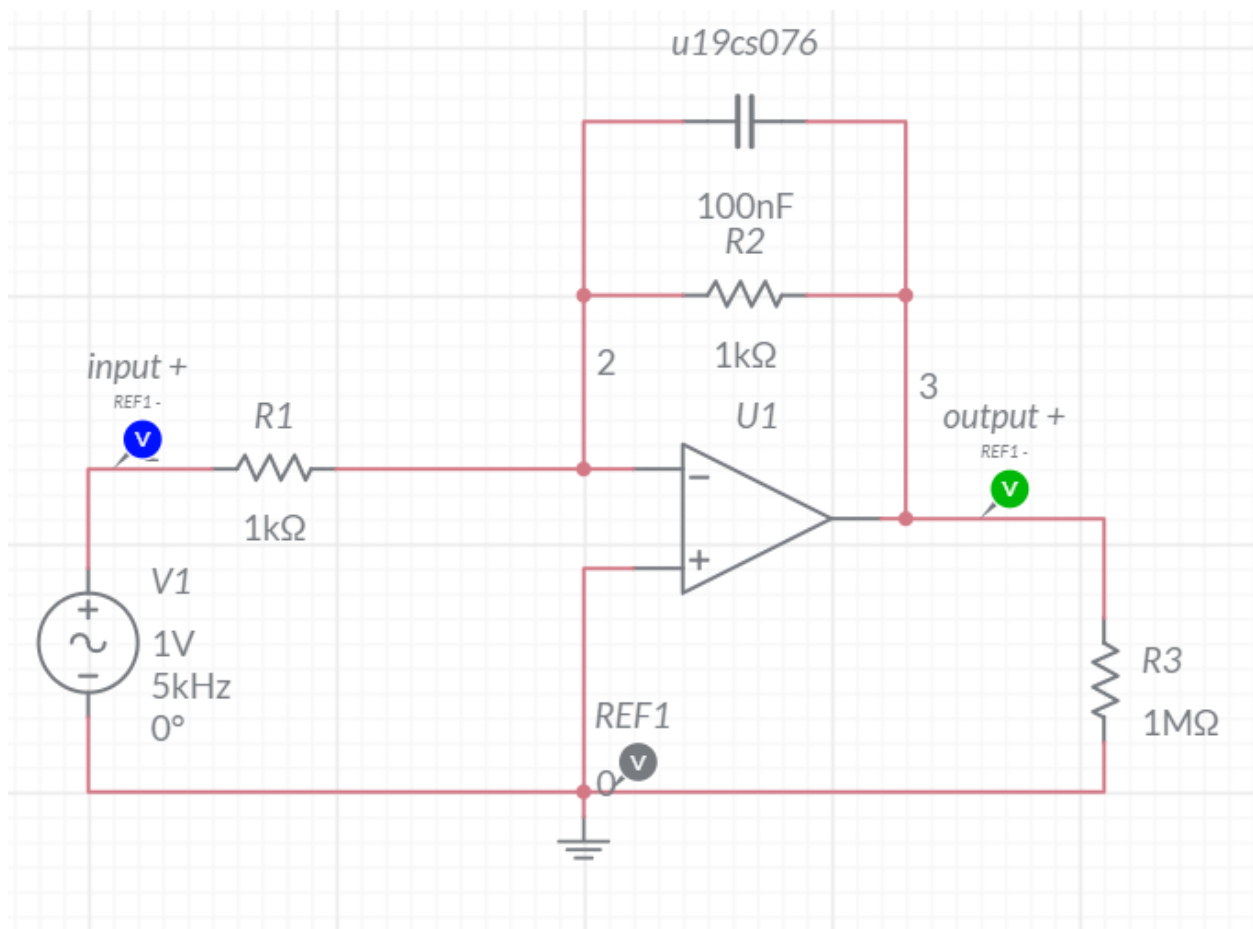


DIGITAL ELECTRONICS AND LOGIC DESIGN [EC - 207]
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SURAT

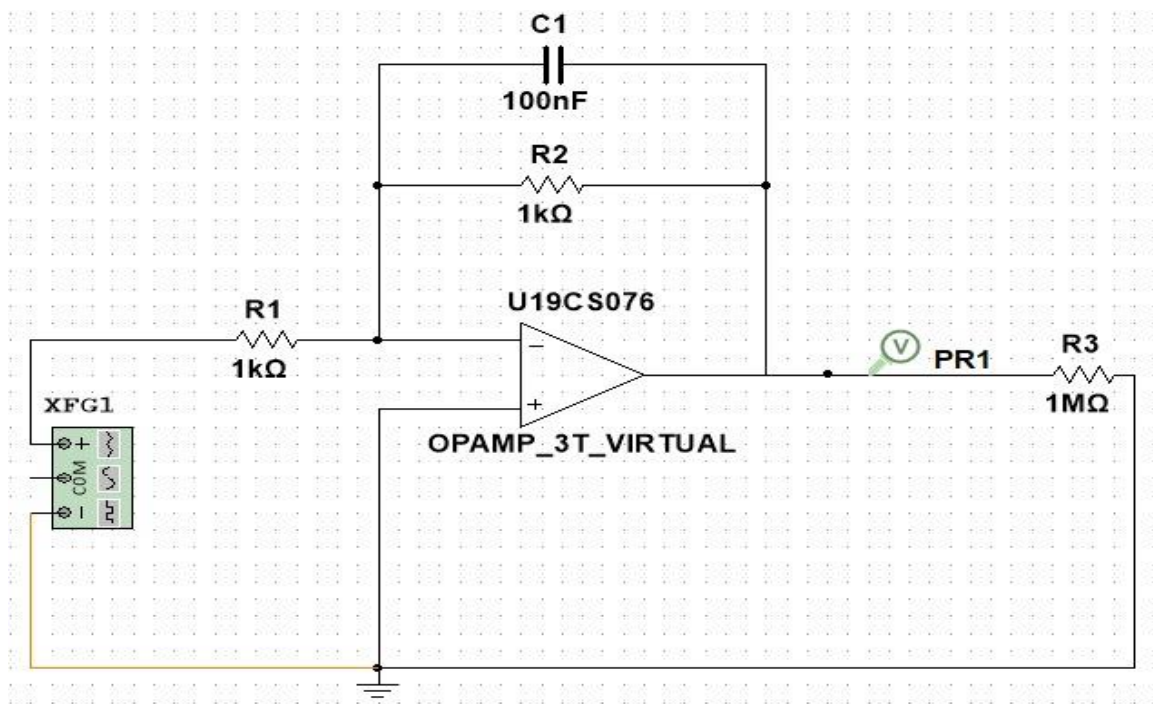
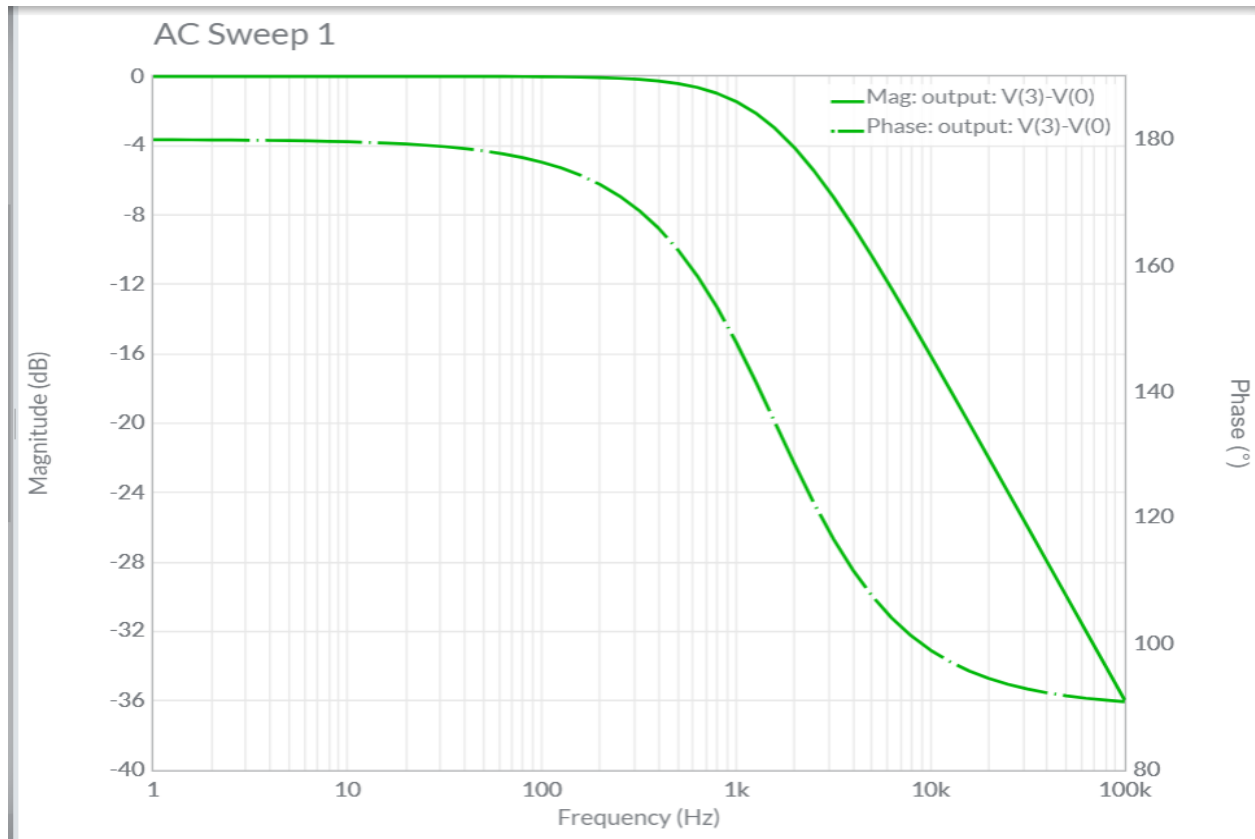
ELECTRONICS ENGINEERING DEPARTMENT

U19CS076 ASSIGNMENT FOR PRACTICAL-13

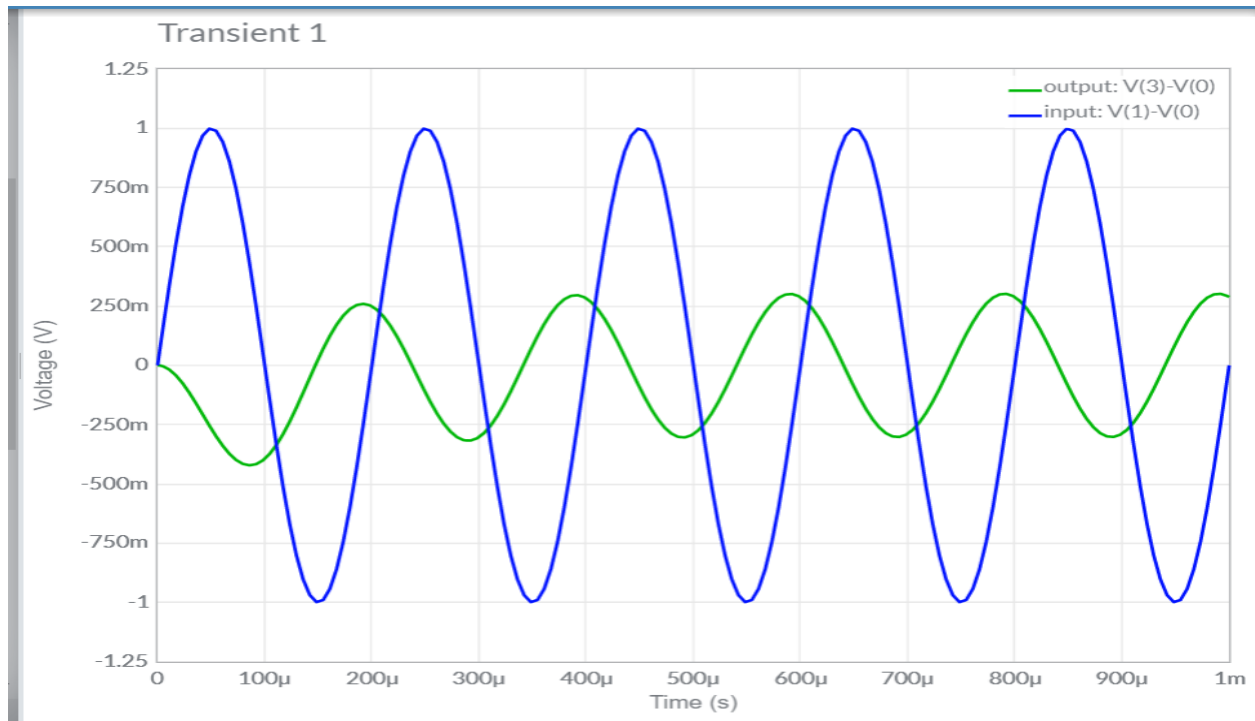
Q. Simulate Active Low Pass Filter Circuit Using OPAMP.



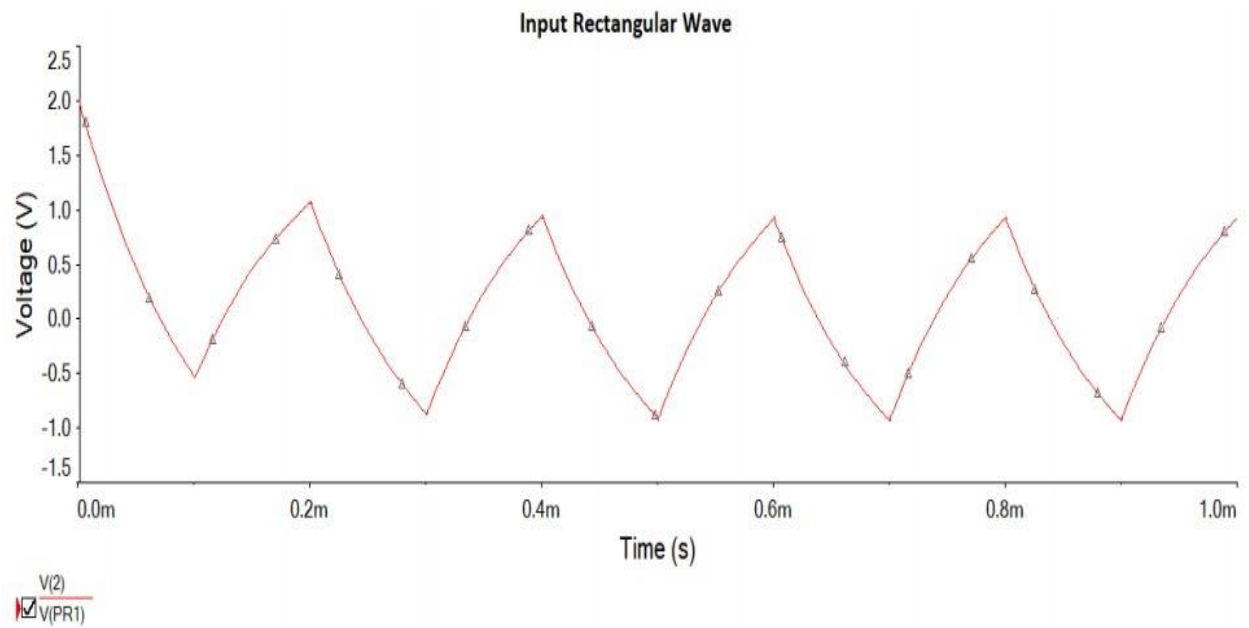
AC SWEEP GRAPH



FOR INPUT SINE WAVE



FOR RECTANGULAR INPUT



FOR TRIANGULAR INPUT

