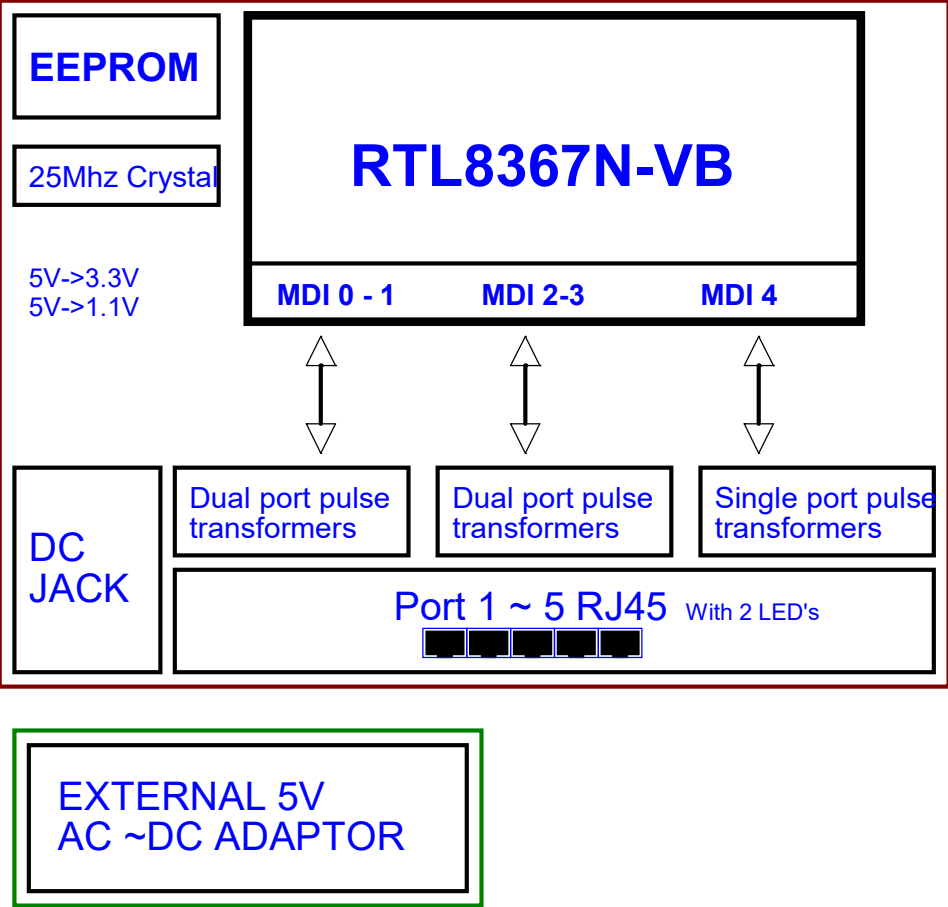


RTL8367N-VB (QFN88) Block Diagram



REVISION HISTORY

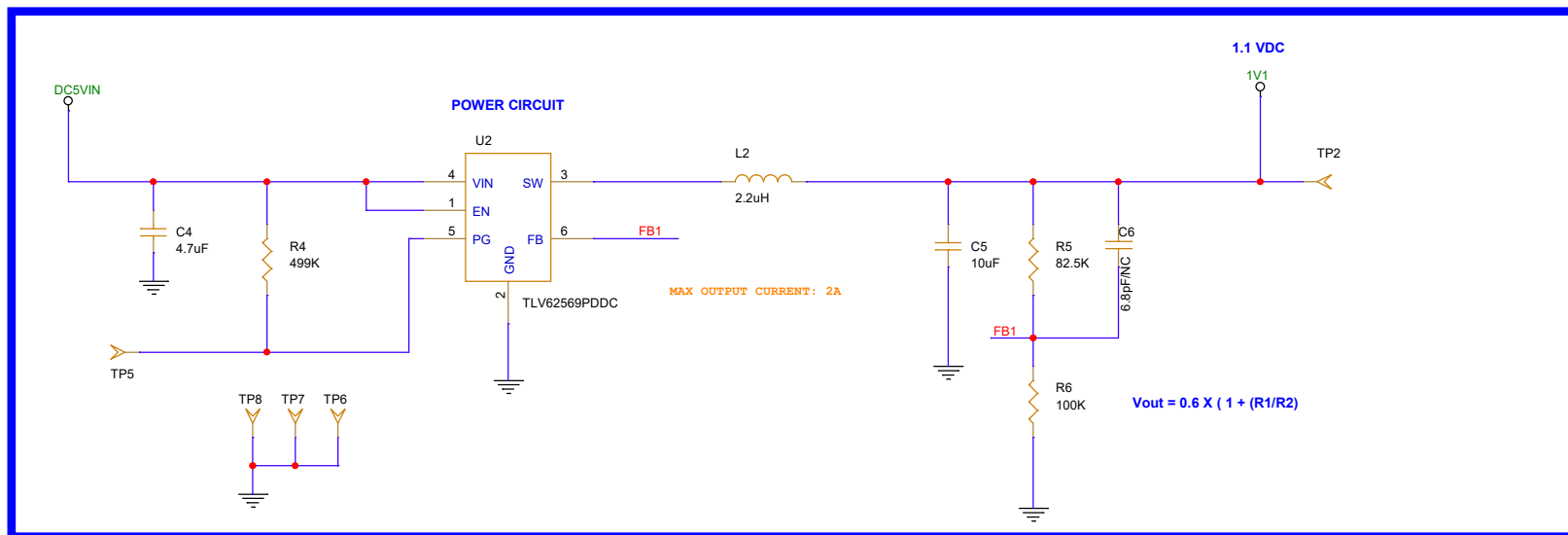
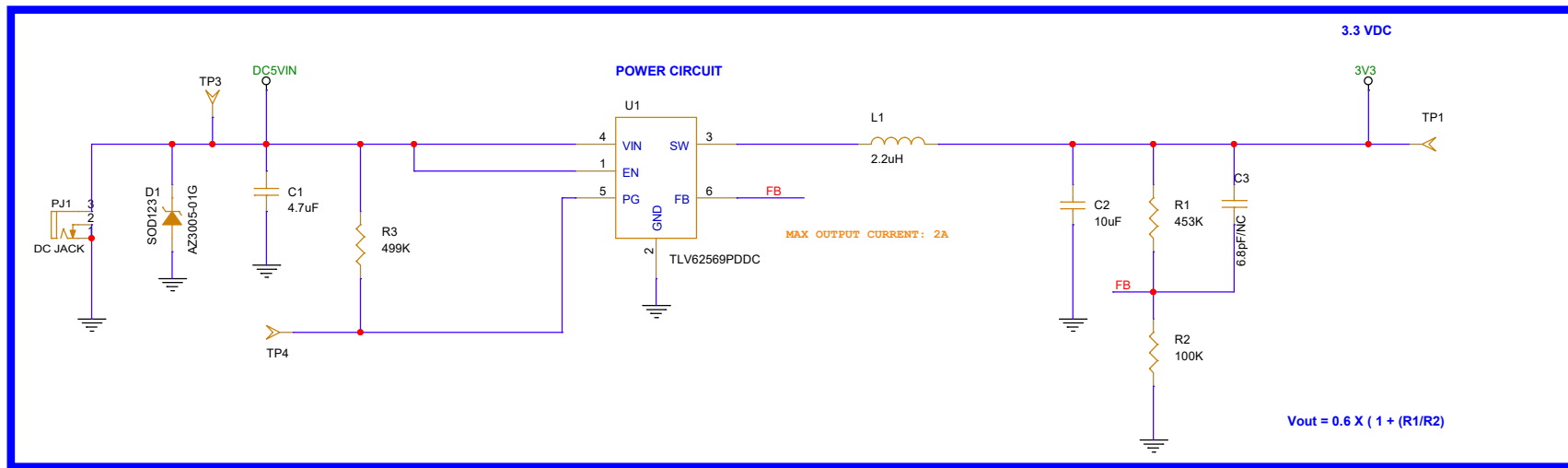
Revision	Release Date	Summary
v . 1 . 0	2018/11/25	RTL8367N-VB First release
v . 1 . 1	2024/08/26	Power Circuit has been Changed - U1 & U2 ENY List Update - U3, U4, C15, C16, R9, R13, R16



Internal Product Code Name

Kyi - Leo
MS-2210U-5T

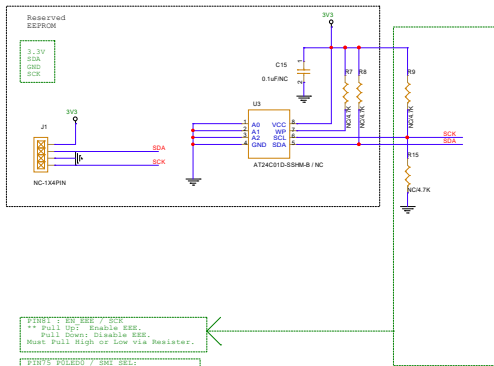
		VELANKANI ELECTRONICS & AUTOMOTIVE PVT LTD		VEAPL Confidential
Size	Document Name	DWG NO	Rev	
B	Kyi - Leo : 5G - Realtek	Block Dia	1.1	
Scale		Designer	KRIKA TRIPATHI	Sheet
Monday, September 09, 2024		1		of 5



*RTL8367N-VB: 3.3V = 0.6V x (1 + 453K / 100K)

*RTL8367N-VB: 1.1V = 0.6V x (1 + 82.5K / 100K)

		VELANKANI ELECTRONICS & AUTOMOTIVE PVT LTD		VEAPL Confidential
		Size B	Document Name Kyi - Leo : 5G - Realtek	Rev 1.1
Monday, September 09, 2024		Scale	Designer KRITIKA TRIPATHI	Sheet 2 of 5



PIN1: EN_EE / SCL
** Pull Up: Enable EE.
Pull Down: Disable EE.
Must Pull High or Low via Resistor.

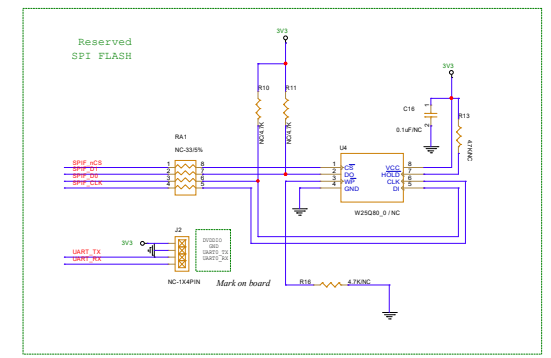
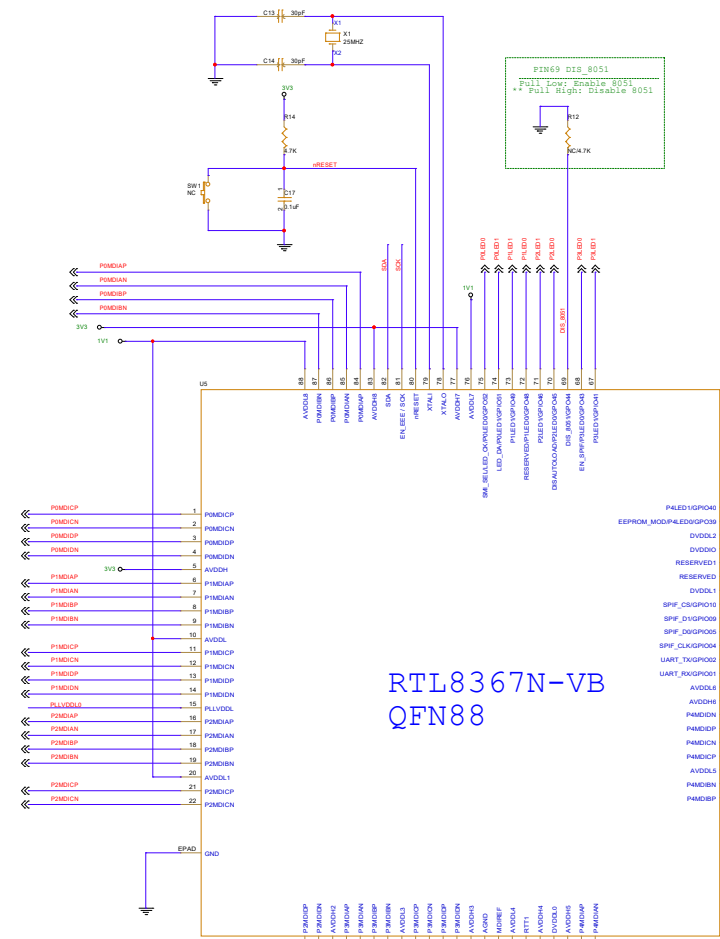
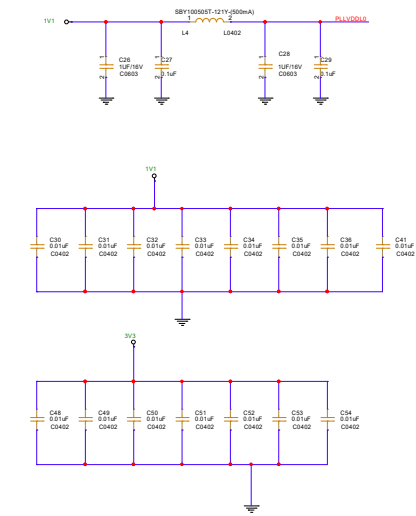
PIN5 P1LED0 / SMI_SEI
** Pull Up: Enable SMI interface.
Pull Down: MIO slave interface.

PIN7 P1LED0: Must Pull Low

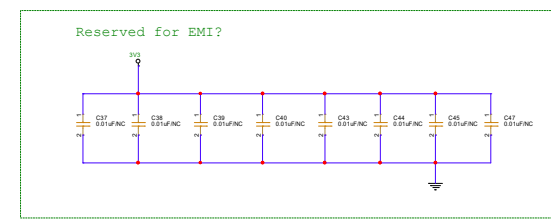
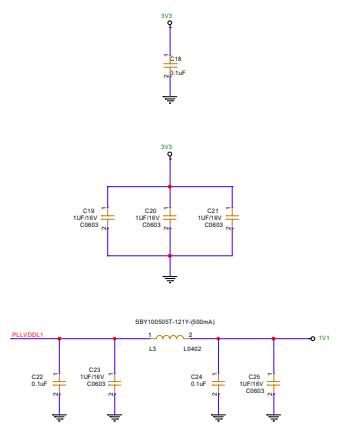
PIN9 P1LED0 / DISAUFOLD
** Pull Low: Enable Auto load EEPROM
Pull High: Disable Auto load EEPROM

PIN6 P1LED0 / EN_SPIF
** Pull Low: Disable SPI Flash Interface.
Pull High: Enable SPI Flash Interface.

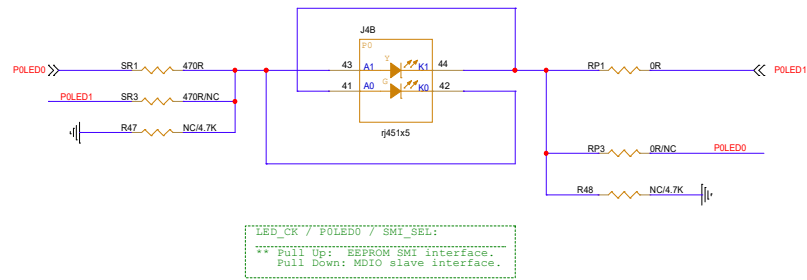
PIN3 P1LED0 / EEPROM_MIO
** Pull Low: 24C02-24C16
Pull High: 24C32-



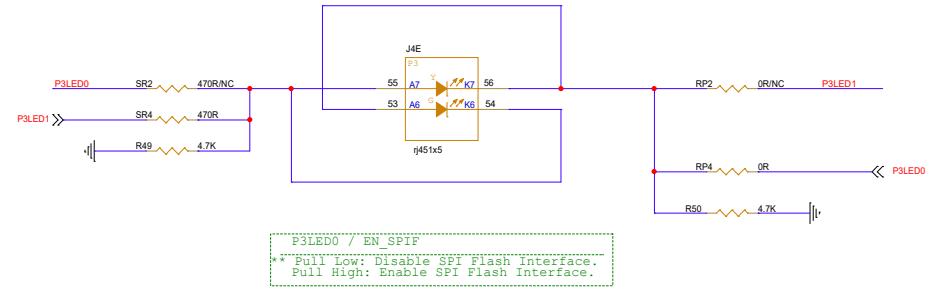
These pin must connect to GND through 4.7k ohm resistor when unused.



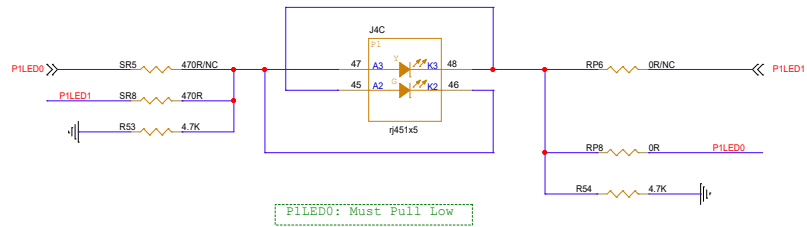
Port 0 LED



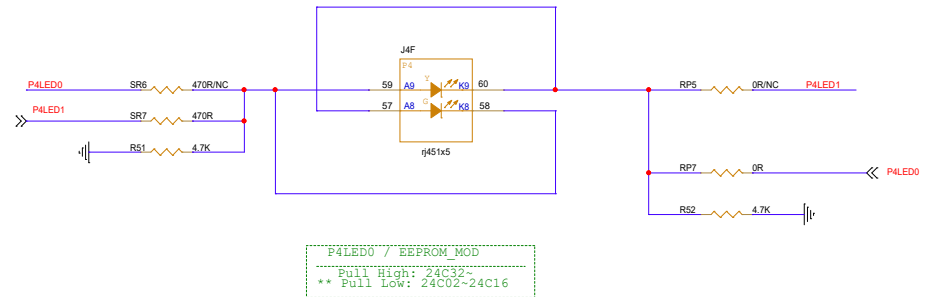
Port 3 LED



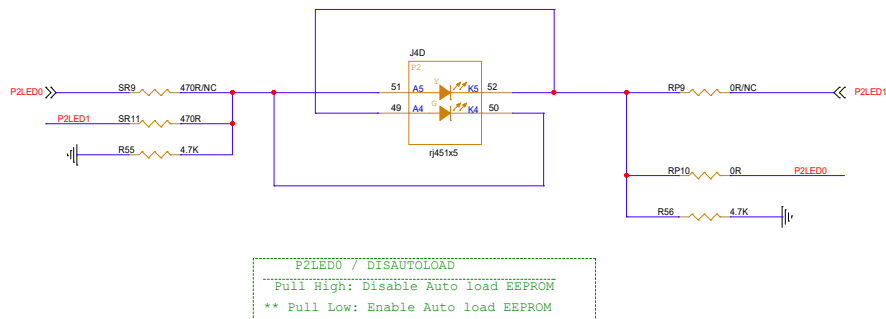
Port 1 LED



Port 4 LED



Port 2 LED



Power LED

