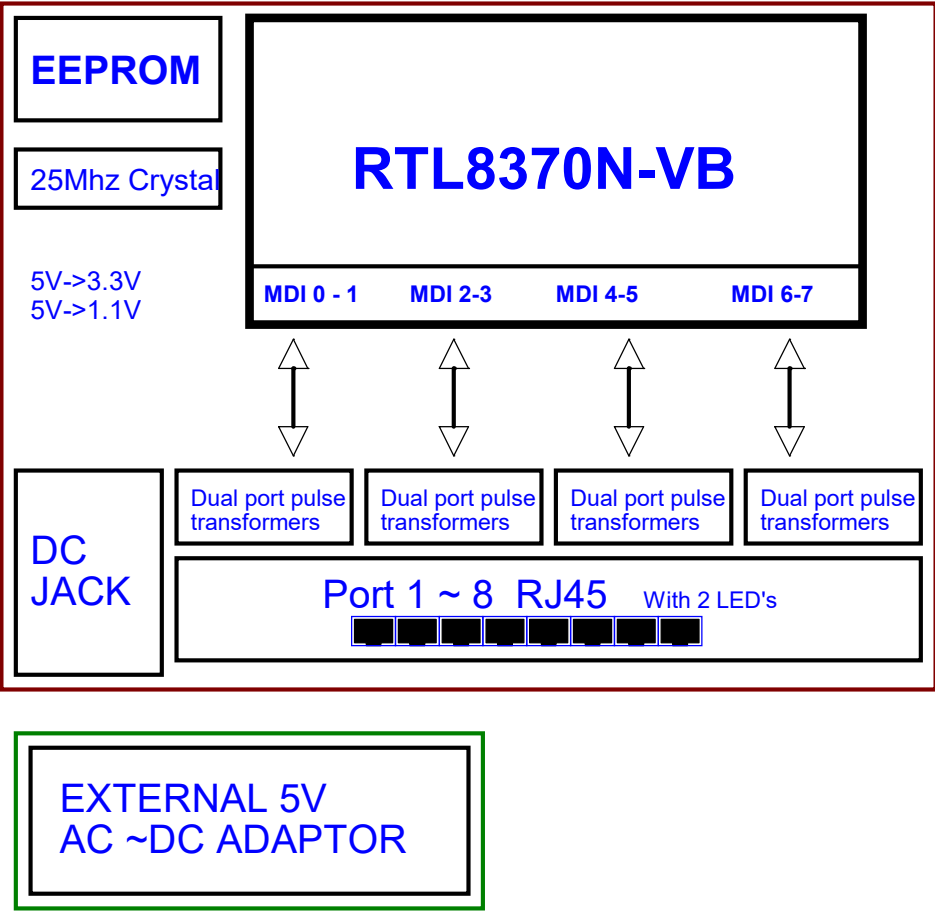


RTL8370N-VB (LQFP128) Demo-Board Block Diagram



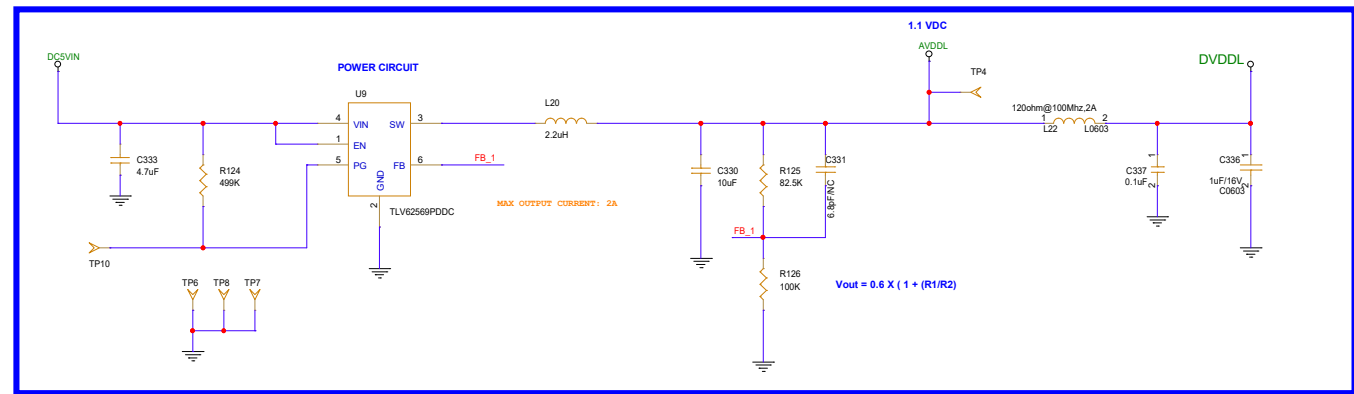
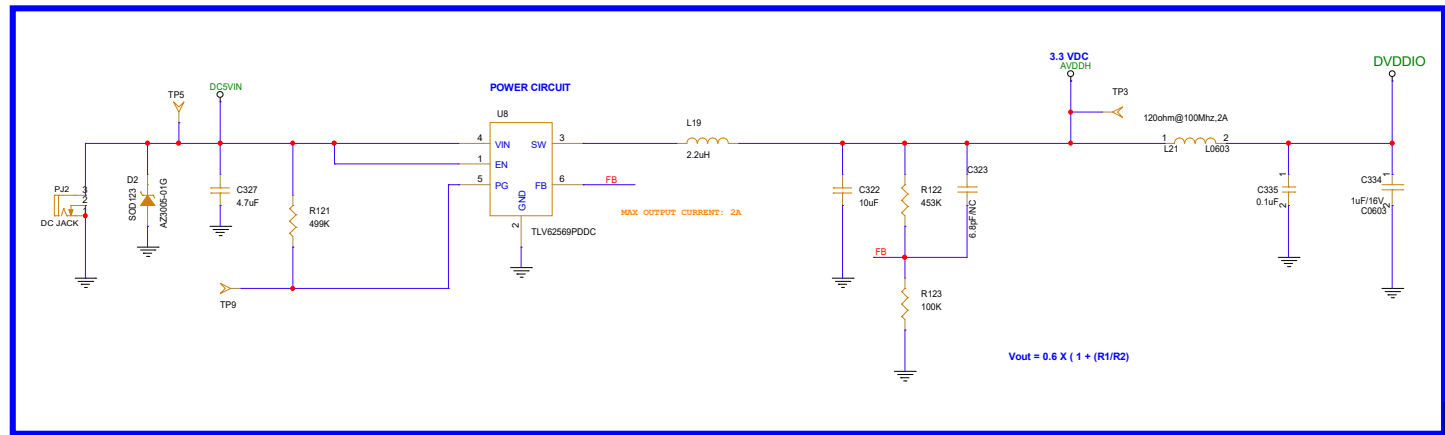
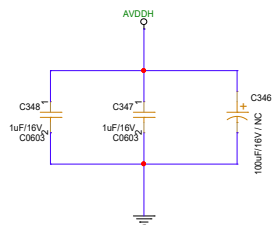
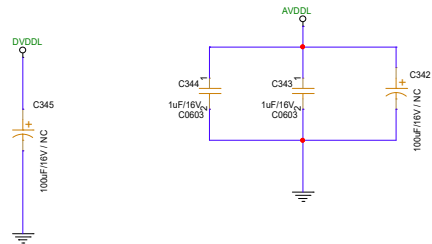
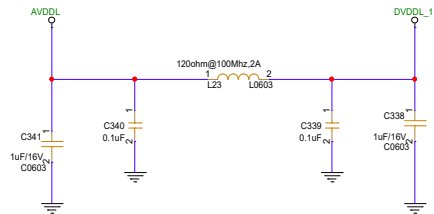
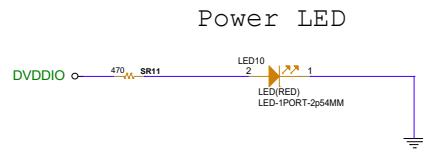
REVISION HISTORY

Revision	Release Date	Summary
v1.0	2018/11/28	Preliminary Release
v1.1	2019/02/28	TEST POINTS ARE ADDED DNI List update - R153 , C342, C345, C346 (DNI) R152 - 4.7K
v1.2	2024/08/27	Power Circuit has been Changed DNI List Update - C288, R9

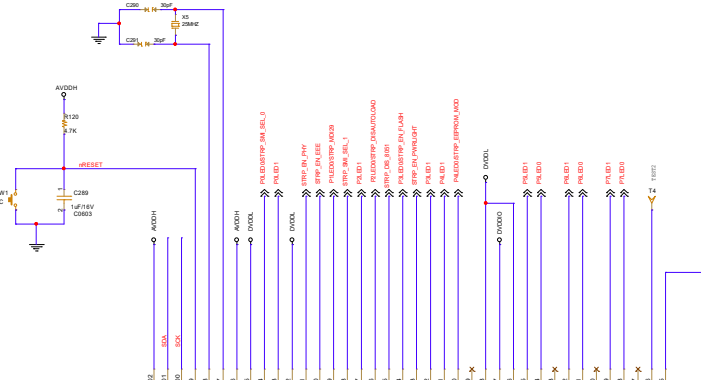
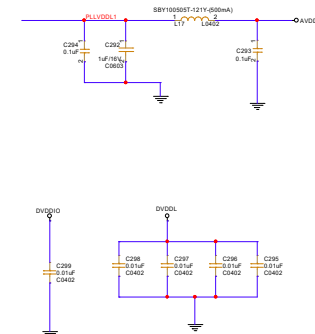


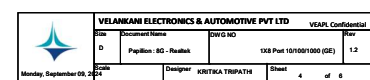
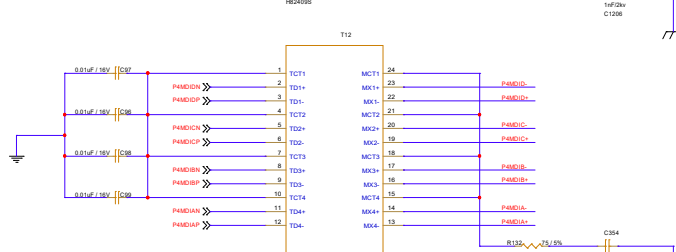
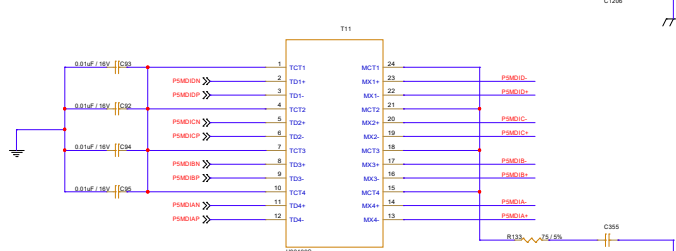
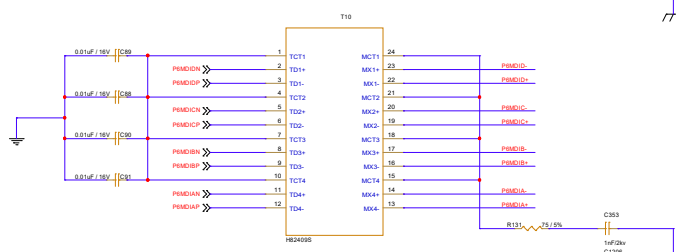
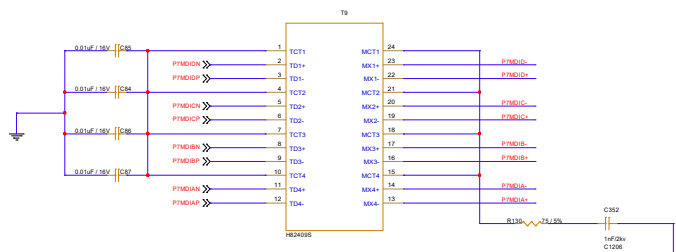
Internal Product Code Name
PAPILLION
MS-2220U-8T

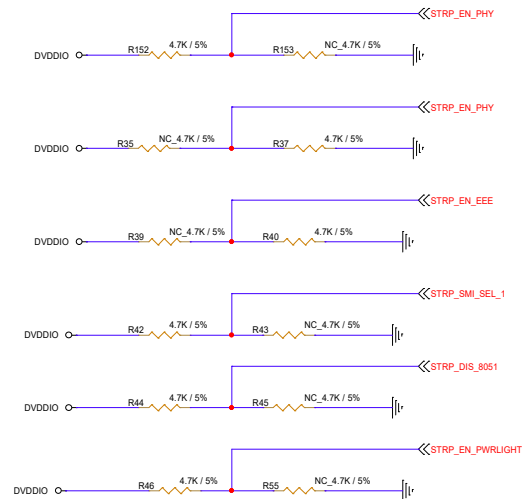
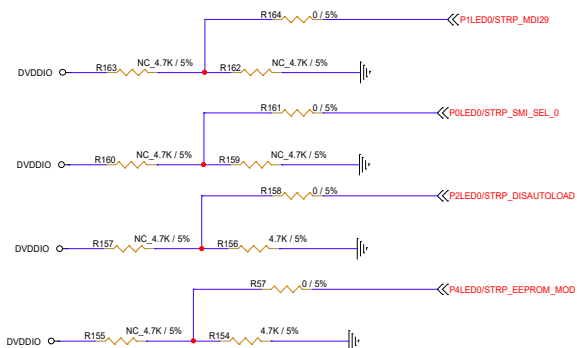
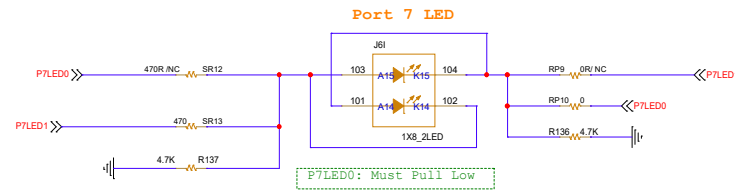
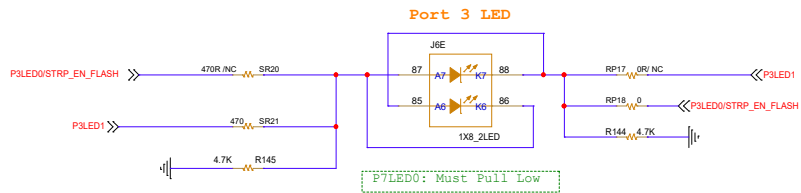
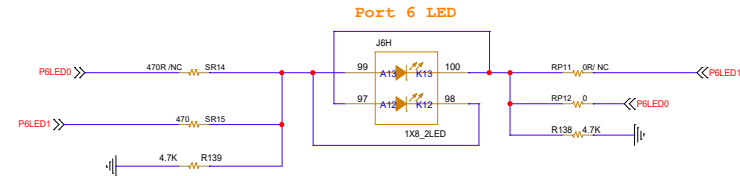
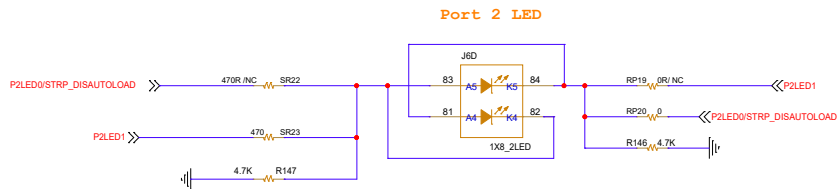
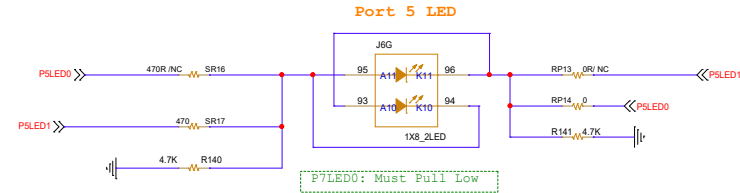
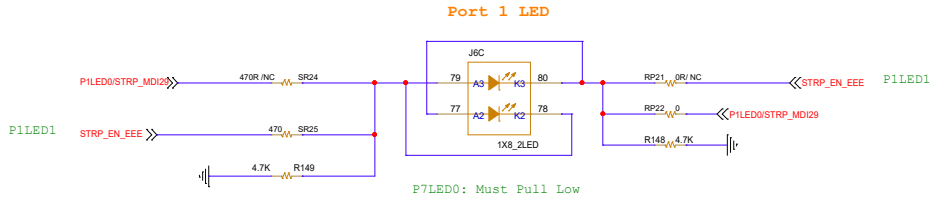
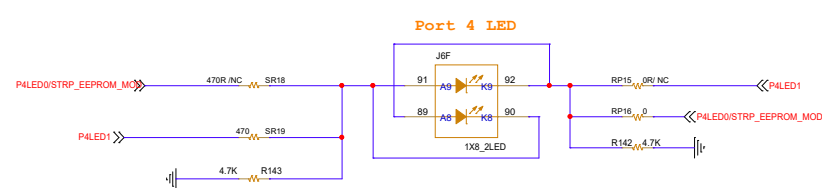
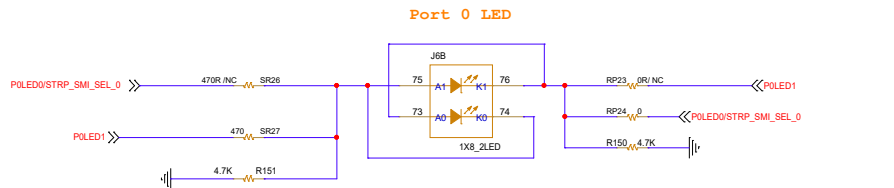
		VELANKANI ELECTRONICS & AUTOMOTIVE PVT LTD		VEAPL Confidential
Size	Document Name	DWG NO	Rev	
B	Papillion : 8G - Realtek	Block Dia	1.2	
Scale		Designer	Sheet	of
Monday, September 09, 2024		KRITIKA TRIPATHI	1	6



*RTL8370N-VB: 3.3V = 0.6V x (1 + 453K / 100K)
 *RTL8370N-VB: 1.1V = 0.6V x (1 + 82.5K / 100K)

[illegible]





EN_PHY:
Pull Up: Enable embedded PHY
** Pull Down: Disable embedded PHY

EN_EEE:
Pull Up: Enable embedded PHY
** Pull Down: Disable embedded PHY

EEPROM/SMI/MI Management Interface Selection
SMI_SEL[1:0]:
00: LSB I2C
01: MSB I2C
10: MDC/MIO
11: RTK I2C

DIS_8051
Pull Low: Enable 8051
** Pull High: Disable 8051

EN_PWRLIGHT
Pull Low: Disable Power on Light
** Pull High: Enable Power on Light

[illegible]

Diagram illustrating the mapping of P3MDIAP, P3MDIAN, P3MDIBP, P3MDIBN, P3MDICP, P3MDICN, P3MDIDP, and P3MDIDN to a common set of 8 lines.

Diagram illustrating the structure of the P7MDIAP-P7MDIDN complex. The complex is shown as a dimeric structure with two subunits, P7MDIAP and P7MDIDN, interacting with a central region. The subunits are labeled P7MDIAP, P7MDIAN, P7MDIBP, P7MDIBN, P7MDICP, P7MDICN, P7MDIDP, and P7MDIDN.



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