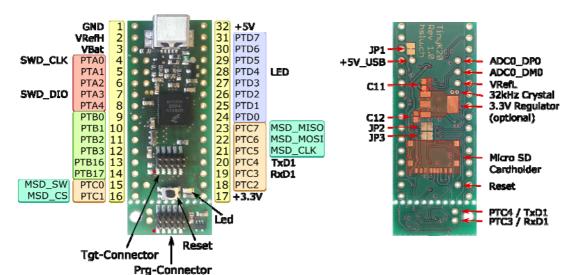
TinyK20, Rev 1.0



Lucerne University of Applied Sciences and Arts

HOCHSCHULE LUZERN

Technik & Architektur CC Electronics

	PTAO	PTA1	PTA2	PTA3	PTA4	PTB0	PTB1	PTB2	PTB3	PTB16	PTB17	РТСО	PTC1	PTC2	PTC3	PTC4	PTC5	РТС6	PTC7	PTD0	PTD1	РТD2	PTD3	PTD4	PTD5	PTD6	PTD7
UARTO		R	Т							R	Т															R	Т
UART1															R	Т											
UART2																						R	Т				
FTMO	5	6	7	0	1								0	1	2	3								4	5	6	7
FTM1						0	1																				
1200						С	D	C	D																		
SPIO																	С	0	_		С	0	_				
ADCO						8	9	12	13	14	15	4b									5b				6b	7b	
CMPO																	0	10	11								
CMP1														10	11	0											
TSI	1	2	3	4	5	0	6	7	8	9	10	13	14	15													



Reset / PTB1 RxD1 / PTC3 CLK DIO
1 * * * * *
TxD1/ GND 3.3V
\$9 <u>9</u> 5
_
PTC4
2
Prg-Connector

