

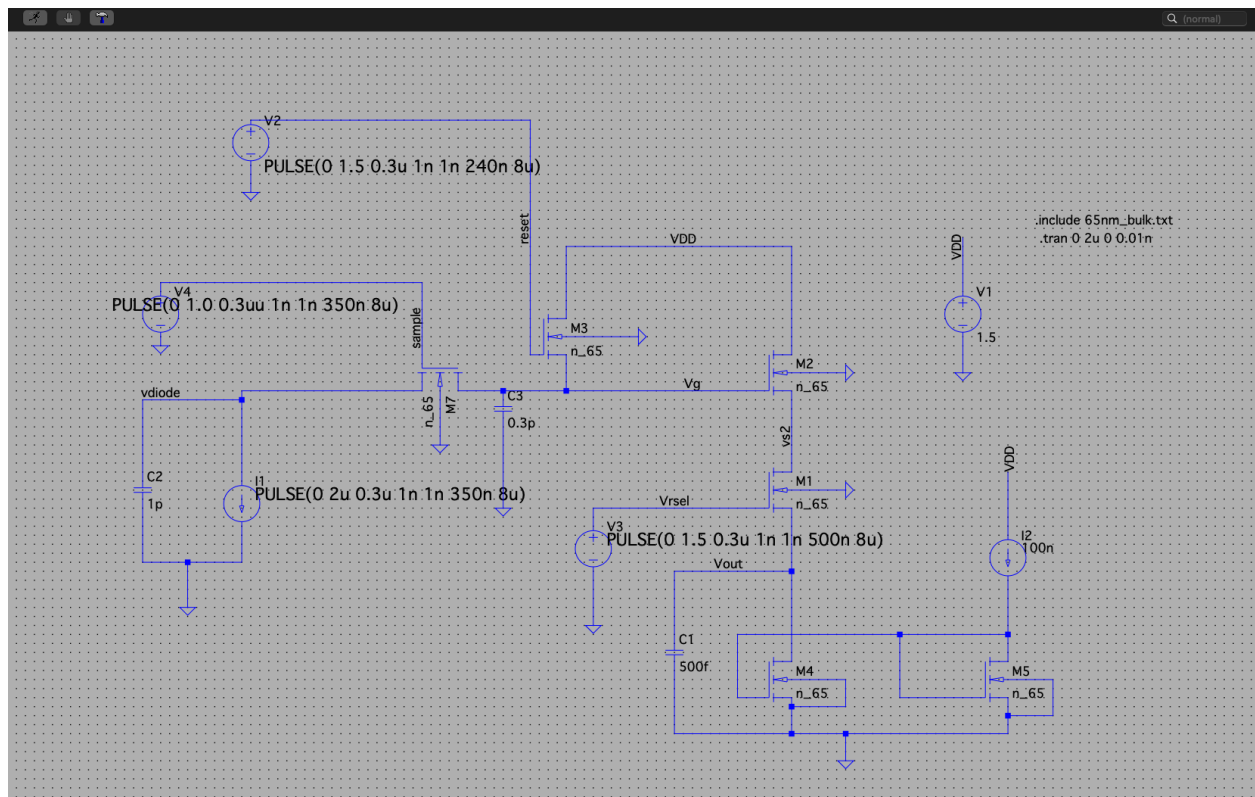
VLSI Summer School Project Documentation 2025

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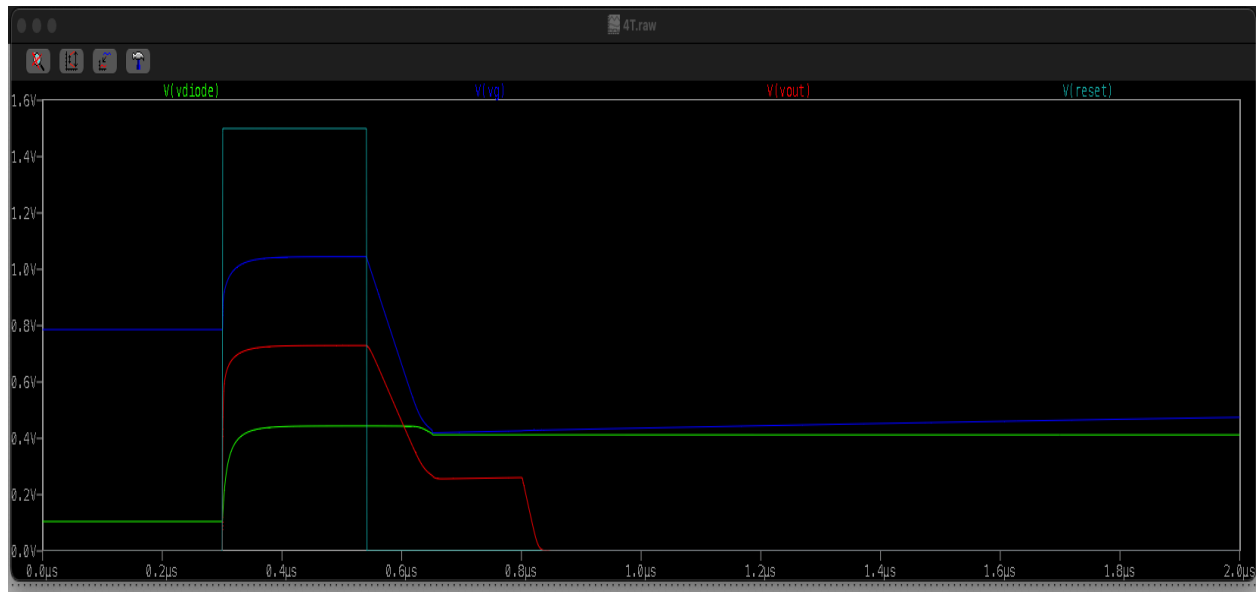
Assignment 2: 4T pixel simulations

- Circuit Diagram:

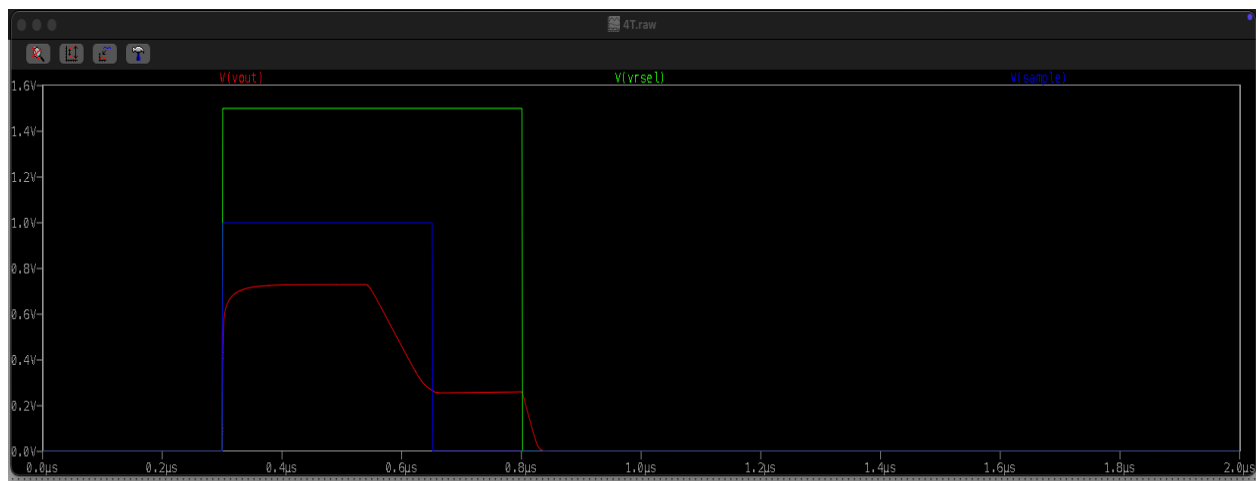


Assignment 2: 4T pixel simulations

i) Transient simulation:



vdiode(green),vout(red),vg(blue),vrselect(peacock green)



Vsample(blue),vout(red)

Assignment 2: 4T pixel simulations

ii) Vout vs ID:

Here ,Id varying from 100nA to 1uA.

