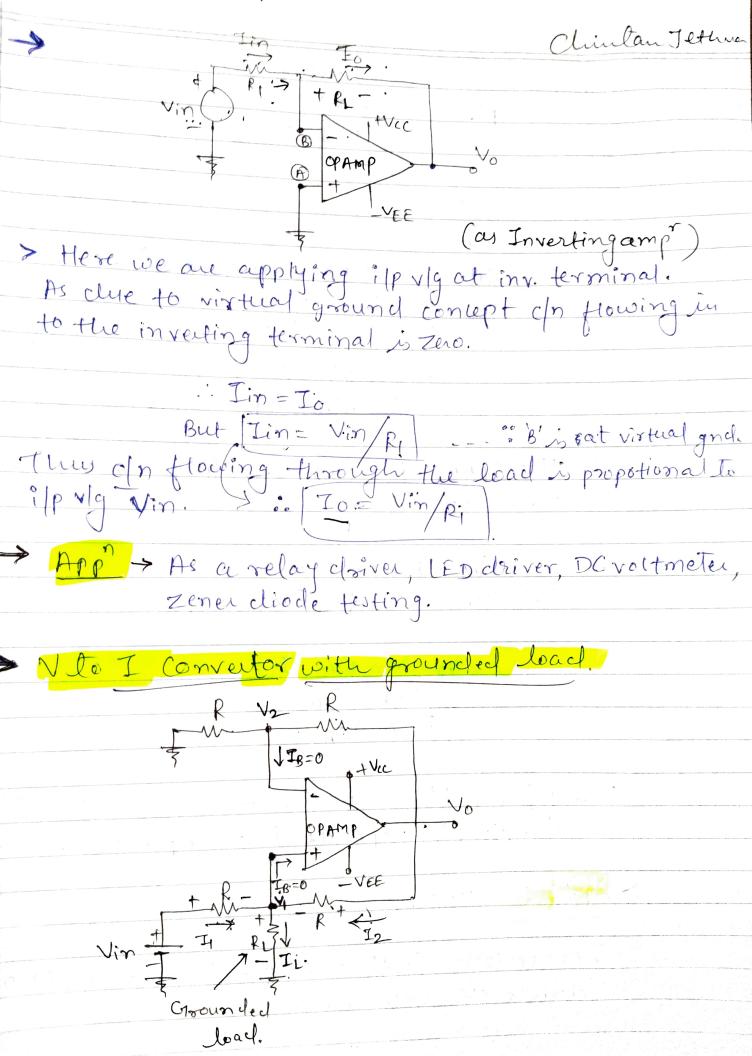
	> Voltage to current (VIoI) Convertor with
	of loading load
angara da garangan kananan da sa	> Here load is called Hoating load Lacour
	it is not connected to ground.
	Prince To
	To It Ve Iso If to aling load) Vol opamp ITo VEE (ampr)
	3. It (floating load)
	Vd pamp IIo
	+ VEE Contrata non-Inv.
	Vin - ampr)
	> I/p v/g is cupalied to non-inv terminal of opamp
	> Ilp rig is applied to non-inv. terminal of oppmp. Re connected in place of the resistor RE.
	As the Alb vig across fins propotional to the
	old do Toil appears in series with ilp v/9.
	Therefore it is called as of series with ilp vlg.
	(Delice 11 2) Centred by self-it
	> Byan lu KVI to flo loop
	> By apply KVL to if loop, Vin = Vd + VF
-	2. It as the agin of oppring in Very large
	But as the gain of OPAMP is Very large we can neglect 'Vd'ie Vd = 0
	we can regreat vare va
The second secon	.; Vin=VF.
The state of the s	= RixIo becoz IB 20 coRi=00
The second secon	or high
	Jo = Vin de Richard
	RI La con reclair to
	propotional of of To=Vin/P, if fi is a precision resistor then we can adjust of ch.
	propotional of dn Io=Vin/P, if the a
	precision resistor then we can adjust of chi
2	HID I I OW VI 9 CI C & CC VO MINICIA, DE D
	Clotte
- American	f/b -> feedback



Applying CL at node V_1 , $T_0+I_1-I_1+I_2+I_3$ but I_8-0 $I_1-I_1+I_2-I_3$ $I_1-I_1+I_2-I_3$ $\frac{2}{R} \frac{T_1 - V_{in} - V_i}{R} \frac{2}{R} \frac{T_2 - V_0 - V_i}{R}$ substituting these values in eq (1) IL = Vin-VI + Vo-VI : IR = Vint Vo + -2 Vi $\frac{1}{2} \cdot V_{\parallel} = \frac{V_{\parallel} + V_{0} - J_{1}R}{2}$ As the opamp is in non-Inverting mode, Crain AVF = ITR = 2 Substituting vi from eqn(2), $V_0 = V_{in} + V_0 - I_{iR}$ $: I_{iR} = V_{in}$ $: I_{i} = V_{in}/R$ They from this equation it is clear that load of the is dependent on ilp vlg & resistore. Are is same. Zenerdiocle testing, As a selay elsiver, LED chriver, DC voltmeter.

Current la voltage convertor (I to V) Tim V_{1} V_{2} V_{3} V_{4} V_{6} V_{6} V_{7} V_{7} As per the concept of virtual short. $V_1 \approx V_2$ & as ifp impedance is very high: IB, = IB2 =0. Crain of inverting amp, AVF = -RF = Vo Pr Vin i. Vo = - RF Vin. -> () > As Vi ~ V2 and Vi=0 as non-Inv. ferminalis connected to ground: V2 =0. Thus inverting terminal is also at ground potential 2 entire ilprig appears across Ri. I'm Win/Ri .. Vin = Iin. Ri put this value in equation (). $V_0 = -RF Jin \cdot R_1$ R_1 · Vo=-RF. Tim Which inclicates that ofp vig is propotional to ilp current Iin. > Anp > light Entensity meter, D/A convertor.