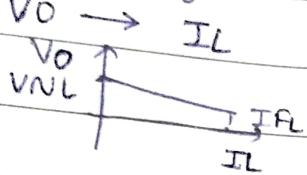


## Ch. 5: Voltage Regulators

\* Voltage regulator → removes ripples  
Keeps o/p vtg. constant irrespective of change in load or line vtg.

Load regulation



$$\% LR = \frac{V_{NL} - V_{FL}}{V_{FL}} \times 100$$

Line regulation

$$V_o \rightarrow V_{in}$$

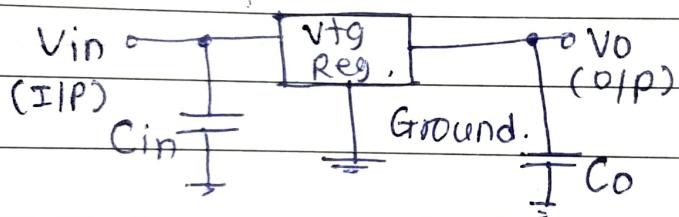
also called source reg.

$$\% SR = \frac{V_L}{V_{nom}} \times 100$$

$S_R = V_L$  with high line vtg  
-  $V_L$  with low line vtg.

$V_{nom} \rightarrow$  nominal load vtg.

### \* 3-terminal fixed vtg. regulators:

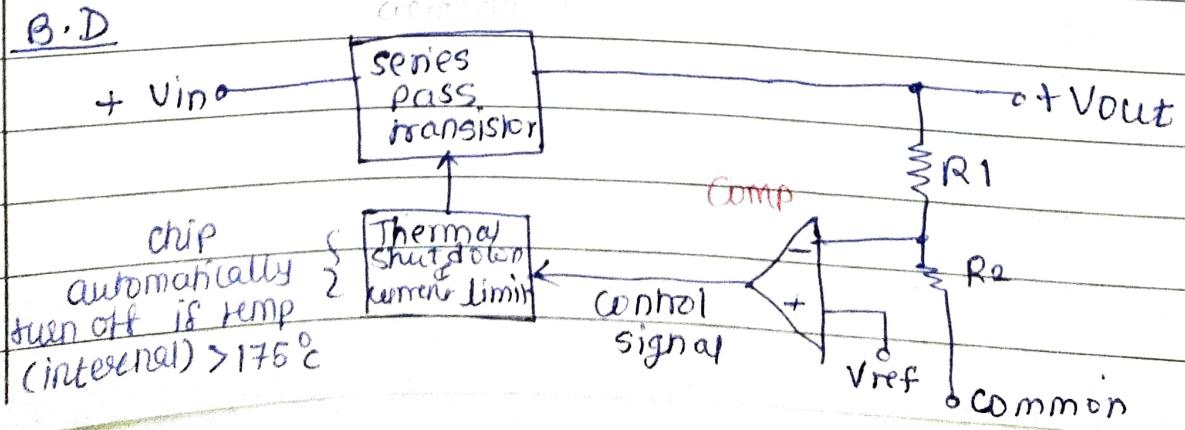


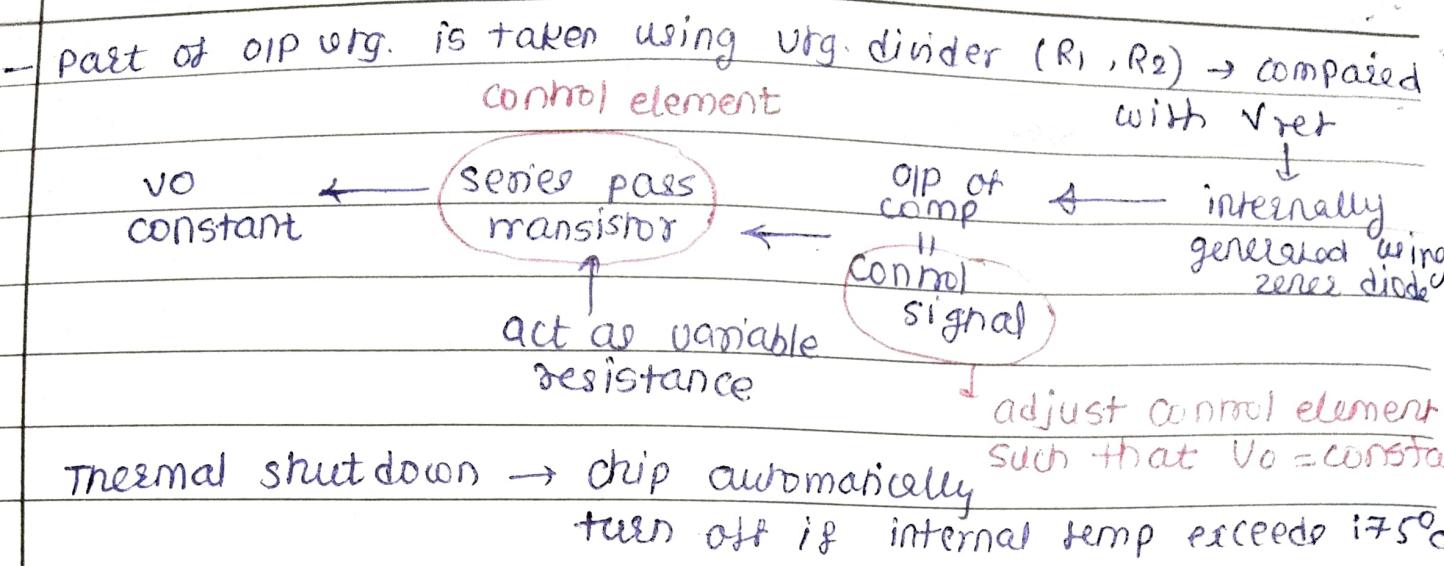
- no f/b,  $C_{in} \rightarrow$  req → if regulator is at more than 5cm dist. from PS.

$C_o \rightarrow$  improves transient response of regulator.  
→ also ↓ noise at o/p.

$$(V_{in} - V_o)^- = \text{dropout vtg.} = 2V \rightarrow \text{for proper func of regulator.}$$

### \* B.D





#### \* Performance Para for Regulator ICs:-

- Line regulation  $= \Delta V_o / \Delta V_i \rightarrow \%$  or mV.
- Load regulation  $= \Delta V_o / V_{FL} - V_{NL} \rightarrow$  mV or %.
- Ripple rejection  $\rightarrow$  regulator's ability to reject ripple v<sub>tg</sub> present in oip  $\rightarrow$  dB.
- $R_o \rightarrow dV_o / dI_o \rightarrow \downarrow$  as possible.
- Dropout v<sub>tg</sub>  $=$  min v<sub>tg</sub> exist betw  $V_{IN} & V_o \approx 2$  to 3V.
- $V_{IN\ max}$   $\rightarrow$  can be applied to regulator safely.
- $P_{D\ max}$   $\rightarrow$  max power regulator dissipate without damage.
- $I_Q \rightarrow$  quiescent current  $\rightarrow$  min current the regulator to perform satisfactorily.
- $I_o \rightarrow I_{o\ max} \rightarrow$  Rated oip current.

\* 78XX & 79XX  $\rightarrow$   
 +ve v<sub>tg</sub>  $\xrightarrow{\quad}$  -ve v<sub>tg</sub>  
 regulators regulators.

XX  $\rightarrow$  OIP v<sub>tg</sub> rating of IC.

e.g. 7805  $\rightarrow$  5V, 7905  $\rightarrow$  -5V

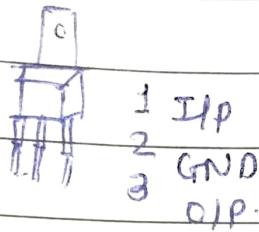
7806  $\rightarrow$  6V . . . 2 extra 2 & 5.2

8, 12, 15, 18, 24

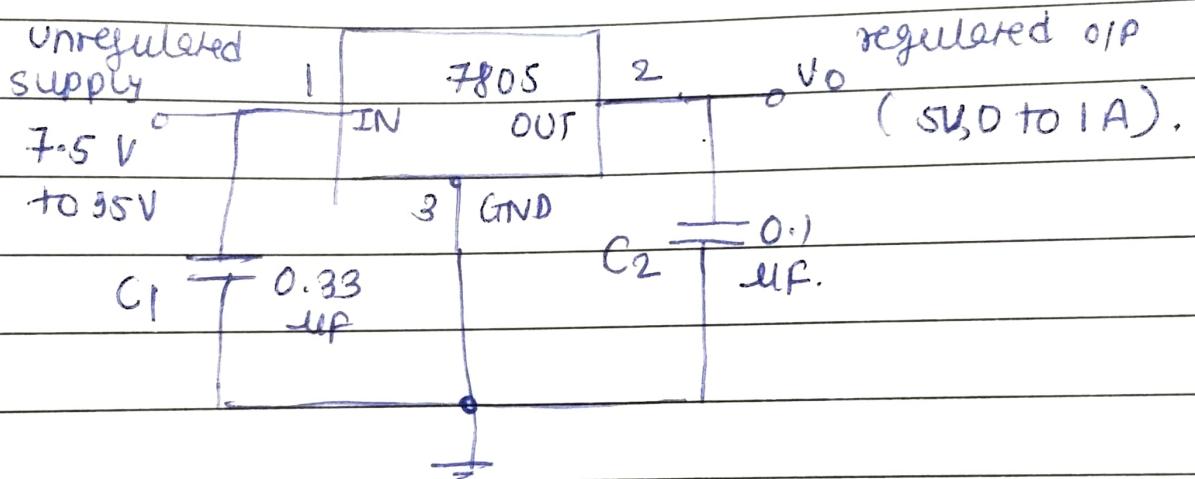
(Begin answer for each question)

- These ICs do not req. ext. Components.
- internal thermal protection, overload & SC protection.

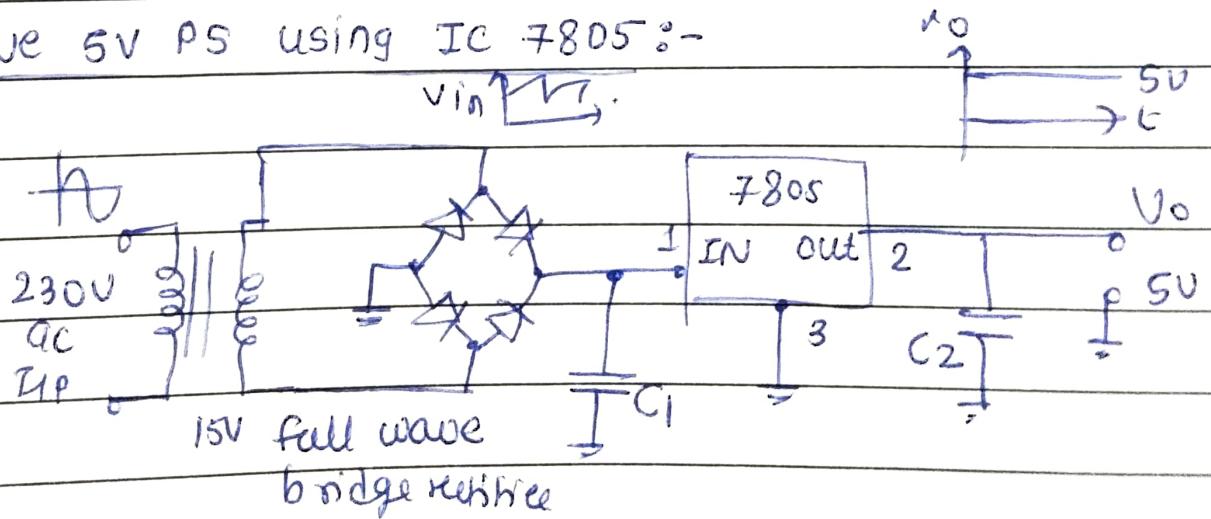
low power      high power.  
↓  
like transistors      ↓ metal cans



### \* 7805 regulator:



### \* +ve 5V PS using IC 7805 :-



→ 230V → Step down to 15V

→ C → V\_L → AC + DC → 7805 → Regulated o/p utg  
→ removes AC

## IC voltage Regulator

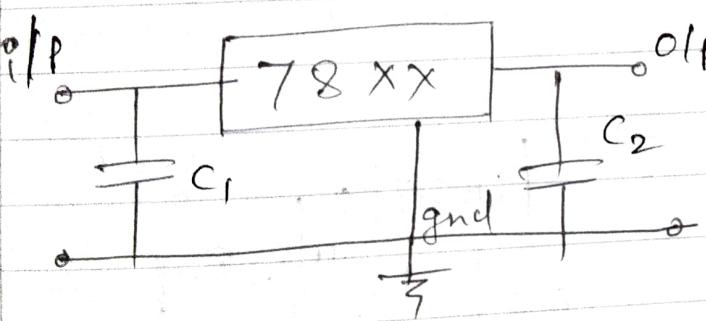
IC voltage regulator is an electronic integrated device which provides constant voltage regardless of changes in load circuits.

## Advantages of IC voltage regulator

- Compact size & light in weight
- Power supply design becomes easy & quick.
- Very cheap due to mass production.
- most efficient & reliable.

## 3 pin voltage Regulator

### Fixed positive voltage regulator

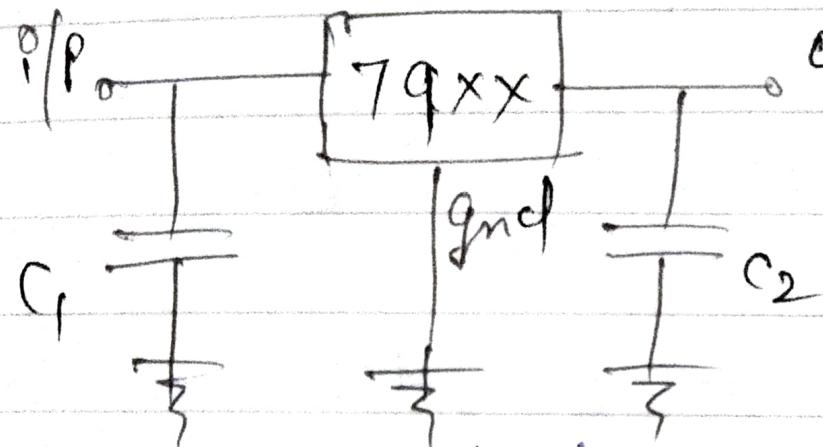


→ The 7800 series of IC voltage regulators is representative of three terminal device that are available with several

- fixed positive o/p voltages.
- The three terminals are i/p, o/p & ground. The last two digit 'XX' in the part number designate the i/p voltage. Eg → 7805 IC is +5V regulator. Similarly 7812 IC for +12V regulator.
- $C_1$  is (0.33μF) required if power supply filter is located more than 3 inches from IC regulator.  $C_2$  (0.01μF) is required to act as a line filter to improve transient response.

→ 78xx series IC regulator can produce o/p voltage ranging between +5 to +24 V.

## → Fixed negative voltage regulator

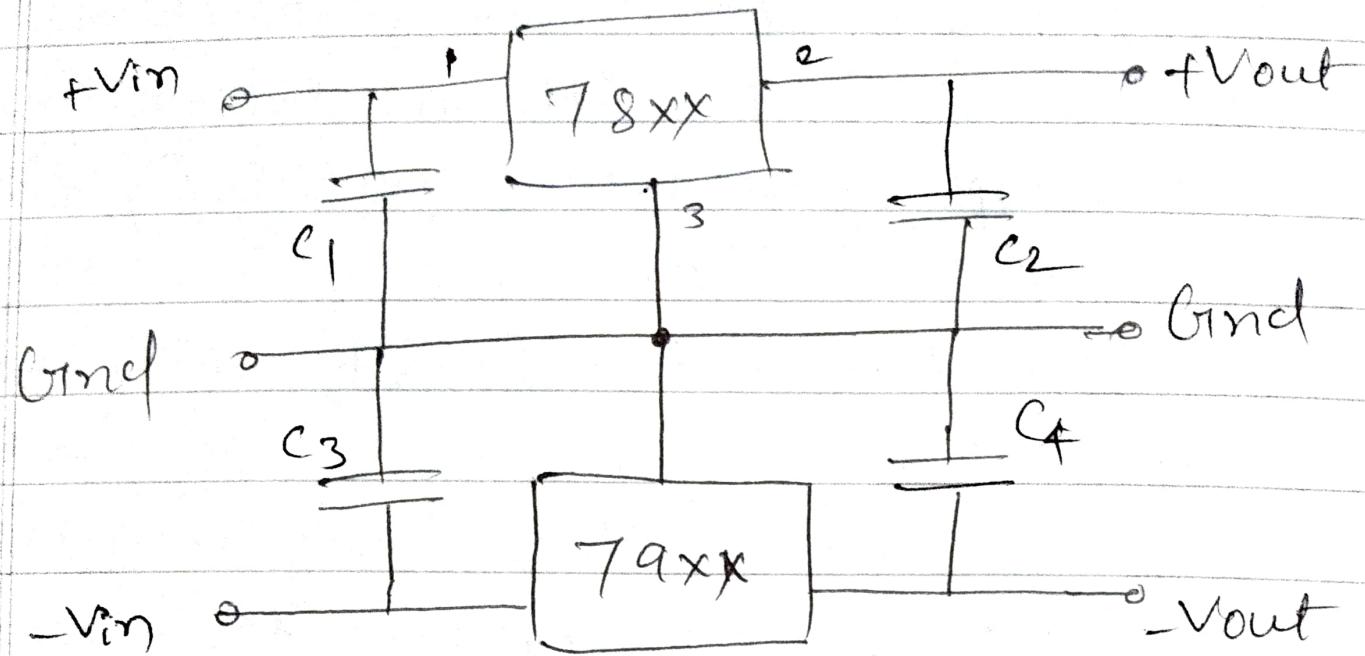


→ 7900 series is typical of three terminal IC regulator that provide fixed -ve o/p voltage.

Range is between -5 to -24 V.

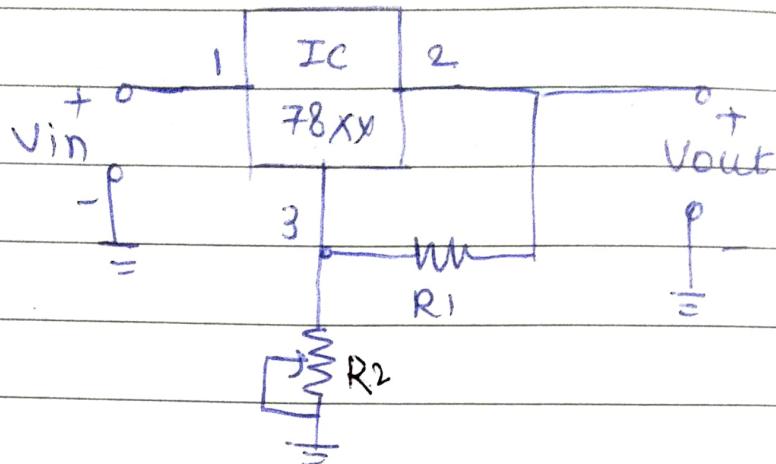
→ Reason of C<sub>1</sub> & C<sub>2</sub> are same as previous.  
i.e. C<sub>1</sub> (0.22 μF) is required only if power supply filter is located more than 3 inches away from IC regulator & C<sub>2</sub> (1 μF) is required for stability of o/p voltage.

## → Dual Tracking Regulator



- Dual tracking regulator is regulator whose o/p can be get by combining an 78xx & 79xx as shown.
- 78xx regulates +ve o/p & 79xx handles -ve o/p. The i/p capacitors prevents oscillation & o/p capacitor improves transient response.
- Dual tracking regulators contain +ve & -ve regulators in a single IC package.

\* adjustable regulators using IC 78XX :-



$$V_{\text{out}} = V_{\text{reg}} \left[ 1 + \frac{R_2}{R_1} \right]$$

where  $V_{\text{reg}}$  = Regulated fixed vrg. of IC.

$R_2$  vary  $\rightarrow$  variable o/p vrg.

\* Applic.

1. to provide constant dc vrg to electric dlets
2. 5V  $\rightarrow$  digital dlets.
3. 12V  $\rightarrow$  OP-amp.

1 calculate  $V_o$  of adjustable regulator with  $R_1 = 5K$ ,  $R_2 = 10K$

$R_2 = 10K$ . If  $R_2 \rightarrow 2K$  to  $10K$  find range of  $V_o$ .

7808 IC is used

$\rightarrow R_1 = 5K$ ,  $R_2 = 10K$ ,  $V_{\text{reg}} = 8V$

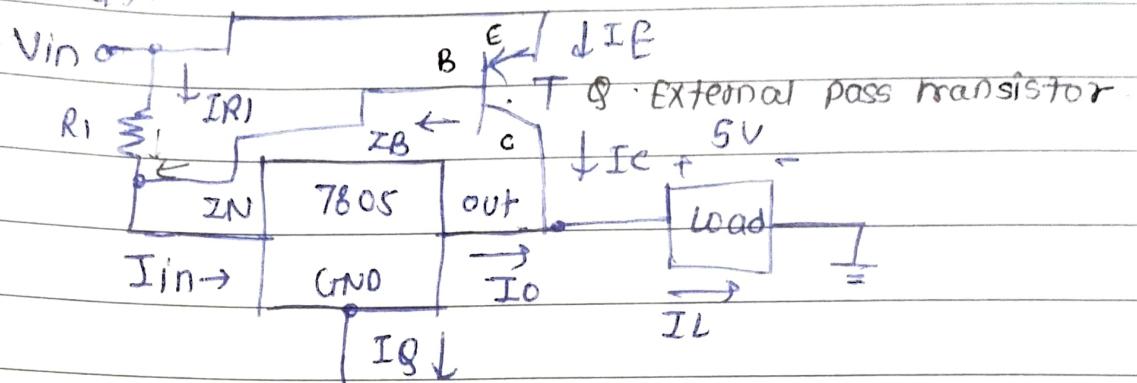
$$V_o = V_{\text{reg}} \cdot \left[ 1 + \frac{R_2}{R_1} \right] = 8 \left[ 1 + \frac{10}{5} \right] = 8 \times 3 = 24V$$

now  $R_2 = 1K$

$$V_o = 9.6V$$

$V_o \rightarrow 9.6$  to  $24V$ .

- \* Current boosting of regulator:- amplification of OIP current.
- ↓ cost & size & ↑ current . connect an transistor with regulator itself.



Hence  $IL$  from  $1A \rightarrow \frac{1}{10}A$ . when  $IL$  - low , could not drive  $T$  ON  
 ↓ because when  $IL \uparrow$  beyond certain limit  $T$ -ON & provides excess current

$I_0$  is shared by regulator & transistor.

for low  $IL \rightarrow VR_1 < 0.7V \rightarrow T_{OFF} \rightarrow IL = I_0$ .

when  $IL \uparrow \rightarrow VR_1 \uparrow > 0.7V$ ,  $T$  ON  $\rightarrow I_C$

∴ excess current req. is provided by  $T$ .

$$\text{as } I_C = \beta I_B$$

$$\therefore IL = I_C + I_0 = \beta I_B + I_0$$

$$\text{& } I_B = I_{in} - I_{R1} \quad (\because I_{in} = I_{R1} + I_B)$$

neglect  $Z_Q$ ,  $I_{in} = I_0$ .

$$\therefore I_B = I_0 - I_{R1}$$

$$\text{But } I_{R1} = \frac{V_{BE}}{R_1}$$

$$\therefore I_B = I_0 - \frac{V_{BE}}{R_1}$$

$$\therefore IL = \beta \left[ I_0 - \frac{V_{BE}}{R_1} \right] + I_0$$

$$\therefore IL = (1 + \beta) \cdot I_0 - \beta \cdot \frac{V_{BE}}{R_1}$$

thus current boosting  $\rightarrow$  value of  $\beta$  &  $Z_Q$  max of regulator.

(Begin answer)  
 7805  $\rightarrow$  current boosting is provided by T with  $V_{BE} = 1V$   
 $\& \beta = 15$ . calculate max load current. assume  $R_I = 7\Omega$

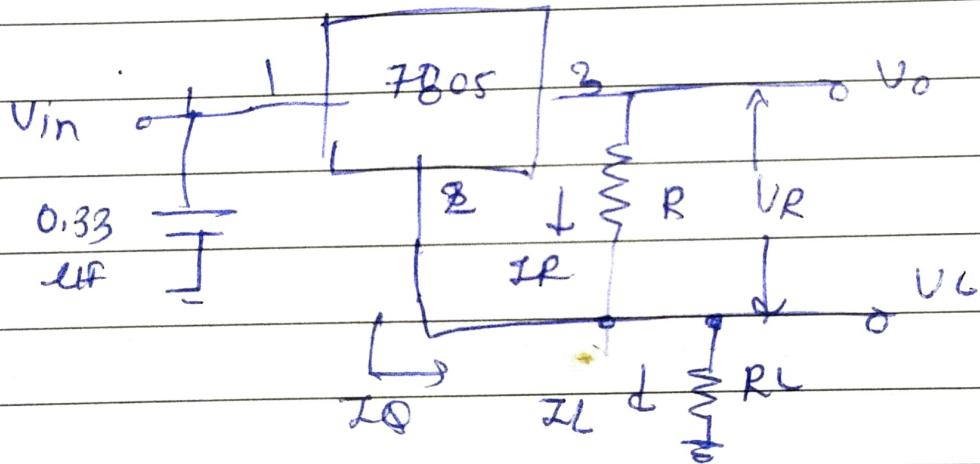
$$I_L = (1 + \beta) I_O - \beta \cdot \frac{V_{BE}}{R_I}$$

$$\therefore I_{Lmax} = (1 + \beta) I_{Omax} - \beta \cdot \frac{V_{BE}}{R_I}$$

$$= (1 + 15) \times 1 - \frac{15 \times 1}{7} = 19.85A$$

\* Current Source

Design a 0.5A current source using IC 7805,  $R_L = 10\Omega$



$$I_L = I_R + I_Q = \frac{V_R}{R} + I_Q \quad (\because I_Q = 4.2mA \text{ for 7805})$$

$$I_L = 5/R + 4.2 \times 10^{-3}$$

&  $V_R = 5V$  for 7805

$$0.5 = 5/R + 4.2 \times 10^{-3}$$

$$I_L = 0.5A - \text{given}$$

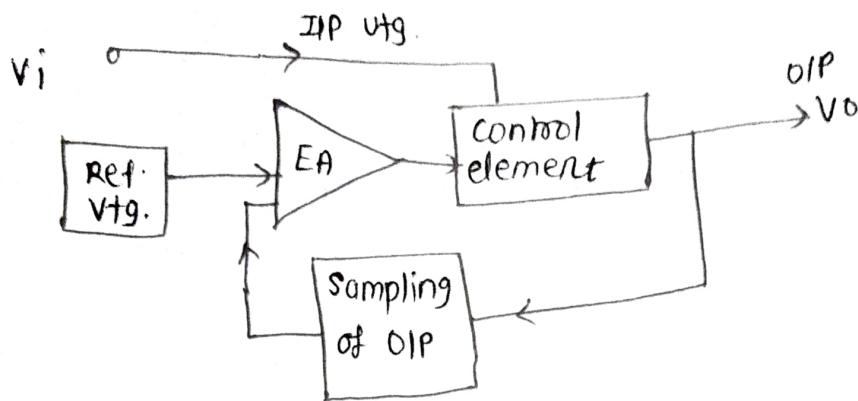
$$R = 10.0847\Omega \approx 10\Omega$$

$$V_o = V_R + V_L = 5 + I_L R_L = 5 + 0.5 \times 10 = 10V$$

$$V_{in} = V_o + \text{Drop out } V_{reg} = 10 + 2 = 12V.$$

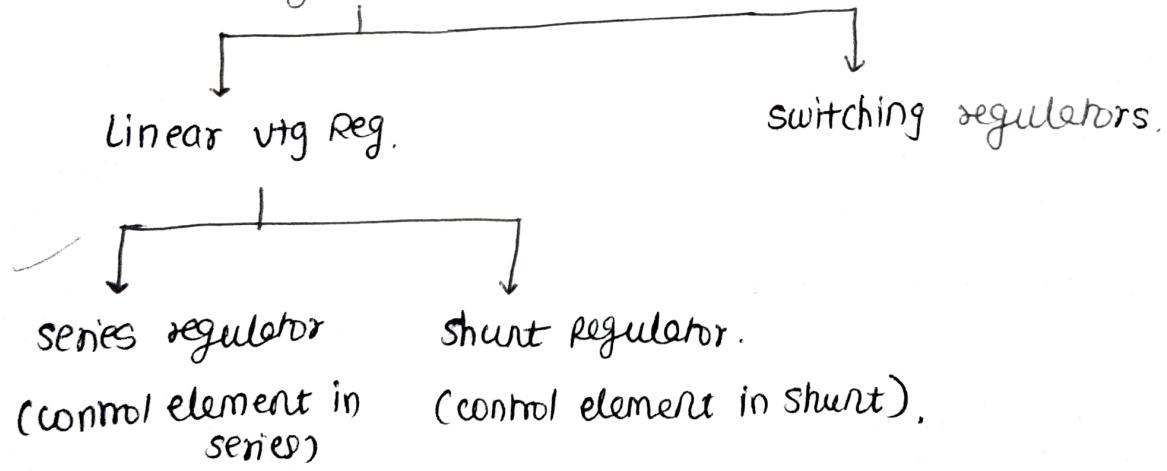
Voltage Regulators: converts AC-DC at constant level.

a) Linear Vtg regulator:

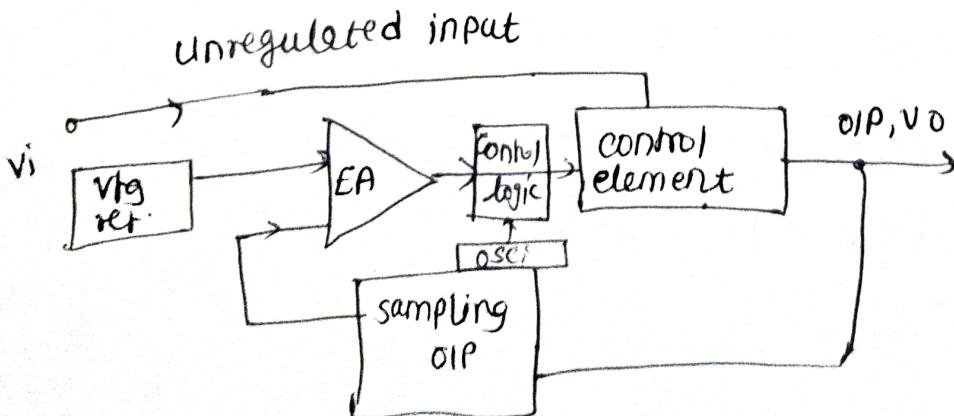


- Unregulated power supply  $\rightarrow v_o \downarrow - v_i \downarrow \text{ or } R_L \uparrow$ .
- Regulated power supply  $\rightarrow v_o$  is not depending on  $v_i$  or  $R_L$ ,  
temp or ripple in  $v_i$

\* - Types of Regulators



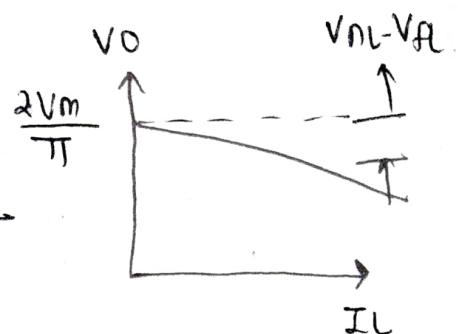
b) switching Regulator:



Line regulation  $\rightarrow$  change in O/P vtg. for a change in line supply  
vtg. Keeping IL & temp constant.

$$\text{Line reg.} = \frac{\Delta V_o}{\Delta V_i} \quad |_{I_L = \text{const.}}$$

$$\text{Load regulation} = \frac{V_{no\text{-load}} - V_{full\text{ load}}}{V_{full\text{ load}}}.$$



### \* IC vtg. Regulators:-

- 1) fixed +ve/-ve O/P vtg regulators.
- 2) Adjustable O/P vtg deg.

### Adv. of IC vtg regulators:-

- 1) Versatile
- 2) relatively inexpensive
- 3) programmable O/P
- 4) current/vtg boosting.
- 5) floating operation for high vtg.

### \* fixed vtg. Regulators:-

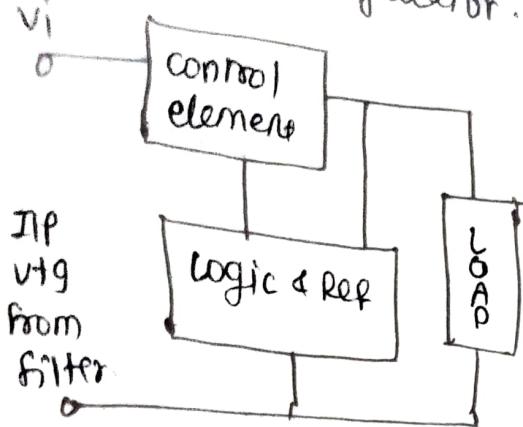
78XX  $\rightarrow$  for 3 terminal +ve fixed vtg. regulators.

7805, 7506, 7808, 7812, 7815, 7818 & 7824.

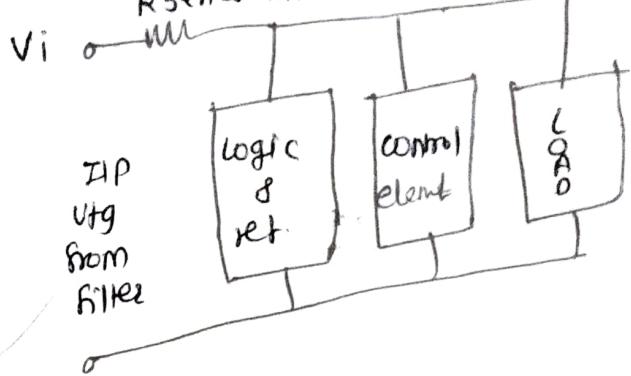
79XX  $\rightarrow$  for 3 terminal -ve fixed vtg. deg.

above 7 + 7.05 - 2V & for -5.2V.

\* Series utg. regulator.



Shunt utg. regulator:  
Rseries-limit



LMPS :

B.D.

