## Multistage Amplifiers

In many applications, a single amplifier cannot provide all the gains that is required to drive a particular kind of load.

- e.g. a loud speaker represents a 'heavy' load (low RL)
in an audio amplifier system & severolamplifier
stages may be required to boost a signal originating
at a microphone or magnetic tape head to a
level suggicient to provide a large amount of power
to the speaker.

if calculate size or bupars rapoulty it

- when the output of one amplifier stage is connected to the input of another amplifier, the amplifier stages are said to be in cascade.

- overall pregyency

- overall suput impedance of a multistage amplifier

= Input impedance of the first stage.

you the amplifier shown below, it is coninced to half power 3 13

Note: 9/p stage is hormally an FET amplifier or Darlington connected BIT amplifier for high i/p impedance applications.

output impedance of the last stage.

power stage or a CE transformer coupled power stage for driving a low resistance load such as

a loudspeaker.  $V_{01} = V_{12}$   $V_{02} = V_{13}$   $V_{03} = V_{13}$   $V_{01} = V_{12}$   $V_{02} = V_{13}$   $V_{03} = V_{13$ 

Voltage Gain of a multistage Amplifier .by there are three stages cancaded,  $\frac{v_i}{v_i} = \frac{v_0}{v_{i_1}} = \frac{\lambda v_3 \cdot v_{i_3}}{v_{i_1}}$ - Aug. Voz - Avg. Avz. Viz The el an amplikien of concase is to provide the palle of the AU = AU, AUZ, AU3 ? Partion An eddition the middle stages provide such other printe out of us on the zit Rs Where Au = 9 m, (20,11212) Au = 9m2 (Zo211 Zi3) - 1 Albino Aug = 9mg (Zolik) source ce contiduction for mideral \* Advantages of cascaded multistage Amplifiers :-O control over the Input Impedance : 2 3991 The ilp stage is usually required to provide a high ilp resistance in order to avoid loss of signal rever when the amplifier is jed from a high resistance voltage source. puilques ramagement (8) In a differential amplifier, the ilp stage must also provide large common-mode rejection. 2) control over the output impedance:-The main junction of the last (ie olp) stage of a amplifier is to provide a low olp resistance (if olp is a vtg signal) in order to avoid loss of gain when a low valued load resistance is connected to the amplifie Also, the olp stage should be able to supply the current required by the load in an excisiont manner. ie without dissipating an unduly large amount of power in the olp transistors.

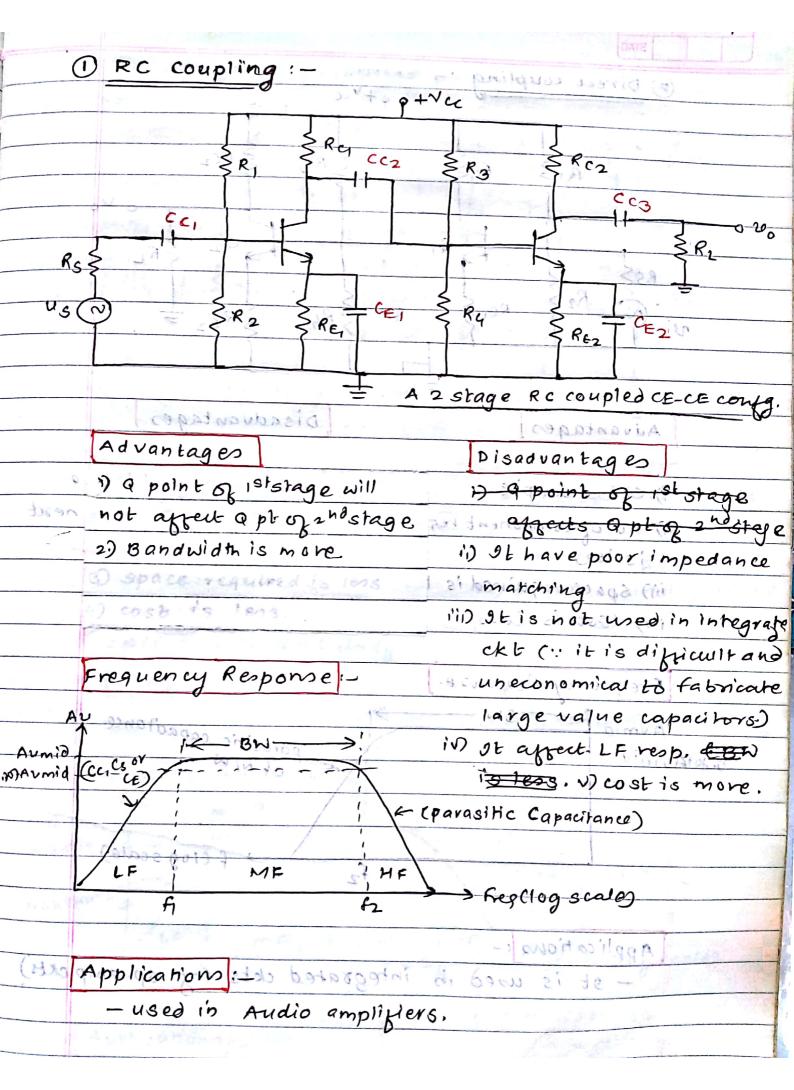
- (a) controlling the voltage gain, Av = Au, Av2, Av3,...

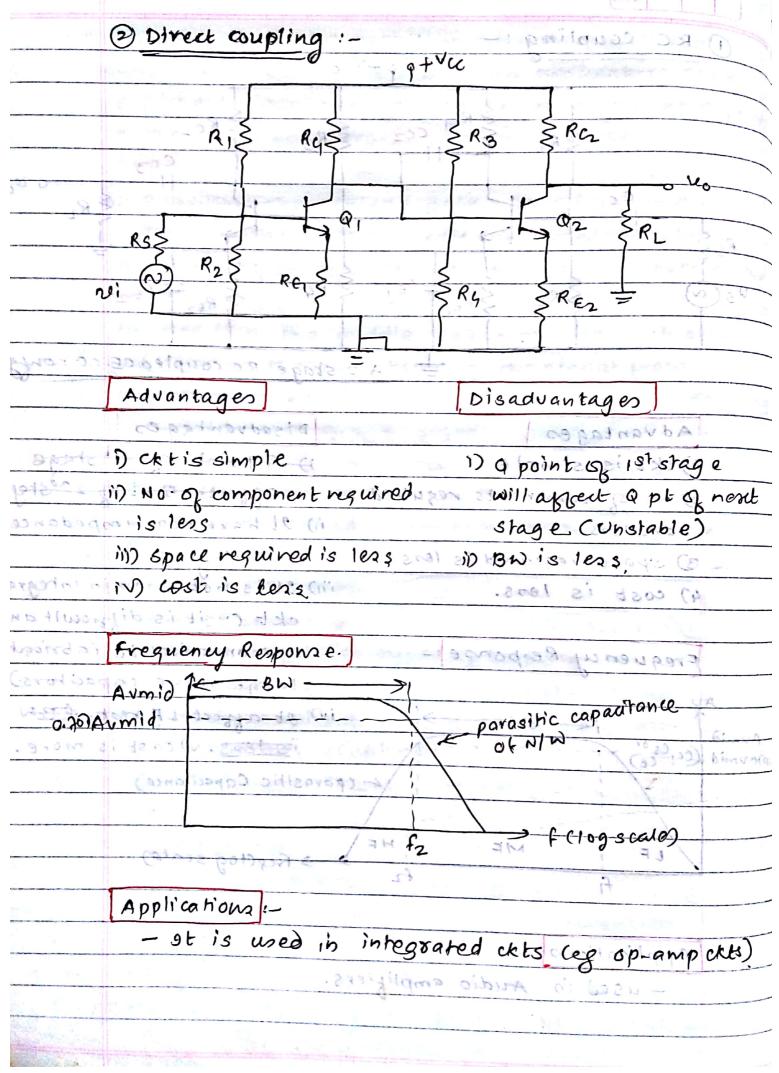
  The function of the middle (intermediate) stages

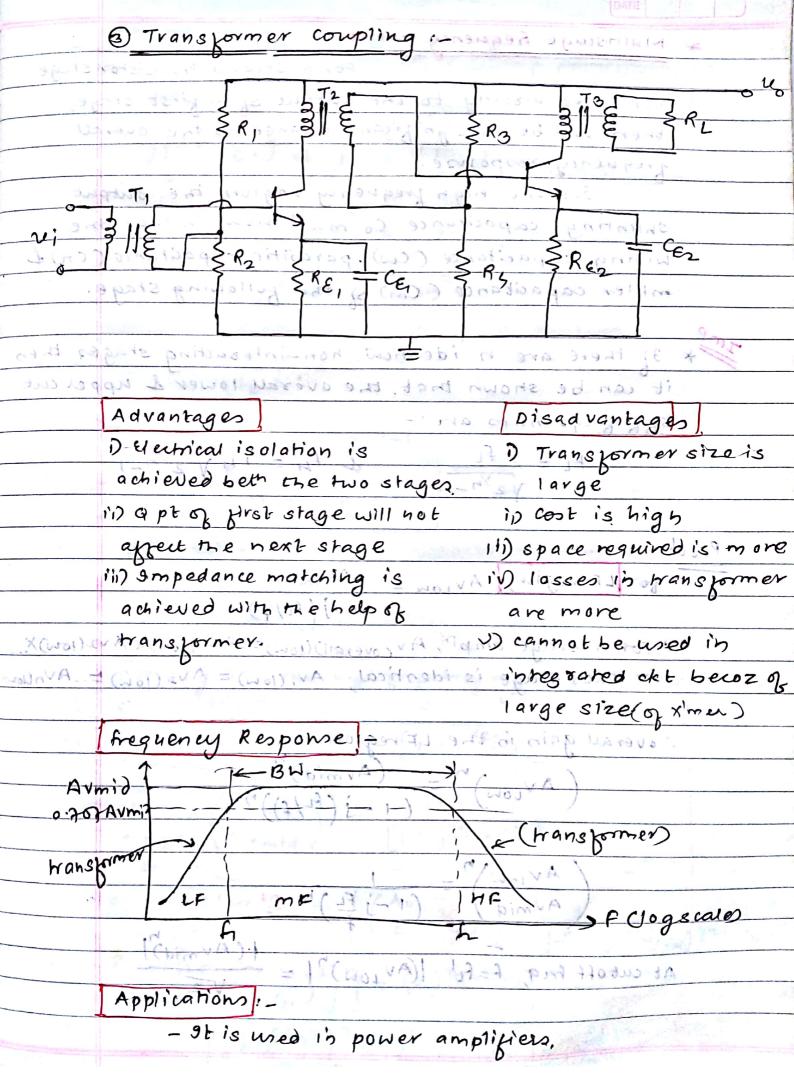
  of an amplifier cancade is to provide the bulk of the voltage gain,

  In addition, the middle stages provide such other functions as the conversion of the signal from differential mode to single ended mode & the shifting of the de level of the signal.
  - @ controlling the bandwidth 1eg ct-cB or cs-ca configuration for wideband
    grequency applications.
  - \* Types of coupling: ware say so long to the types of coupling to the types of coupling to the coupling to t
- sometime Direct resuplings remained & make town
  - denne sporte alimenta resplique lating sylph a ne

abo provide large common mode rejection.







*	Multistage frequency Effects -
al	For a second transistor stage
3	connected disectly to the output of a first stage
77	there will be a significant change in the overall
	frequency response,
	In the high prequency region, the output
	shunting capacitance Co must now include the
	wining capacitance (cw), parasine capacitance (cn) &
	miller capacitance (Cm) of the following stage.
0	
******	97 there are n identical non-interacting stages, thes
	it can be shown that, the overall lower a upper cut
	off frequencies are -
sizo is	The state out and many which was a series of the state out and many which we have the series of the
	12 mary 12/n-10pate out and made of side of
	me de la frestade mill not in cost is high
mo on Pro	- affect the one stage : me - it spare material
round's 4	for LF region, AVLOW 21 AVMIGN DODG (1)
6	achieved m(4/47) it off of
di k	:. For h-stage amply, Av (overall) (10W) = AVILIOW) X AVZLOW)X
le zonad	each stage is identical AV, (10 W) = AV2 (10 W) = AValle
1.5	The street of th
	. : overall gain in the LF region noger prossession
	$(AV_{LOW})^{n} = (AV_{mid})^{n}$
	(1-j(f2/f))h
	(noundsuper)
	/ Aviow / _ 1
also	AVION M = 1 AVmid 1 (1-j FL) bim
	At cut off freq, $f = f(1)  (Av_{LOW})^n  = \frac{ (Av_{mid}) }{\sqrt{2}}$
	- constavilga A
The state of the s	- It is uned in power amplificas.

$$|A \times I \otimes W|^{\frac{1}{N}}| = \frac{1}{\sqrt{2}} \left[ \sqrt{1 + \left(\frac{f_{1}}{f_{1}}\right)^{2}} \right]^{\frac{1}{N}}$$

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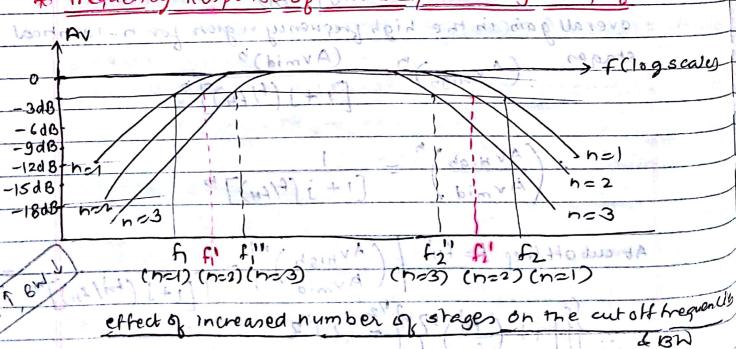
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	3	0.5)	0.51 FH	1.96Fy	

For an identical two stage non-interacting amplifier the upper cutoff preg becomes 64% for a single stage while the lower cutoff freg becomes 1.56 fz. for n=3, upper out of gres is approx, 50% of a single stage while fil becomes almost twice the single stage similarly for HE region sections value.

to frequency Response of cancaded/multistage amplifiers:-

bimVA



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