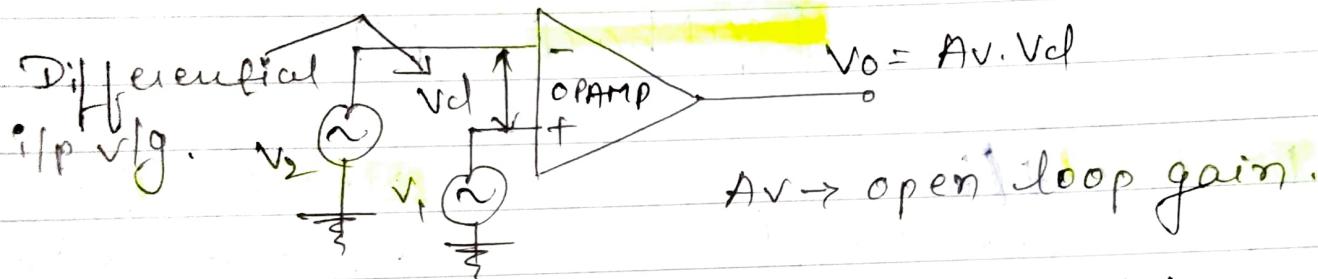


- Open loop configuration
- > It means there is no feedback present from output to the i/p, ie. no part of output is connected to the i/p.



OPAMP in Open loop Configuration.

- V_1 is applied to non-inverting terminal & V_2 is applied to inverting terminal.
- V_d is the differential i/p to the opamp $= V_1 - V_2$.
- A_v is the open loop gain & it is very high.
clip vlg is given by, $V_o = A_v \cdot V_d = A_v(V_1 - V_2)$.
 $= \pm V_{sat}$.

- because maximum value of $V_o = \pm V_{sat}$.
- Depending upon polarity of i/p ie. ' V_d ', o/p will change from $\pm V_{sat}$ or $-V_{sat}$.
- As the value of ' A_v ' is very high, very small value of V_d will drive opamp into $\pm V_{sat}$. So ' V_o ' is not proportional to ' V_d '. Open loop configuration is not used for linear appn.

[i.e. → that is, Appn → Application]

→ Features of Open loop config.

No feedback, High R_i , Low R_o , Very high A_v ,
large Bandwidth, Can't used as linear amp.

→ Closed loop configuration of OPAMP.

In this feedback is given to the i/p from its output. ie. Some part of o/p is given to the i/p.

Feedback (f/b)

Positive f/b

i.e. regenerative f/b.

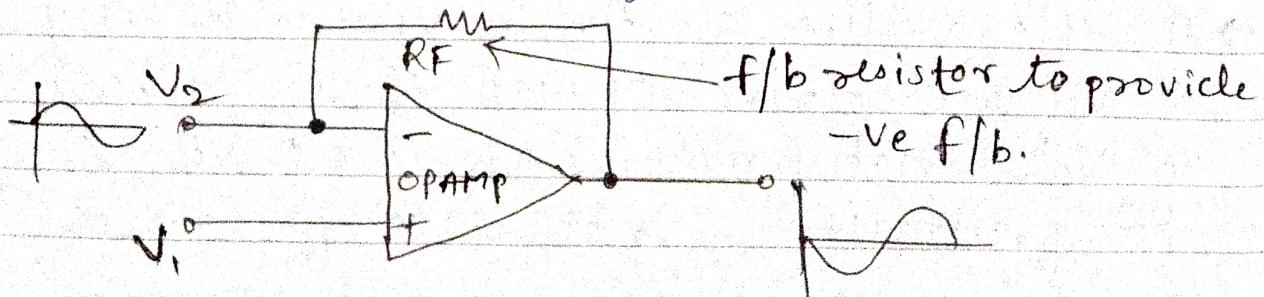
Negative f/b

i.e. Degenerative f/b.

- **+ve f/b** → When i/p original sig & f/b sig are in phase with each other then it is called '**+ve f/b**'. Eg. in app such as Oscillator, Schmitt trigger.
- **-ve f/b** → When feedback sig & original sig are 180° out of phase then it is called as '**-ve f/b**'.

→ In OPAMP as an amp feedback resistor 'RF' is connected between opamp & inverting i/p to introduce -ve feedback.

As opamp & i/p are 180° out of phase it is -ve f/b.



[$\text{Sig} \rightarrow \text{Signal}$, $f/b \rightarrow \text{feedback}$, $\text{Amp} \rightarrow \text{amplifier}$]

- Advantages of -ve f/b. (Why -ve f/b is used?)
- R_i is more & R_o is less.
 - It reduces distortion.
 - It increases Bandwidth.
 - It reduces & stabilizes gain.
 - Reduces effect of variations in temperature & supply voltage on the o/p of OPAMP.

→ Comparison of Open loop & Closed loop Config.

	<u>Open loop Config.</u>	<u>Closed loop Config.</u>
Type of f/b.	No feed back	+ve or -ve f/b.
V/g gain	Very high	low
R_i	Very high	Depends on ckt
R_o	Low	Very low
Bandwidth	High	Very high
Drawback	Waveform Distortion	Low gain.
Appn?	Comparator	Linear amp, osc etc

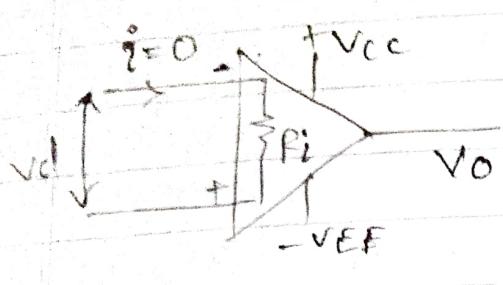
→ Virtual short & Virtual ground Concept.

It means the potential difference betⁿ two i/p terminal of OPAMP is zero. i.e. both the i/p have the same potential.

➤ Virtual short

Ideally i/p Resistance of OPAMP is ' ∞ '. So current flowing from one terminal to other will be zero. \therefore v/g drop across ' R_i ' will be zero & both i/p will be at same potential means virtually shorted.

[Distortion \rightarrow Distortion, Osc \rightarrow oscillator, amp \rightarrow amplifier]



$$V_o = A V_d \quad \text{and} \quad V_d = V_o / A$$

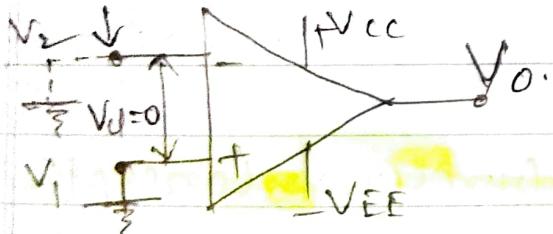
But ideally $V_d = 0$ i.e. $V_1 = V_2$
 $\therefore V_1 = V_2$

> Virtual Ground

- > If non-inverting terminal is connected to ground then because of the 'Virtual short' concept as we have above i.e. $V_1 = V_2$, the inverting terminal will also be at ground potential.
- > Thus actually inverting terminal is not connected to ground but the potential is zero. It is said to be Virtual ground.

It can be also possible if inverting terminal is connected.

Virtual Ground



$$V_d = V_1 - V_2 = 0 \quad \text{--- Virtual short.}$$

& here $V_1 = 0$
 $\therefore V_2 = 0$ as $V_1 = V_2$

OPAMP

Open loop Config.

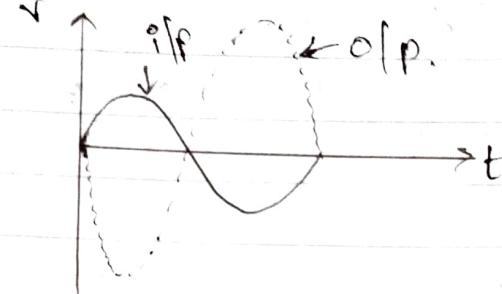
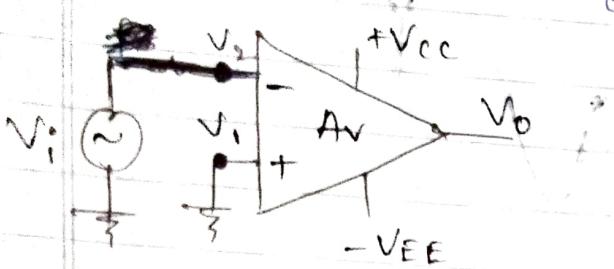
- Inverting amp
- Non Inverting amp
- Differential amp

Closed loop config.

- Inverting amp
- Non Inv. amp
- Differential amp.

Open loop amplifiers.

i) Inverting amp \rightarrow I/P is applied to Inv. terminal & other terminal is connected to ground.

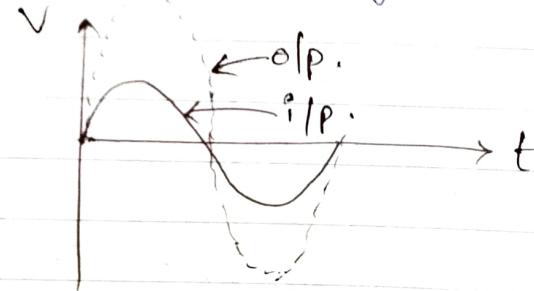
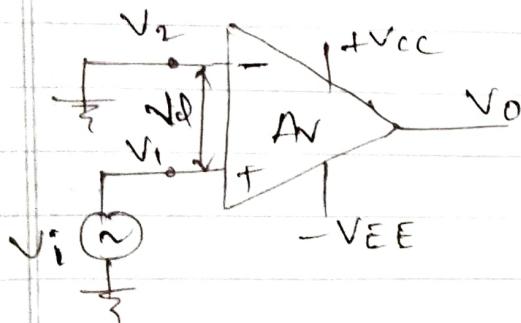


$$V_o = A_v \cdot V_d = A_v (V_1 - V_2) \text{ but } V_2 = V_i \text{ & } V_1 = 0.$$

$$\therefore V_o = A_v (-V_i) \text{ ie. } V_o = -A_v \cdot V_i$$

The -ve sign indicates that o/p is 180° out of phase.
ie. o/p is inverted \therefore known as inverting amp.

ii) Non Inverting amp \rightarrow I/P is applied to Non inv. & Inv. terminal is connected to ground.

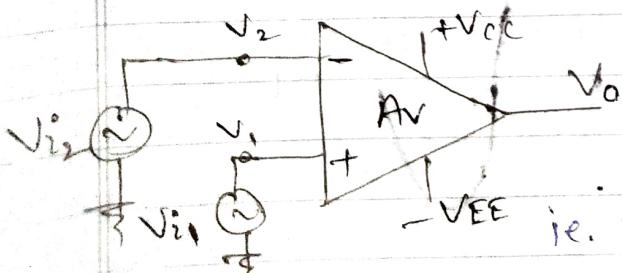


$$\text{Here } V_o = A_v \cdot V_d = A_v (V_1 - V_2) \text{ but } V_1 = V_i \text{ & } V_2 = 0.$$

$$\therefore V_o = A_v (V_i) = A_v \cdot V_i$$

Thus there is no phase shift between i/p & o/p. \therefore
known as non-inverting amplifier.

iii) Differential amp \rightarrow I/P if applied to both terminal



$$V_o = A_v \cdot V_d = A_v (V_1 - V_2)$$

$$\text{but } V_1 = V_{i1} \text{ & } V_2 = V_{i2}$$

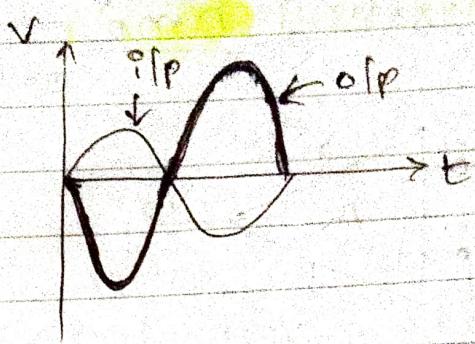
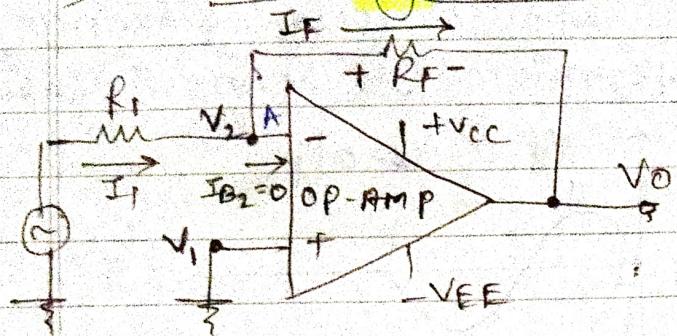
$$\therefore V_o = A_v (V_{i1} - V_{i2})$$

ie. V_o is depends on both i/p.

[amp \rightarrow amplifier]

→ Closed loop amplifiers.

i) Inverting amp \rightarrow I/p applied to Inv. terminal.



Applying KCL at point A,

$$I_1 = I_{B2} + I_F \rightarrow ①$$

But $I_{B2} = 0$ --- since $R_i = \infty$ ie. current flowing to OPAMP is zero.

$$\therefore I_1 = I_F \rightarrow ②$$

By ohm's law $I = V/R$

$$\therefore I_1 = \frac{V_i - V_2}{R_1} \quad \& \quad I_F = \frac{V_2 - V_o}{R_F}$$

$$\therefore \frac{V_i - V_2}{R_1} = \frac{V_2 - V_o}{R_F}$$

But $V_2 = 0$ --- virtual ground concept.

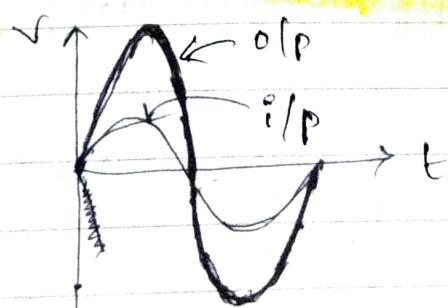
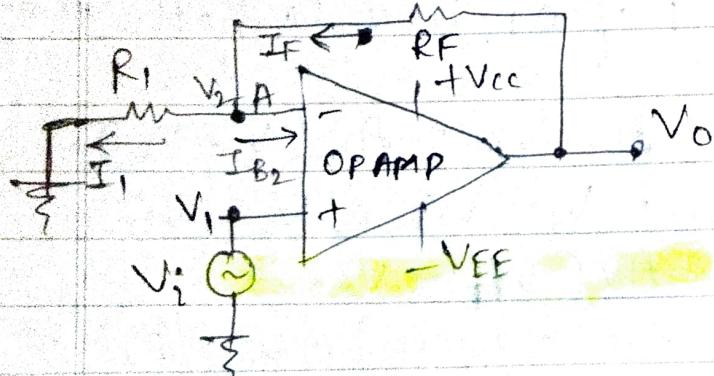
$$\therefore \frac{V_i}{R_1} = -\frac{V_o}{R_F}$$

$$\therefore V_o = \left(-\frac{R_F}{R_1} \right) V_i$$

shows $\frac{R_F}{R_1}$.

180° phase shift

(ii) Non Inverting amp \rightarrow I/p is applied to Non Inv.



Applying KCL at point A,

$$I_1 + I_{B2} = I_F \rightarrow ①$$

But $I_{B2} = 0$ since $R_i = \infty$.

$$\therefore I_1 = I_F \rightarrow ②$$

$$I_F = \frac{V_2 - 0}{R_1} \quad \& \quad I_F = \frac{V_o - V_2}{R_f}$$

$$\therefore \frac{V_2 - 0}{R_1} = \frac{V_o - V_2}{R_f}$$

But $V_2 = V_i$ since virtually short
 $\therefore V_1 = V_2 \& V_1 = V_i$

$$\therefore \frac{V_2}{R_1} = \frac{V_o - V_i}{R_f}$$

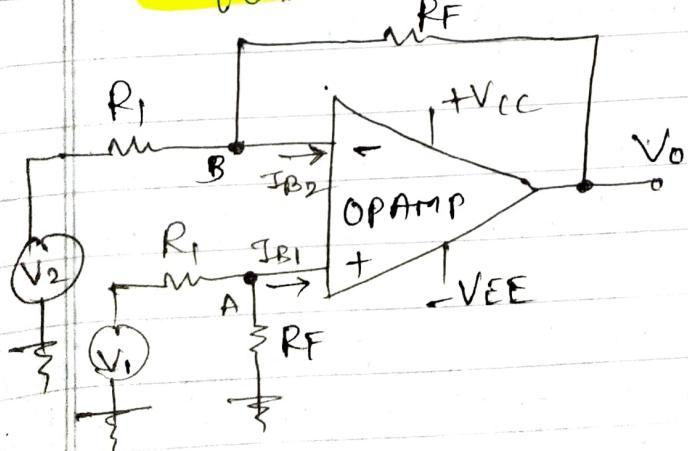
$$\therefore R_f \cdot V_i = R_1 \cdot V_o - R_1 \cdot V_i$$

$$\therefore V_o = \frac{R_1 V_i + R_f \cdot V_i}{R_1}$$

$$\therefore V_o = V_i \left(\frac{R_1 + R_f}{R_1} \right)$$

$$\therefore V_o = \left(1 + \frac{R_f}{R_1} \right) V_i$$

(iii) Differential Amp \rightarrow I/P is applied to both the i/p's.



Case 1 when $V_1 = 0$ i.e. grounded.

\therefore Inverting amp is formed.

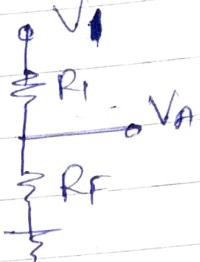
$$\therefore V_{O2} = \left(-\frac{R_f}{R_1} \right) \cdot V_2 \rightarrow ①$$

Case 2 When $V_2 = 0$ i.e. grounded.
 : Non-inverting amp is formed.
 $\therefore V_{O1} = \left(1 + \frac{R_f}{R_i}\right) \cdot V_A \rightarrow \textcircled{2}$.

By voltage divider rule,

$$V_A = \frac{R_f}{R_i + R_f} \cdot V_1$$

\therefore equation $\textcircled{2}$ becomes,



$$V_{O1} = \left(1 + \frac{R_f}{R_i}\right) \cdot \left(\frac{R_f}{R_i + R_f}\right) \cdot V_1$$

$$= \left(\frac{R_i + R_f}{R_i}\right) \left(\frac{R_f}{R_i + R_f}\right) V_1$$

$$V_{O1} = \left(\frac{R_f}{R_i}\right) V_1 \rightarrow \textcircled{3}$$

Now adding both the o/p. i.e. $V_o = V_{O1} + V_{O2}$

$$\therefore V_o = V_{O1} + V_{O2} = \frac{R_f}{R_i} V_1 - \frac{R_f}{R_i} V_2$$

$$\therefore V_o = V_{O1} + V_{O2} = \frac{R_f}{R_i} (V_1 - V_2)$$

★ Comparison

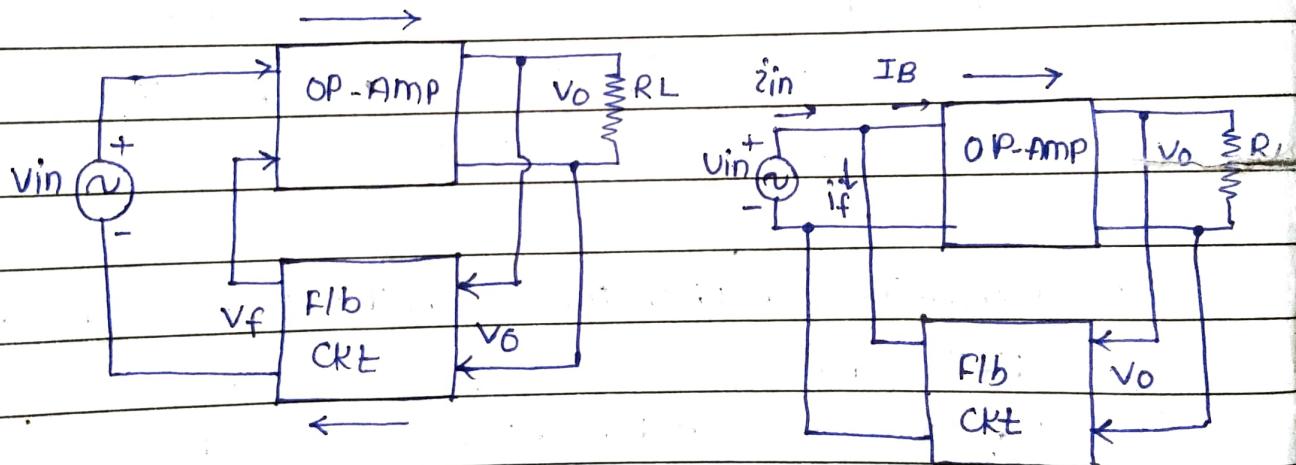
Inverting Amp Non-Inv. Amp

- V/g gain. $A_{VF} = -R_f/R_i$ $A_{VF} = 1 + R_f/R_i$
- Phase relation 180° out of phase. In phase, bet'n o/p & o/p.
- Value of gain. Can be more than, less than or equal to unity Always greater than or equal to unity.

| bet'n \rightarrow between |

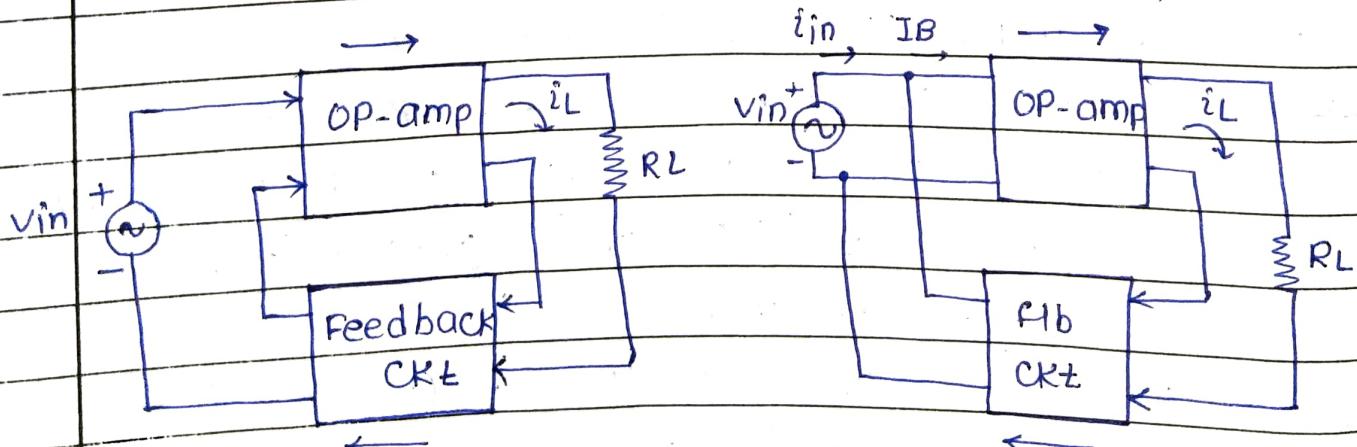
→ Op-Amp with f/b :- closed loop configuration:

- An op-amp that uses f/b is called a feedback amp.
- Also referred as a closed loop amp. because the f/b forms a closed loop betn the IIP & the OIP.
- It consists of two parts: an op-amp & a f/b ckt.
- There are 4 ways to connect these two parts as
 - voltage-series f/b
 - voltage-shunt f/b
 - current-series f/b
 - current-shunt f/b.



a) voltage-series f/b.

b) voltage-shunt f/b.



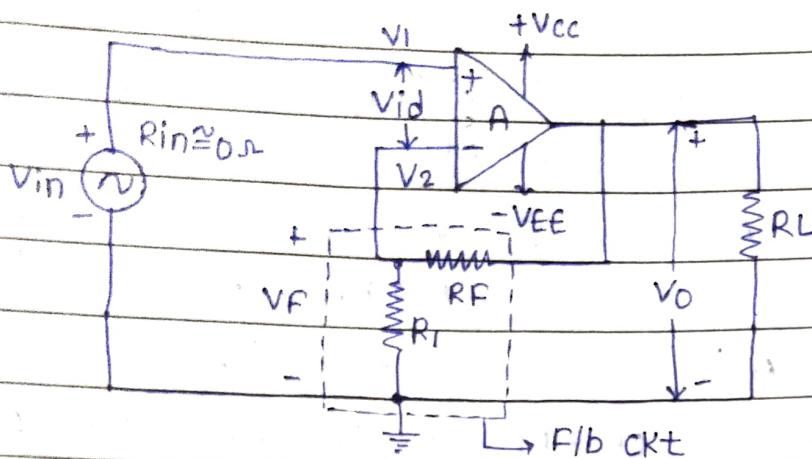
c) current-series f/b

d) current-shunt

→ Arrow indicates the signal flow direction.

A)

Voltage series F/b comp^r (non-inverting amp^r with A/fb) :→



— non-inv amp^r with f/b because it uses f/b & the IIP is applied at the non-inv terminal of op-amp.

— open-loop utg. gain (gain without f/b) $A = v_o / v_{id}$

— closed-loop utg. gain (gain with f/b) $|AF| = v_o / v_{in}$

— gain of the f/b ckt. $B = v_F / v_o$

→ Negative f/b :

— Apply KVL to the IIP loop ($v_{in} - v_{id} - v_F = 0$)

$$\therefore v_{id} = v_{in} - v_F$$

where v_{in} = IIP voltage

v_F = feedback utg.

v_{id} = difference IIP utg.

— The f/b utg. always opposes the IIP voltage (or is out of phase by 180° wrt the IIP voltage). Hence the f/b is said to be negative.

— Closed loop utg. gain :-

we know that, $A_{f0} = v_o / v_{id}$

$$AF = \frac{v_o}{v_{in}} \quad \& \quad v_o = A(v_1 - v_2)$$

from fig. we see that,

$$v_1 = v_{in}$$

$$v_2 = v_F = \frac{R_1 \cdot v_o}{R_1 + R_F} \quad \text{since } R_i \gg R_s$$

$$V_o + \frac{A R_1 V_o}{R_1 + R_F} = \frac{A(R_1 + R_F)V_{in}}{R_1 + R_F} \Rightarrow V_o \left[1 + \frac{A R_1}{R_1 + R_F} \right] = \frac{A(R_1 + R_F)V_{in}}{R_1 + R_F}$$

START WRITING HERE $\frac{V_o}{V_{in}} = \frac{A(R_1 + R_F)V_{in}}{R_1 + R_F + A R_1}$

$$\therefore V_o = A_v \left(V_{in} - \frac{R_1 V_o}{R_1 + R_F} \right) = A \left[\frac{(R_1 + R_F)V_{in} - R_1 V_o}{R_1 + R_F} \right]$$

i.e. $V_o = \frac{A_v (R_1 + R_F) \cdot V_{in}}{R_1 + R_F + A R_1}$

Thus, $A_F = \frac{V_o}{V_{in}} = \frac{A_v (R_1 + R_F)}{R_1 + R_F + A R_1}$ --- (1)

Generally A_v is very large (2×10^5).

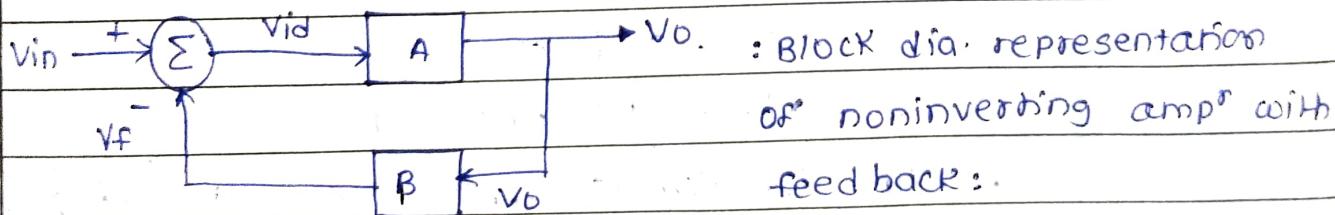
$$\therefore A R_1 \gg (R_1 + R_F) \quad \& \quad (R_1 + R_F + A R_1) \cong A R_1$$

Thus, $A_F = \frac{V_o}{V_{in}} = 1 + \frac{R_F}{R_1}$ --- (2)

- External component values should be less than $1\text{m}\Omega$ so that they do not adversely affect the internal circuitry of the op-amp.

- Gain of ffb ckt (B) = $\frac{V_F}{V_o} = \frac{R_1}{R_1 + R_F}$

- for given R_1 & R_F the values of A_F & B are fixed.



- The closed loop gain A_F can be expressed as in terms of open loop gain A & ffb ckt gain B as.

$$A_F = \frac{A_v \left(\frac{R_1 + R_F}{R_1 + R_F + A R_1} \right)}{\frac{R_1 + R_F}{R_1 + R_F} + \frac{A R_1}{R_1 + R_F}}$$

(from eqn (1))

$$\therefore A_F = \frac{A_v}{1 + A B}$$

where. A_F = closed-loop vfg. gain

A_o = open-loop vfg. gain

B = gain of the fb ckt.

AB = loop gain.

⇒ difference IIP vfg. is ideally zero (i.e. $v_{id} = 0$).

we know that ? $A_o = \frac{v_o}{v_{id}}$

$$\text{i.e. } v_{id} = A_o \frac{v_o}{A_o} \rightarrow ①$$

AS A is very large, $v_{id} \approx 0$.

that means $v_1 \approx v_2 \rightarrow ②$

from fig. of the non-inv FB amp.

$$v_1 = v_{in} \text{ & } v_2 = v_{id} + v_F \rightarrow ③$$

$$v_F = \frac{R_1 v_o}{R_1 + R_F} \rightarrow ④$$

$$\therefore v_{in} = \frac{R_1 v_o}{R_1 + R_F}$$

$$\text{i.e. } \boxed{A_F = \frac{v_o}{v_{in}} = 1 + \frac{R_F}{R_1}} \quad \begin{array}{l} \text{- vfg. gain of non-inv} \\ \text{with fb.} \end{array}$$

⇒ IIP resi. with FB (R_{IF}) :-

$$R_{IF} = \frac{v_{in}}{i_{in}} = \frac{v_{in}}{v_{id}/R_i}$$

$$\text{But } v_{id} = \frac{v_o}{A_o} \text{ & } v_o = \frac{A_o}{1+AB} \cdot v_{in} \quad \left(\text{As } \frac{v_o}{v_{in}} = A_{IF} = A_o \right)$$

$$\therefore R_{IF} = R_i \cdot \frac{v_{in}}{v_o/A_o}$$

$$= \frac{A_o R_i \cdot v_{in}}{A_o v_{in} / (1+AB)}$$

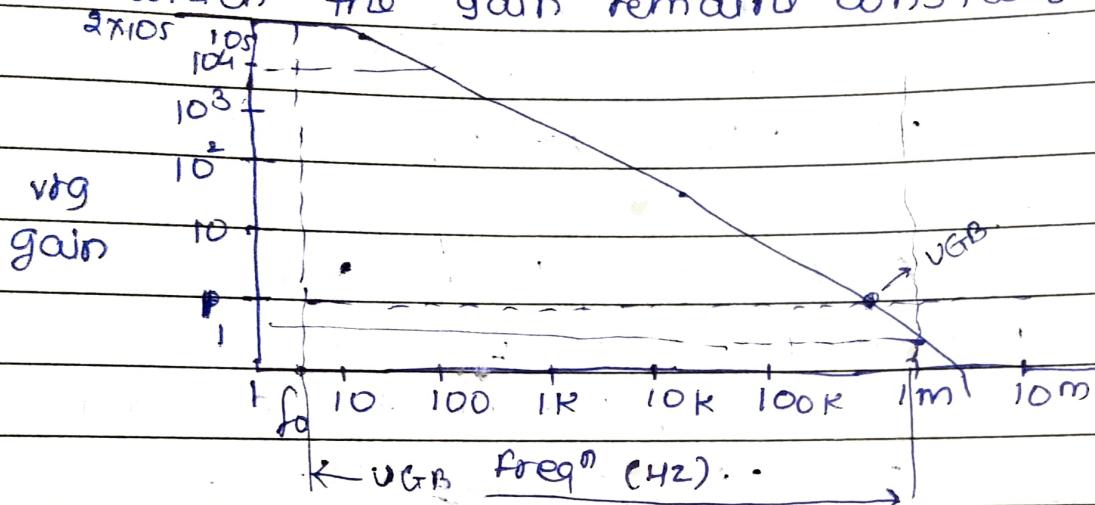
$$R_{IF} = R_i (1+AB) \quad | \quad R_i - IIP \text{ resi without FB.}$$

⇒ OIP resistance with FIB (ROF) :-

$$ROF = \frac{R_o}{1+AB}$$

⇒ B/W with FIB :-

- B/W of an amp^r is the band (range) of freqⁿ for which the gain remains constant.



- Break freqⁿ :- freqⁿ at which the gain A is 3dB down from its value at 0Hz & denoted by f_0 .

- unity gain B/W :- The freqⁿ at which the gain equals 1 is known as the unity gain B/W.

$$UGB = (A_v) \cdot (f_0) \quad \text{--- (1)}$$

where A_v = Open-loop vrg. gain

f_0 = break freqⁿ of an op-amp.

$$UGB = A_v F \cdot f_F \quad \text{--- (2)}$$

where A_v = closed-loop vrg. gain

f_F = bandwidth with FIB.

∴ from eqn (1) & (2)

$$A_v f_0 = A_v F \cdot f_F$$

$$f_F = \frac{A_v f_0}{A_v F} \quad \text{--- (3)}$$

However for the non-inverting amp^r with FIB

$$A_v = \frac{A_o}{1+AB}$$

(Begin answer for each question on a new page)

∴ Substituting value of AF in eqⁿ ③ we get.

$$f_F = (A_B \cdot f_0)$$

$$A_B / (1 + A_B)$$

$$\text{Or } f_E = f_0 (1 + A_B).$$

⇒ Total OIP offset voltage with f_{lb}:

$$V_{OOT} = \frac{\pm V_{sat}}{1 + A_B}$$

⇒ Adv. of -ve f_{lb}:

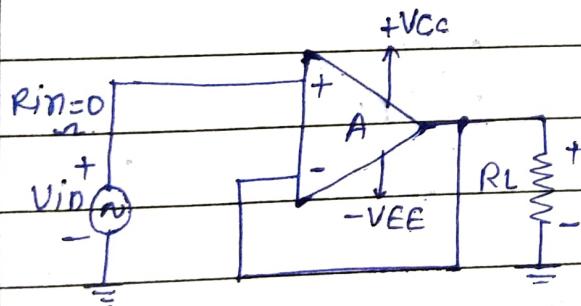
→ It can be used to

- high I_P resistance.
- reduce significantly the effect of noise
- very low OIP_{resi}
- variations in supply voltages &
- stable v_{tg} gain
- changes in temp. on the OIP v_{tg}
- large BIL &
- very little OIP offset v_{tg}.

⇒

V_{tg} follower:

Application:- Used to remove the loading on the 1st n/w when placed betⁿ two n/w's.



- non-inv amp^r with unit gain

- $V_o = V_{in}$ is called as v_{tg}. follower.

$$- B = A_F = 1$$

Ex:

The 741C OP-amp has following parameters if connected as a non-inv. amp^r with $R_1 = 1\text{ k}\Omega$ & $R_F = 10\text{ k}\Omega$.

$$A = 2 \times 10^5 = 200,000, \text{ supply v_{tgs}} = \pm 15\text{V}$$

$$R_i = 2\text{ M}\Omega$$

$$, \text{ OIP v_{tg} swing} = \pm 13\text{V}$$

$$R_o = 75\Omega$$

compute value of AF, R_{iF}, R_{oF}, f_F & V_{OOT}.

$$f_0 \approx 3\text{MHz}$$

$$\beta = \frac{R_1}{R_1 + R_F} = \frac{1K}{1K + 10K} = \frac{1}{11}$$

$$\frac{\beta = V_F}{V_O} = \frac{\left(\frac{R_1}{R_1 + R_F}\right) \cdot V_O}{V_O}$$

$$1 + AB = 1 + \frac{200,000}{11} = 18,182.8 \quad \boxed{\beta = \frac{R_1}{R_1 + R_F}}$$

$$AF = \frac{200,000}{18,182.8} = 10.99$$

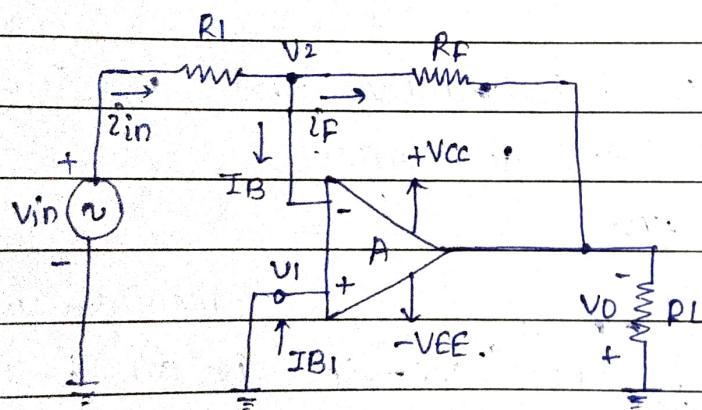
$$R_{IF} = 2m\Omega (18,182.8) = 36.4G\Omega$$

$$R_{OF} = \frac{75\Omega}{18,182.8} = 4.12m\Omega$$

$$f_F = (5\text{Hz}) \cdot (18,182.8) = 90.9\text{kHz}$$

$$V_{OOT} = \pm 13V = \pm 0.715\text{mV}$$

* \Rightarrow Voltage shunt A_{fb} amp: (Inv amp with A_{fb}) :-



\rightarrow Closed loop voltage gain :-

From above fig.

$$i_{in} = i_F + I_B \quad \text{--- (1)}$$

$\because R_1$ is very large, the JFET bias current I_B is negligibly small.

$$\therefore i_{in} \approx i_F \quad \text{--- (2)}$$

$$\text{i.e. } V_{in} - V_2 = \frac{V_2 - V_O}{R_F} \quad \text{--- (3)}$$

(Begin answer for each question on a new page)

$$AS, A = \frac{v_o}{v_{id}}, v_{id} = \frac{v_o}{A} \text{ i.e. } v_1 - v_2 = \frac{v_o}{A}$$

since $v_1 = 0V$, $v_2 = -\frac{v_o}{A}$

\therefore from eqn ③, we get.

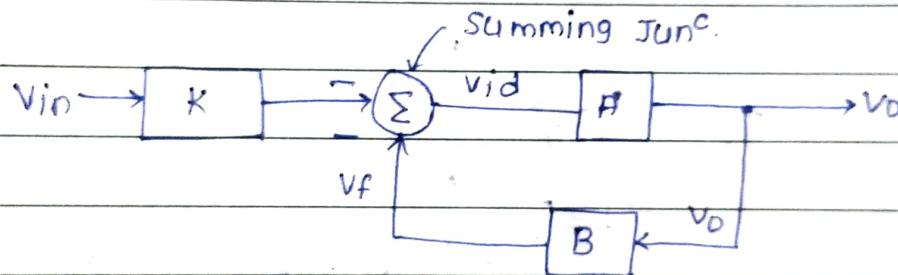
$$\frac{v_{in} + v_o/A}{R_1} = -\frac{(v_o/A) - v_o}{R_F}$$

$$\therefore AF = \frac{v_o}{v_{in}} = -\frac{R_F}{R_1 + R_F + A R_1} \approx A R_1 \quad \text{--- (4)}$$

\rightarrow - sign indicates that the IIP & OIP signals are out of phase by 180° (or of opposite polarities). Hence called inv. amp?

As A is very large (2×10^5), eqn ④ can be written as,

$$AF = \frac{v_o}{v_{in}} = -\frac{R_F}{R_1}$$



: Block dia. of inv. amp with fib using a utg summing jun? :-

\Rightarrow Virtual ground:-

- see the fig. of ~~non~~-inv. amp, non-inv terminal is grounded & the IIP signal is applied to the inv. terminal via R_{in}, R_1 .

- As discussed earlier, since A is very large the eqn becomes

$$v_{id} = \frac{v_o}{A}, v_{id} \approx 0 \quad (\because A \gg)$$

$$\therefore v_1 \approx v_2$$

- Thus as v_1 (non-inv) is gnded, v_2 is also at same potential i.e. $v_2 = 0$

- Therefore the inverting terminal (v_2) is said to be at virtual ground.

→ B/IW with F/b :-

$$f_F = \frac{UGB(1+AB)}{A} = \frac{UGB(K)}{AF}$$

where, $R = RF$ $AF = \frac{AK}{1+AB}$

→ Total OIP offset v_{tg} : v_{OOT} :-

$$v_{OOT} = \frac{\pm v_{sat}}{1+AB}$$
 { same for both inv. & non inv F/b amprs.

where $\pm v_{sat}$ = saturation voltages

A = open-loop v_{tg} · gain

B = gain of F/b ckt. = $\frac{R_I}{R_I + R_F}$

Sol?

we know that, $A_D = 1 + \frac{R_F}{R_1}$.

$$\therefore A_D = 1 + \frac{6.8K}{680\Omega} = 11$$

IIP resi.

$$R_{IFY} = (2m\text{-}A) \cdot \left[1 + \frac{(2)(10^5) \cdot (6.8K\Omega)}{6.8K\Omega + 680\Omega} \right] = 364 \text{ G}\Omega$$

$$R_{IFX} = (2m\text{-}A) \cdot \left[1 + \frac{(2)(10^5) \cdot (680)\Omega}{6.8K\Omega + 680\Omega} \right] = 36.4 \text{ G}\Omega$$

b) OIP vbg. can be calculated by,

$$V_O = \left(1 + \frac{R_F}{R_1} \right) \cdot V_{XY} = (11) \cdot (-1.5 + 2) = 5.5 \text{ Vpp. sine wave at } 1 \text{ kHz.}$$

Practical OP-AMP

- It has some DC OIP vbg. called output offset voltage, even though both inverting & noninverting IIP terminals are grounded.

- we will see the properties of practical op-amp that produce the output offset voltage.

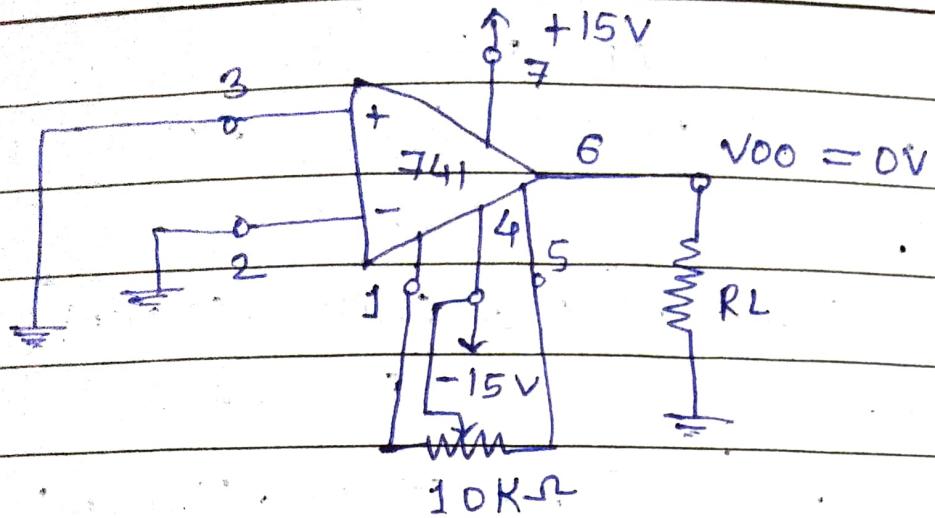
- causes of OIP offset voltage:

1) By mismatching betn two IIP terminals

\Rightarrow To reduce V_{OO} to 0, we need to have a ckt. at the IIP terminals of the op-amp that will give us the flexibility of obtaining V_{IO} of proper amplitude & polarity. This ckt. is called as an IIP offset voltage-compensating nw.

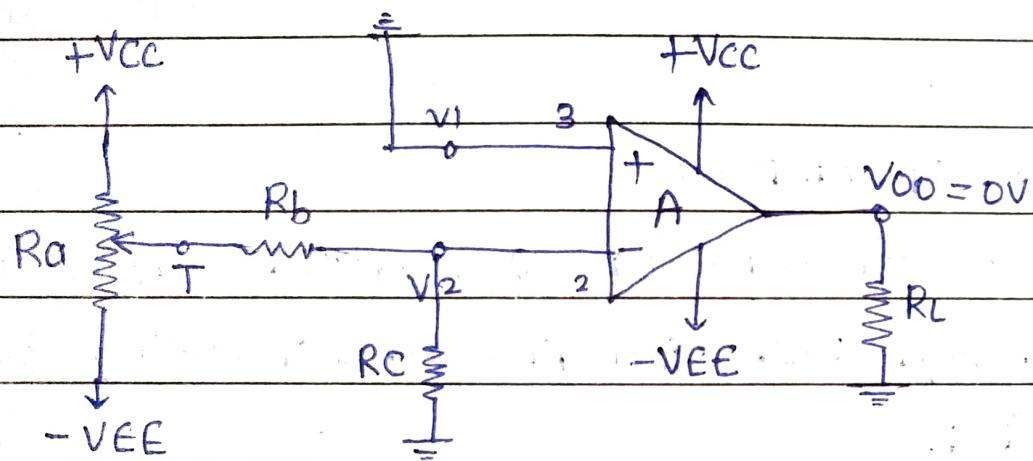
- Before applying external IIP vbg. by using this ckt we make the $V_{OOT} = 0$. The op-amp is then said to be nulled or balanced.

- For 741C these offset null pins have been provided so no external offset voltage-compensating nw is required.



→ By changing the position of the wiper, we can adjust the output voltage.

⇒ Offset v_{tg}, compensating n/w:



→ $v_{tg \text{ a/c }} R_C = V_2 = \text{IIP } v_{tg \text{ a/c }} \text{ inv terminal}$

< above ckt. can be used as a non-inverting amp since the compensating n/w is connected to the IIP terminal of the op-amp.

- Pot. R_a is connected betw $+VCC$ & $-VEE$.

∴ $V_{\text{Voltage a/c }} R_C = V_2$

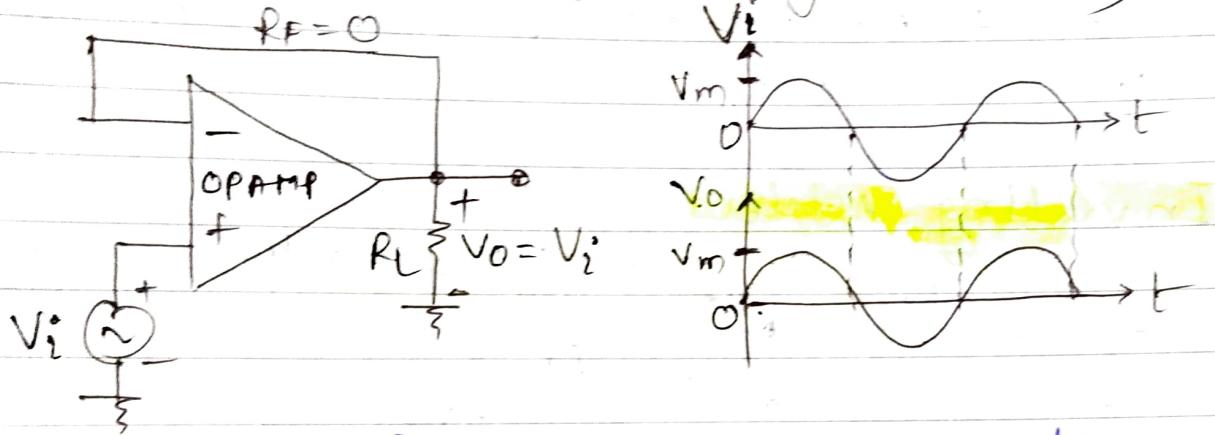
- By adjusting wiper on R_a , V_2 can be made equal to V_I i.e. v_{tg} can be made 0.

Find vlg gain of Inv. & nonInv. amp if $PF = 100k\Omega$
 $R_f = 1k\Omega$, $V_i = 10mV$, $V_{cc} = \pm 12V$.

Solⁿ Inverting mode, $V_o = -PF \cdot V_i = -100 \times 10 \times 10^{-3} = -1V$

Non Inverting mode, $V_o = \left(1 + \frac{P_f}{R_f}\right) \cdot V_i = \left(1 + \frac{100}{1}\right) \times 10 \times 10^{-3} = 1.0V$

→ Voltage Follower (Unity gain Buffer)



When $R_f = \infty$ & $P_f = 0$ then non-inverting amp becomes voltage follower or unity gain amp.

It is configured such that as to obtain gain = 1.

It is obtained by short cktting P_f i.e. $P_f = 0$ & open cktting R_f i.e. $R_f = \infty$. Thus all o/p is fed back to i/p of OPAMP.

$$V_o = AVF \cdot V_i \rightarrow ①$$

& for non-inverting amp $AVF = 1 + \frac{P_f}{R_f}$... but $P_f = 0$ & $R_f = \infty$

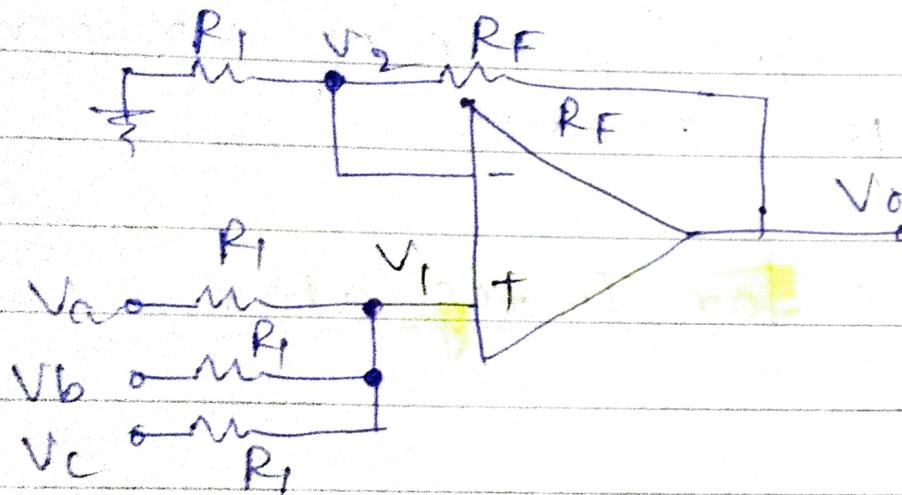
$\therefore AVF = 1$ & from equation ① it is clear that o/p vlg is equal to & in phase with the i/p vlg.

→ Application

- As a Buffer amplifier to avoid loading of source
- For impedance matching.

For non-inverting summing amp having
 if $V_a, V_b, V_c = 1V, 2V, 3V$ & If $R_F = 10k\Omega$, $R_i = 5k\Omega$
 what is o/p vlg. Draw ckt also.

Soln



i) If $V_b = V_c = 0$,

$$V_1' = \frac{R_1 || R_1}{R_1 + (R_1 || R_1)} \times V_a = \frac{R_1 / 2}{R_1 + R_1 / 2} \cdot V_a = \frac{V_a}{3} //$$

ii) If $V_a = V_c = 0$, $V_1'' = \frac{V_b}{3} //$

iii) If $V_a = V_b = 0$, $V_1''' = \frac{V_c}{3} //$

$$AVF = \frac{1 + R_F}{R_i} = 1 + \frac{10}{5} = 3$$

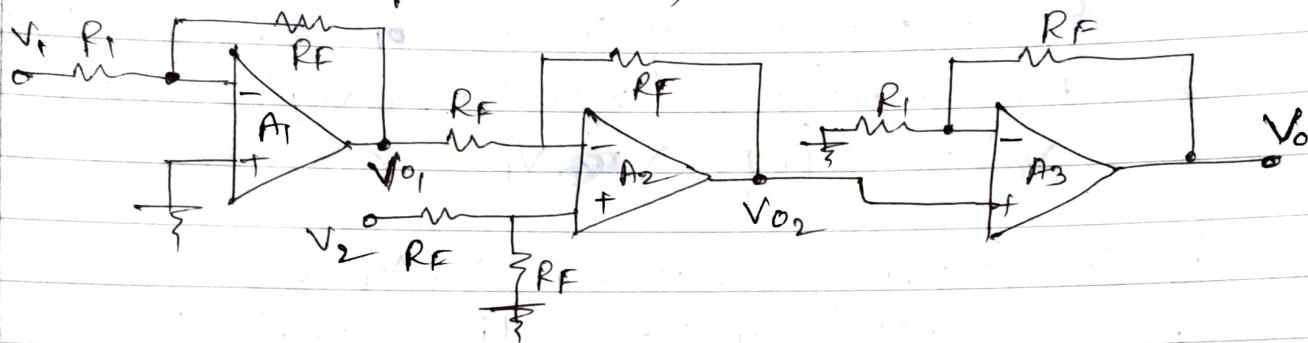
Chintan Jethva

$$V_o = A_{VF} \times V_i \quad \& \quad V_i = V_1' + V_2' + V_3' = \frac{1}{3} (V_a + V_b + V_c)$$

$$V_o = 8 \cdot \frac{1}{3} (V_a + V_b + V_c)$$

$$\therefore V_o = V_a + V_b + V_c = 6V.$$

→ Give o/p of circuit, $R_i = 1K\Omega$, $R_F = 10K\Omega$.



Sol i) $V_{o1} = -\frac{RF}{R_i} \times V_1 = -10V_1$

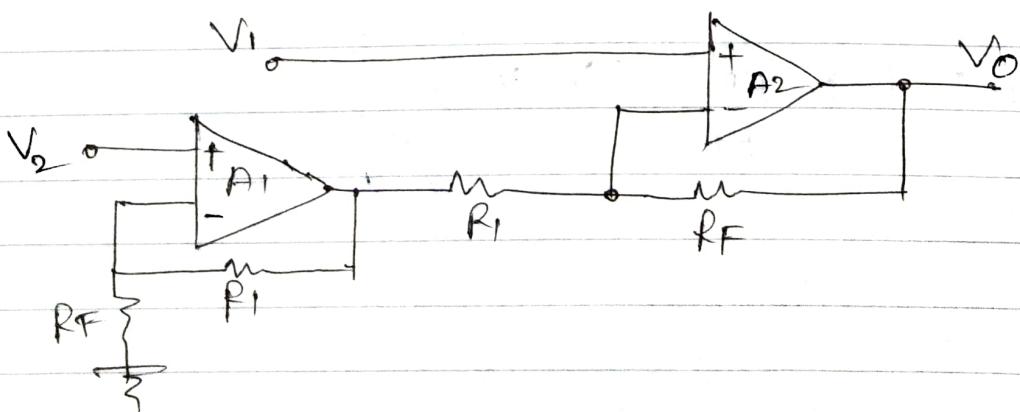
ii) $V_{o2} = V_2 - V_{o1} = V_2 - (-10V_1)$

$$V_{o2} = V_2 + 10V_1$$

iii) $V_o = \left[1 + \frac{RF}{R_i} \right] V_{o2} = \left[1 + \frac{10}{1} \right] (V_2 + 10V_1)$

$\therefore [V_o = 11(V_2 + 10V_1)]$

→ Give expression of o/p vfg.



SOLN → Consider op of A₁ as V_{O1}, which is
acting as non-inv. amp,

$$V_{O1} = \left(1 + \frac{R_{O1}}{R_F}\right) V_0 \quad \rightarrow \textcircled{1}$$

Now for A_2 , two cases, ~~and one~~ ~~one~~ ~~one~~ ~~one~~ ~~one~~ ~~one~~ ~~one~~.

i) Considering only V_1 , ie. $V_{01} = 0$, ie. grounded.

As if it is acting as non-inv. amp,

$$V_o' = \left(1 + \frac{R_F}{R_i} \right) V_1 \rightarrow (2)$$

ii) Considering only V_{o1} , i.e. $V_1 = 0$ is grounded.
As it is acting as inverting amp.

$$V_o'' = \left(\frac{-R_F}{R_1} \right) \cdot V_{o1} \quad \rightarrow (3)$$

$$\rightarrow \text{Also } V_0 = V_0' + V_0''$$

$$V_0 = \left(\frac{1 + R_F}{R_I} \right) V_I - \frac{R_F}{R_I} V_{O1}$$

2 put value of V_0 from eqn (i).

$$\therefore V_0 = \left(\frac{1+RF}{R_1} \right) V_1 - \left[\frac{RF}{R_1} \left(\frac{1+R_1}{RF} \right) V_2 \right]$$

$$= \left(\frac{1+RF}{R_1} \right) V_1 - \left[\left(\frac{RF+R_1}{R_1} \right) V_2 \right]$$

$$V_o = \left(\frac{1 + RF}{R_1} \right) (V_1 - V_2)$$