

## IIVISK A Unit Of Wales Educational Trust Imperial Institute of Management Science & Research

## Bachelor Programme In Electronics Engineering

**Duration: 04 Year** 

Lateral Entry: 03 Year 12,000/Sem

Semester: 1	Semester: 2
<ol> <li>Engineering Maths- I</li> <li>Engineering Physics</li> <li>Elements of Civil Engineering &amp; Engineering Mechanics</li> <li>Basic Electrical Engineering</li> <li>Elements of Mechanical Engineering</li> </ol>	<ul><li>4. Computer Aided Engineering Drawing</li><li>5. Basic Electronics</li></ul>
Semester: 3	Semester: 4
<ol> <li>Engineering Mathematics –III</li> <li>Industrial Electronics</li> <li>Fundamentals of Instrumentation &amp; Control</li> <li>Electromagnetic Engineering</li> <li>Fundamentals of HDL</li> </ol>	<ol> <li>Environmental Studies</li> <li>Digital Electronics &amp; Logic Design</li> <li>Control Systems</li> <li>Signals &amp; Systems</li> <li>Linear IC's &amp; Applications</li> </ol>
Semester: 5	Semester: 6
<ol> <li>Electronic Instruments &amp; Measurement Systems</li> <li>DSP Based Design Systems</li> <li>Analog Communication</li> <li>Microwaves Engineering</li> <li>Fundamentals of CMOS VLSI</li> </ol>	<ol> <li>Embedded System Design</li> <li>Microprocessors &amp; Microcontrollers</li> <li>Microelectronics Circuits</li> <li>Power Electronics Devices &amp; Circuits</li> <li>VLSI for Wireless Networks</li> </ol>
Semester: 7	Semester: 8
1 Advance Analog Signal Processing 2. Computer Communication Net work 3. Algorithm for VLSI Design Automation 4. Antenna & Wave Propagation 5. FuzzyLogic for Emb eddedSystem Application	<ol> <li>Wireless Sensor Networks Stability</li> <li>Digital Switching Systems</li> <li>Power System Analysis and</li> <li>Viva</li> <li>Project Work</li> </ol>