Vigomar Kim Algador

CpE-64 – 05
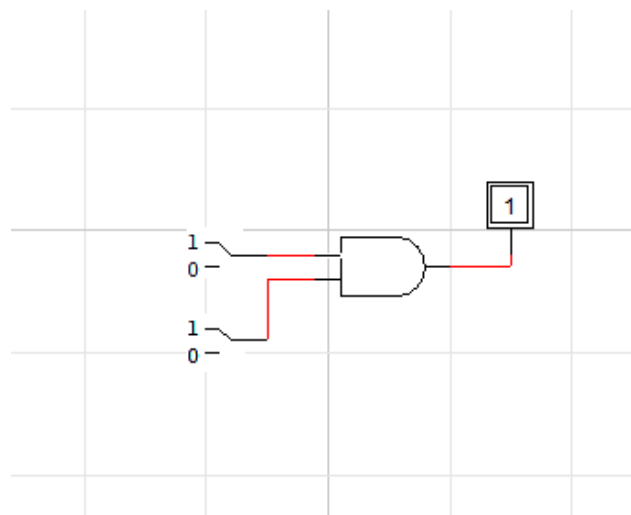
Laboratory 01

10 February 2022

Part 1: Simulation of AND gate with Binary switches and Binary Probe.

Part 2. Simulation for D Flip Flop with a binary switch and Clock.