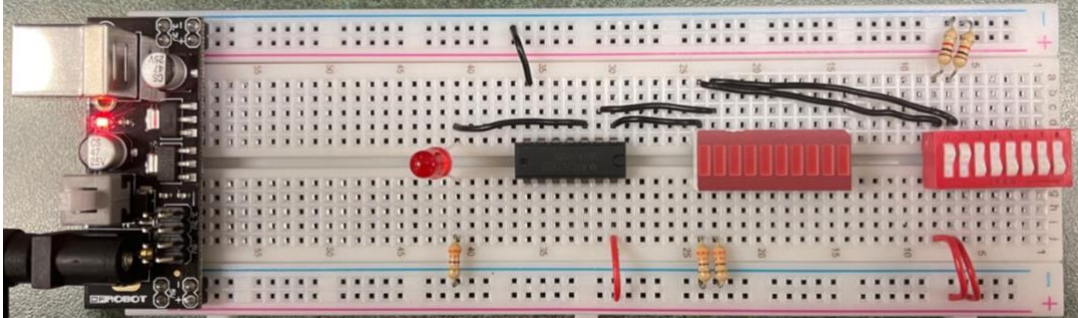
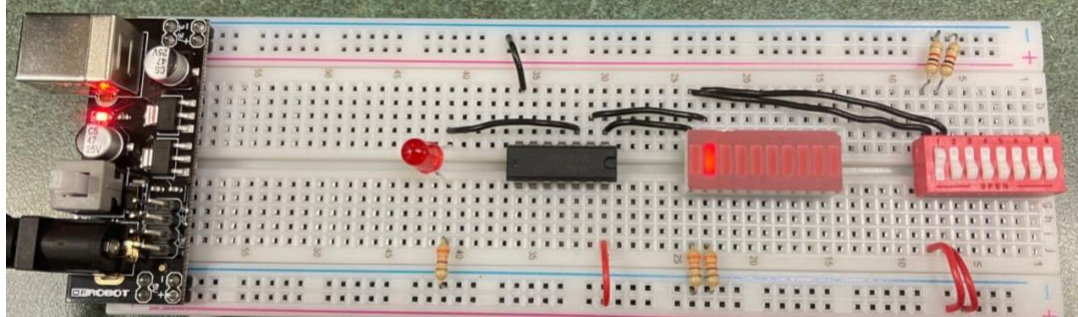
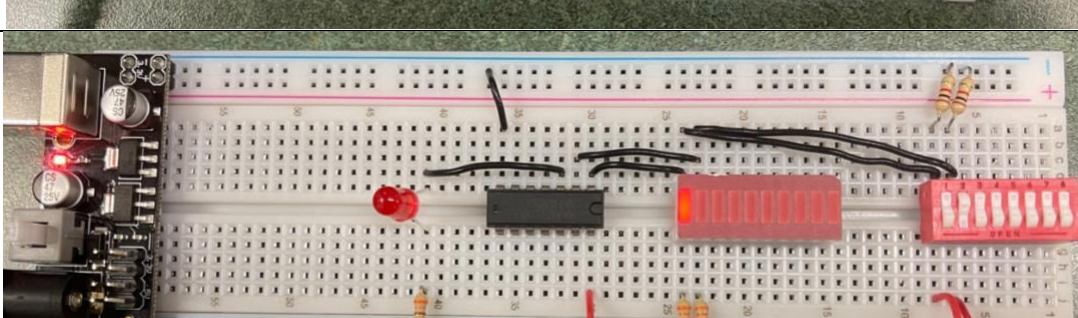
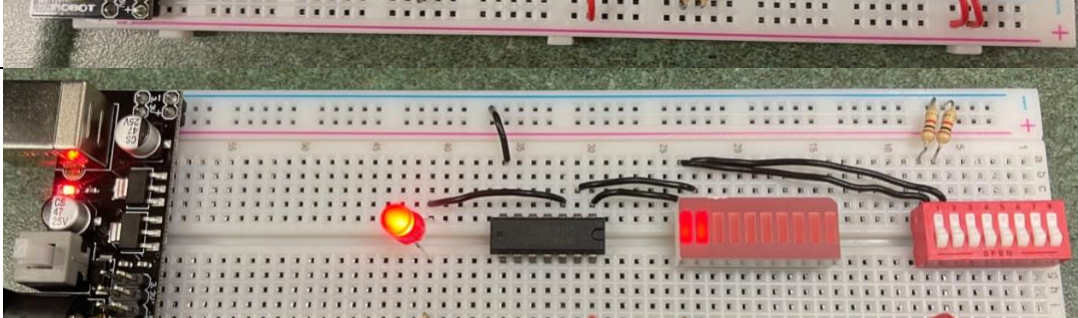


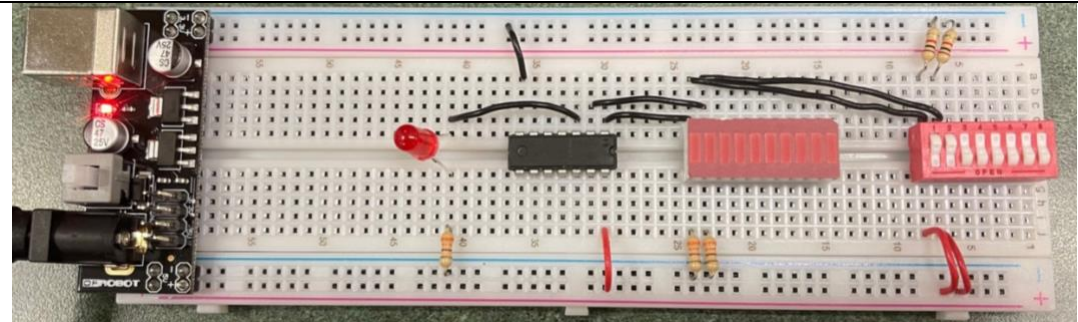
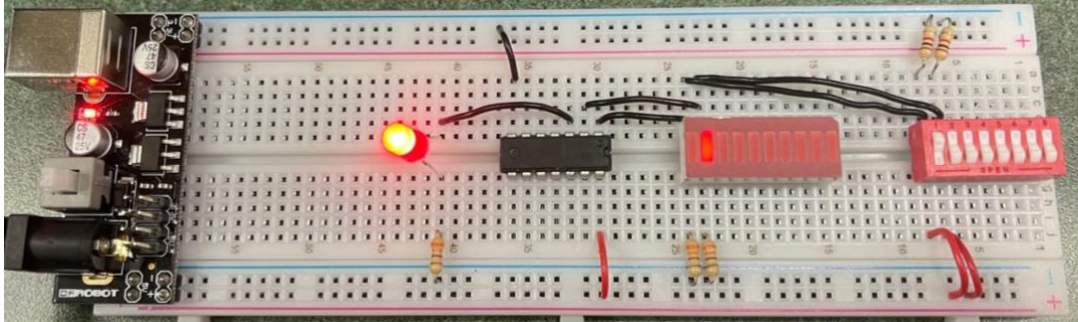
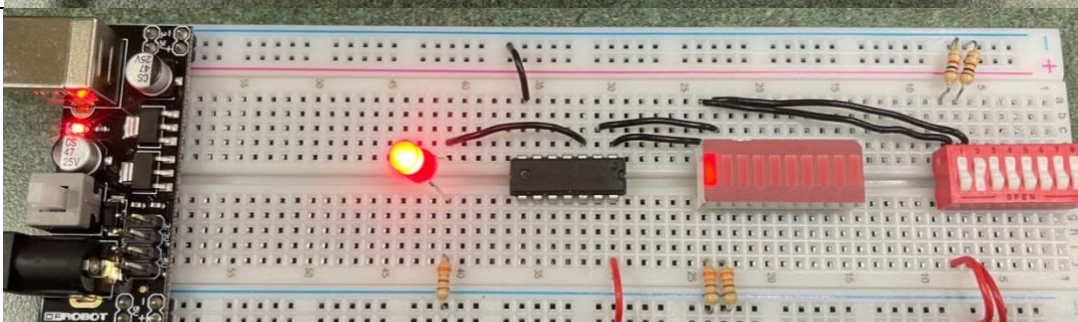
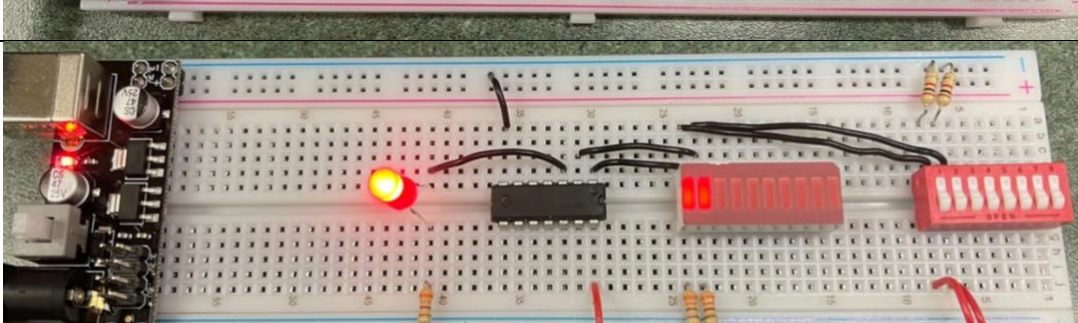
Laboratory 5: Breadboard with Gates

AND gate
Truth Table

Breadboard setup	A	B	Y
	0	0	0
	0	1	0
	1	0	0
	1	1	1

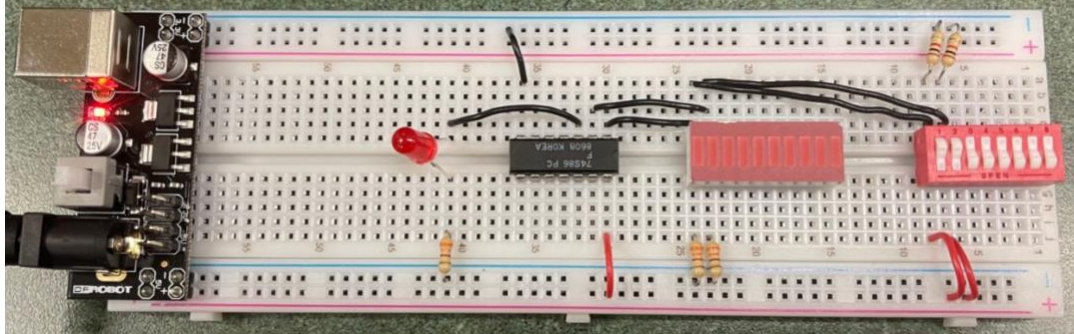
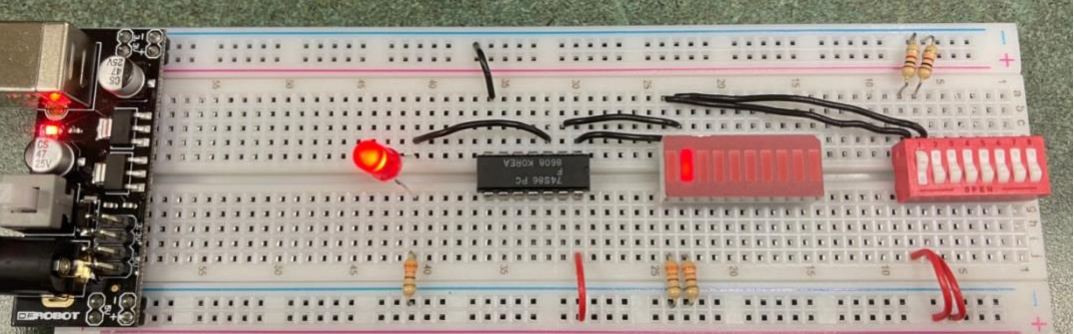
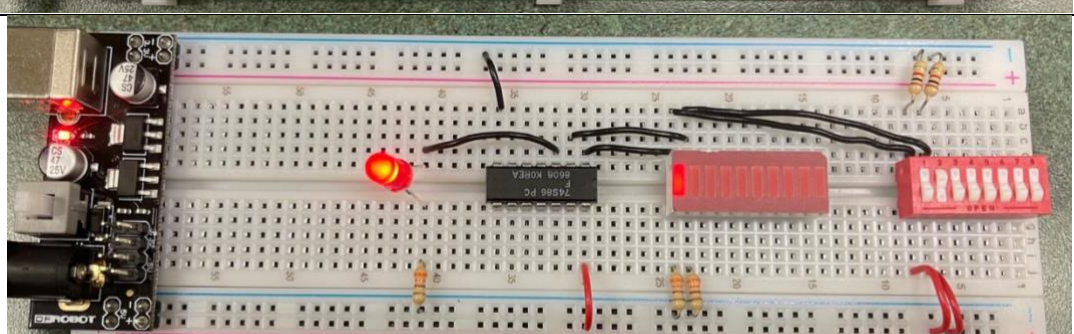
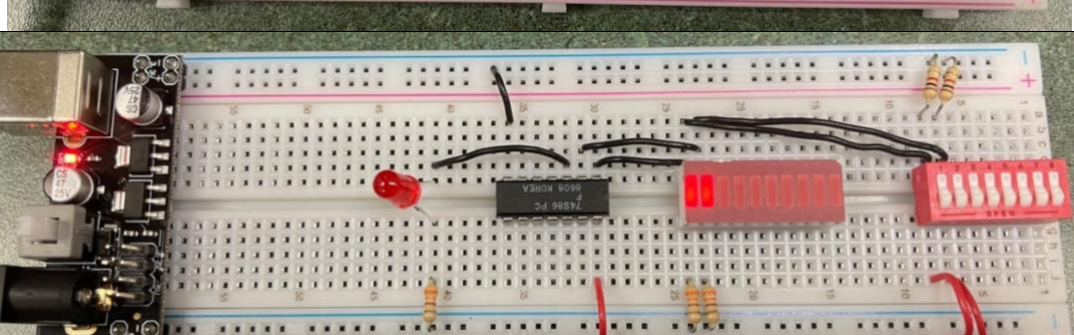
OR gate

Truth Table

Simulation	A	B	Y
	0	0	0
	0	1	1
	1	0	1
	1	1	1

XOR gate

Truth Table

Simulation	A	B	Y
	0	0	0
	0	1	1
	1	0	1
	1	1	0