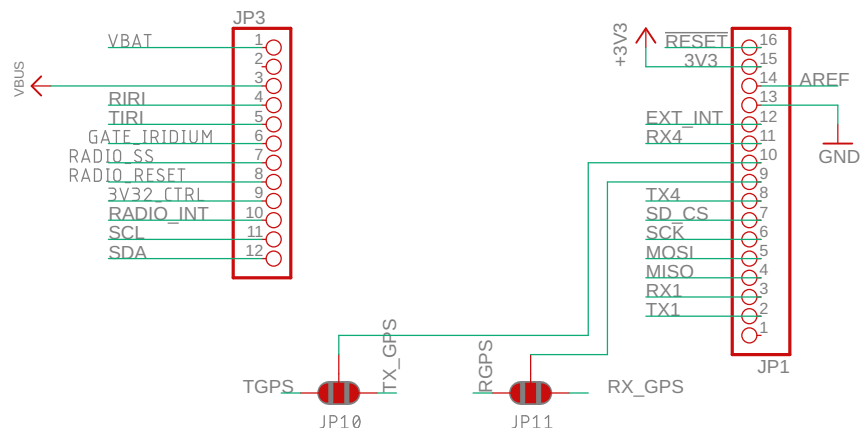


## Feather M4 (SAMD51) headers

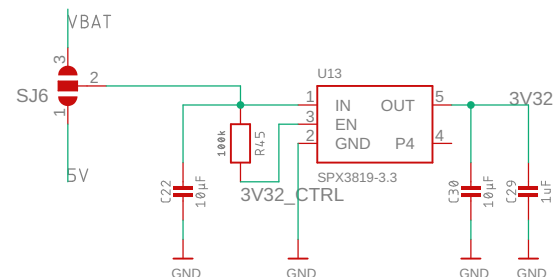
These headers connect to a FeatherM4, mounted from the top of the PCB.

Pins 9 and 10 of JP1 are connected to solder jumpers.  
Select TX\_GPS and RX\_GPS to bypass the RS232 level shifter IC.  
TGPS and RGPS connect the feather pins to teh RS232 shifter.



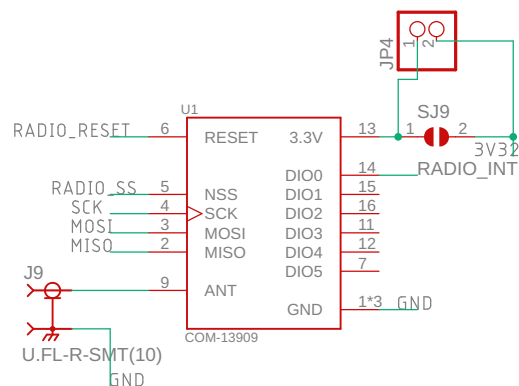
## External 3v3 regulator

The input to the regulator is connected to a solder jumper for selection between VBAT and the 5V rail.  
Choosing the 5V rail option means that even if the regulator is enabled with the 3V3\_CTRL signal, the photoMOS mus also be on.



## RFM69-HCW debug radio

A solder jumper and pin header connection are provided for controlling power to the radio.  
The radio should be disabled during the actual mission.



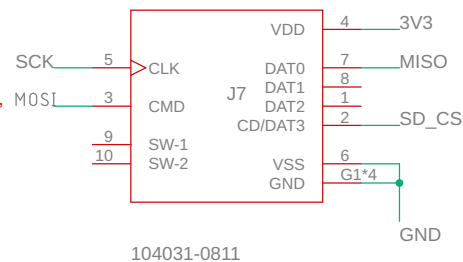
## External activation header

This two pin header serves as a connection point to the spring steel pieces that stick out of the backshell and make contact with the KREM.

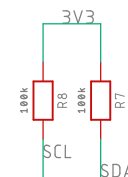
When the KREM is intact, the GPIO pin is shorted to ground. When the KREM separates, an interrupt is generated



## SD Card



## I2C pull-up resistors



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