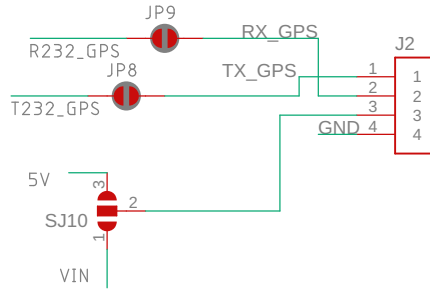


Board to board connections

Depending on jumper settings, this port either provides access to a 3.3V TTL UART or the same UART but shifted to RS232 levels



Battery and power connections

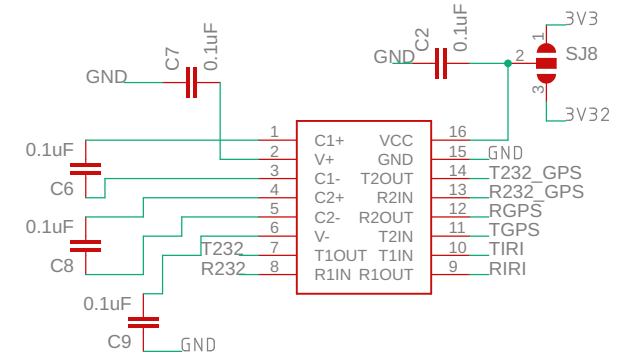
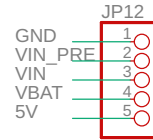
Testpoints for system voltages

VIN is the high voltage pack positive node after being switched by the photoMOS

VIN_PRE is the high voltage pack positive node before being switched by the photoMOS

VBAT is the low voltage pack positive node

5V is the output node of the 5V regulator. The photoMOS must be enabled to supply this rail

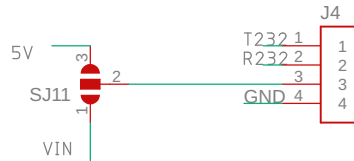


RS232 converter

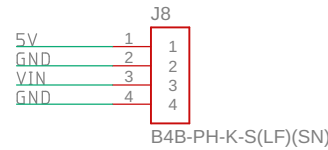


Mounting holes

Rs232 level shifted UART for use with Iridium modem. Power rail select jumper for 5v regulator or raw VIN



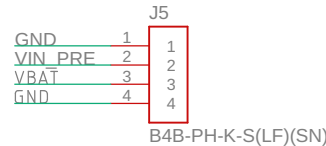
connection to 5v regulator



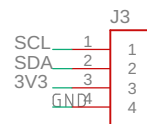
3.3V TTL UART for connection to other subsystems or use as GPIO if no serial port needed.



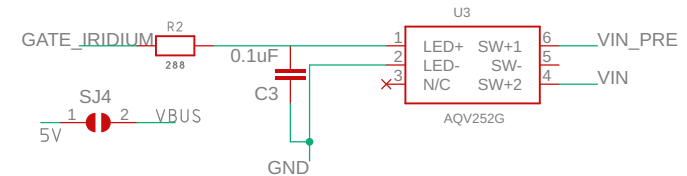
Connection to high and low voltage battery packs.



I2C Header, intended to go to I2C mux for pressure sensors



PhotoMOS POL switch for enabling 5V regulator connected to high voltage pack



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