注：选取单元测试报告（Aunit生成的报告中每个函数一个用例）

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| --- | --- | --- |
| 序号 | 入口函数 | 调用函数 |
|  | PL\_Motor\_Control | void PL\_CloseLoop\_Ctl(u32 PL\_Dst,u32 PL\_BMQ\_Value)  void PL\_OpenLoop\_Ctl(u32 Step\_Nums,u16 Step\_State) |
|  | PL\_Motor\_Control | void PL\_CloseLoop\_Ctl(u32 PL\_Dst,u32 PL\_BMQ\_Value)  void PL\_OpenLoop\_Ctl(u32 Step\_Nums,u16 Step\_State) |
|  | PL\_Motor\_Control | void PL\_CloseLoop\_Ctl(u32 PL\_Dst,u32 PL\_BMQ\_Value)  void PL\_OpenLoop\_Ctl(u32 Step\_Nums,u16 Step\_State) |
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|  | PL\_Motor\_Control | void PL\_CloseLoop\_Ctl(u32 PL\_Dst,u32 PL\_BMQ\_Value)  void PL\_OpenLoop\_Ctl(u32 Step\_Nums,u16 Step\_State) |
|  | PL\_Motor\_Control | void PL\_CloseLoop\_Ctl(u32 PL\_Dst,u32 PL\_BMQ\_Value)  void PL\_OpenLoop\_Ctl(u32 Step\_Nums,u16 Step\_State) |
|  | Init\_Motor\_Para | void PL\_Motor\_Ctl(u32 data1,u32 data2)  void TJ\_Motor\_Ctl(u32 data1,u32 data2) |
|  | NandFlash\_Write\_Start | void Write\_Data\_To\_FPGA(volatile u32 \*Addr,u32 Data) |
|  | Write\_Nandflash | void NandFlash\_Write\_Block\_Set(u16 StartBlock,u16 EndBlock)  void NandFlash\_Write\_Start() |
|  | Write\_Nandflash | void NandFlash\_Write\_Block\_Set(u16 StartBlock,u16 EndBlock)  void NandFlash\_Write\_Start() |
|  | Write\_Nandflash | void NandFlash\_Write\_Block\_Set(u16 StartBlock,u16 EndBlock)  void NandFlash\_Write\_Start() |