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Assignment 4

AI1110: Probability and Random Variables Indian Institute of Technology Hyderabad

K S Ananth CS22BTECH11029

Abstract—In this assignment we have made a Random number generator using shift registers

1 Components used

Component	Value	Quantity	
Breadboard		1	
Seven Segment Diplay	Common Anode	1	
Decoder	7447	1	
Flip Flop	7474	2	
X-OR Gate	7486	1	
555 IC		1	
Resistor	1 ΚΩ	1	
Capacitor	100 nF	1	
Capacitor	10 nF	1	
Jumper Wires			

TABLE 0: Components used

2 Procedure

(i) We connected the 555 timer circuit according to the figure (i)

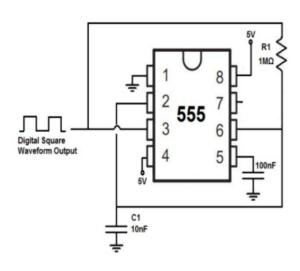


Fig. (i): Connection in 555 timer circuit

- (ii) Then we connected Clock output of 555 timer circuit to the clock signal of D-Flip flops
- (iii) Now we make the circuit for shift registers using a 4 D-Flip flops (using two 7474 IC's)

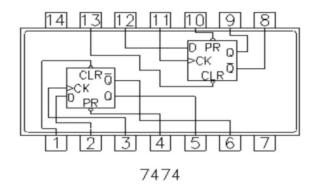


Fig. (iii): Connection in 7474 IC

(iv) Then we connected XOR gate (7486 IC) according to the figure (iv)

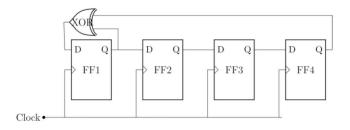


Fig. (iv): Connection in XOR gate

- (v) then we connected the decoder (7447 IC) and connected its A,B,C,D with Q_0,Q_1,Q_2,Q_3 respectively as per the figure (v)
- (vi) Then we connected The seven segmented display and then connected it with the decoder (7447 IC) according to the table (vi) and the figure (vi)

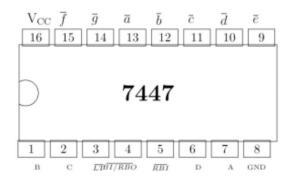


Fig. (v): Connection in Decoder gate

7447	\bar{a}	\bar{b}	\bar{c}	\bar{d}	\bar{e}	\bar{f}	\bar{g}
Display	a	b	С	d	е	f	g

Fig. (vi): Connection of seven segmented display with decoder

(vii) We connected all the independent parts with each other and then connected the power source

3 Output

Output was changing digits on the seven segment display the output is shown in figure (vii)

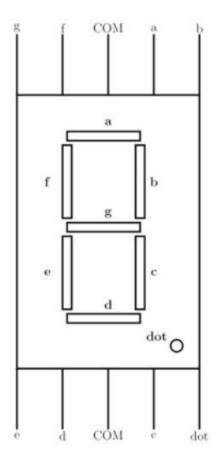


Fig. (vi): Seven segmented display

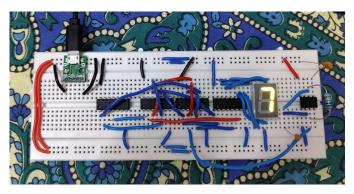


Fig. (vii): output