EE 451

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Parallelization of Scientific Realtime Generative AI Response Verification System for Continuous Quality Assurance in Safety-Critical Software

Introduction

The integration of Artificial Intelligence (AI) into safety-critical systems requires rigorous, continuous quality assurance to mitigate risks associated with autonomous decision-making errors. Traditional serial and manual approaches to AI output verification are not sufficient due to the real-time nature and complexity of such systems.

For autonomous systems, a separate AI system could be used to supervise another system. Once the primary AI produces its response, the secondary AI would analyze the response related to the features of a correct statement. This proposal introduces a parallelization of such verification system that utilizes multiple Large Language Models (LLMs) designed to enhance throughput, and scalability of the real-time verification pipelines in high-reliability applications.

By parallelizing the generative AI output verification system, while maintaining or enhancing decision-making accuracy, compared to a serial implementation, this approach will enable continuous quality assurance with improved decision-making accuracy, significantly boosting system efficiency and capacity compared to serial implementations. Leveraging task-level and data-level parallelism, the system can achieve superior efficiency through optimized memory hierarchy usage, data locality, and reduced communication overhead between cores/processors. Theoretical analysis of upper and lower bounds on performance gains, guided by Amdahl's and Gustafson's Laws, will demonstrate the cost-optimal parallel algorithm, as measured by token processing rate, speed-up ratios, resource utilization, and execution time. As time permits, architectural considerations, such as multicore or GPU efficiency, will be addressed to ensure scalability and high throughput.

Description of Implementation

Architecture Overview

The system architecture will leverage multi-agent LLMs operating together to evaluate and verify AI-generated outputs. Using task-level and data-level parallelism, the system effectively utilizes multiple cores, optimizing memory hierarchy and data locality to minimize communication overhead and latency.

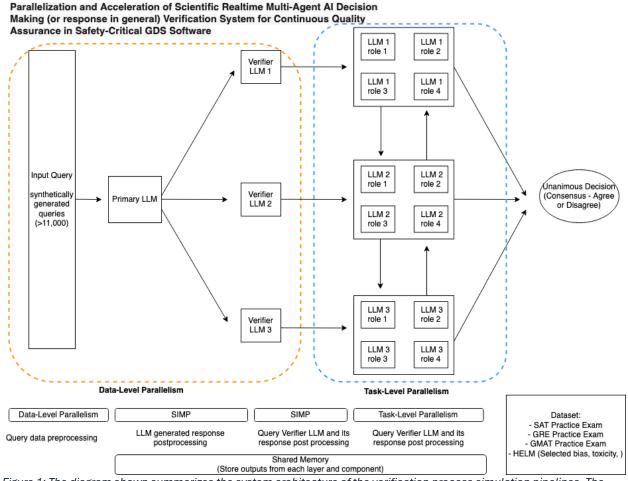


Figure 1: The diagram shown summarizes the system architecture of the verification process simulation pipelines. The system consists of 3 stage simulation, input layer that comprises of data ingestion and the output of the LLM under test. The second layer consumes the primary LLM's output, independently decide whether it agrees with the primary LLM's outputs or not. In the third layer, the three LLM will play different roles to mimic a panel of experts and non-experts. The output of this layer will be

Parallelization Strategy

<u>Task-Level Parallelism</u>: Tasks such as syntax verification, semantic coherence assessment, and fact-checking are assigned to distinct processors. This allows reduction on the overall verification time. Let T_{serial} be the time it takes for one processor to perform all tasks serially. If $T_{task\ i}$ represents the time for task i, then under parallel execution with n processors, the expected time $T_{parallel}$ can be approximated by:

$$T_{
m parallel} pprox {
m max}(T_{
m task~1}, T_{
m task~2}, \ldots, T_{
m task~n})$$

Where each $T_{task\ i}$ is the maximum time, any processor spends on a single task. <u>Data-Level Parallelism</u>: To maximizing the usage of computational resources and minimizing idle times, input data will be partitioned and distributed across processors. Each processor will work independently on its partitioned data.

Technological Stack: Programming Languages used is Python for LLM integration and management, with critical performance segments implemented in C++ for efficiency.

Parallel Computing Tools: MPI for Python (mpi4py) for managing data distribution and task coordination, and OpenMP for handling intra-node parallelism effectively.

Evaluation

Metrics for Evaluation:

<u>Throughput</u> will be measured in number of tokens processed per second to assess how effectively the system handles large volumes of data.

<u>Speedup</u> will be calculated by comparing the execution times of parallel versus serial implementations, capturing the efficiency gains from parallelization and scalability of it. As mentioned below, we will use Amdahl's Law and Gustafson's Law to evaluate the obtained speedup and its theoretical backing.

<u>Resource Utilization</u> will be monitored to ensure all available CPU/GPU resources are being utilized effectively and minimizing waste.

<u>Execution Time</u>: Total time taken for processing the set amount of input (set number of tokens in the input). This system is meant for a real-time system monitoring, hence, delay tolerance is minimal.

<u>Theoretical Performance Bounds</u>: Using Amdahl's and Gustafson's Laws, we will establish theoretical upper bounds on performance gains.

Efficiency (E) will be measured to optimize the number of cores used for the proposed pipeline.

$$E = \frac{Speedup}{Par\# of \ processors \ used}$$

<u>Amdahl's Law</u> will be used to compute the theoretical upper bound expected speedup for a given fixed workload. This provides a theoretical benchmark for assessing the scalability limits imposed by the serial portions of the task. Mathematically, Amdahl's Law is expressed as

$$S_{speedup} = \frac{Serial \, Time}{Parallel \, Time} = \frac{S+P}{S+\frac{P}{f}} = \frac{1}{S+\frac{(1-S)}{f}} \le \frac{1}{S}$$

where S id the serial portion of the program, P is the parallelizable portion of the program, f is the number of processor (or speedup factor), and S + P = 1.

<u>Gustafson's Law</u> will be applied to predict scalability when the workload increases with the number of processors, which is more reflective of the expanding data environments in real-time systems.

Scaled Speedup =
$$\frac{Serial\ Time}{Parallel\ Time} = \frac{S' + P'p}{S' + P'} = \frac{S' + (1 - S')p}{S' + P'} = \frac{S' + (1 - S')p}{1}$$
$$= S' + (1 - S')p \approx (1 - S')p$$

Experimental Setup

A series of experiments will be designed to simulate safety-critical scenarios where AI decision-making is crucial. The system will be evaluated under controlled conditions with predefined datasets like SAT and GRE practice exams, alongside the HELM dataset for measuring societal acceptance and the correctness of AI decisions.

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