Lab 1 Rubric

Activity: Lab 1) Feasibility Model Phase 1

Course: ECE 298 - Spring 2021

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Document	Yes 3 points	Mostly 2 points	Marginally 1 point	No 0 points	Criterion Score
Part 1: There are three or more unambiguous Project Design Requirements	Present and clear ✔	Present but too vague (e.g. "big", "fast") or too specific (e.g. "weighs 3.72 kg")	Too many or too few or very unclear	Extremely confusing, or not present	3 / 3
Part 2: All the project Sensor and User Input types have been defined, with corresponding components chosen and MCU connectivity described.	Present and clear ✔	All sensor and user input types listed, but components or connection types missing	Some sensor and user input types are missing; or components and connection types missing	The section lacks enough content to be useful	3 / 3
Part 2: All the project Actuator and User Output types have been defined, with corresponding components chosen and MCU connectivity described.	Present and clear ✔	All actuator and user output types listed, but components or connection types missing	Some actuator and user output types are missing; or components and connection types missing	The section lacks enough content to be useful	3 / 3
Part 2: All the project MCU Resources and important Parameters have been listed and described.	Present and clear ✔	All MCU resources and important parameters listed, but descriptions are weak or confusing	Some MCU resources and important parameters missing, or descriptions not present	The section lacks enough content to be useful	3 / 3
Part 3: Each device has a) A statement of how to control and observe its functions; b) An image of the test schematic; c) An image of the test results	Present and clear ✔	Most required elements present	Many required elements missing or incorrect	Too many required elements are missing or incorrect	3 / 3
Part 3: Each device has d) A verification statement (description of how the test confirms device behaviour); e) A validation statement (description of how the device behaviours satisfy project design requirements)	All components have a verification statement; All components have a validation statement ✔	Some ambiguities that make verification and validation unclear	Many ambiguities that make verification and validation very unclear	Extremely confusing, or not present	3 / 3
Part 4: System-level block diagram - content	All necessary blocks (components and modules) are present and labeled; And, all signal flow connections are present, sensible, and have correct directional arrows ✔	Some ambiguities or minor errors (e.g. signal flow directions, subsystems unclear)	Too many ambiguities to understand	Extremely confusing, or not present	3 / 3
Part 4: System-level block diagram - quality	Professional in appearance ✔			Unprofessional in appearance or not present	3 / 3

Document	Yes 3 points	Mostly 2 points	Marginally 1 point	No 0 points	Criterion Score
Overall: Document quality	Formatting of the document is professional and includes appropriate use of paragraph and font styles, and appropriate formatting of tables and figures ✔	Formatting of the document is generally consistent and adequate, and mostly includes appropriate use of paragraph and font styles, and appropriate formatting of tables and figures	Formatting of the document is inconsistent and inadequate, and marginally includes the use of paragraph and font styles, and marginal formatting of tables and figures	Document is formatted poorly and lacks appropriate use of paragraph and font styles, or appropriate formatting of tables and figures	3 / 3

Simulation	Yes 1 point	No 0 points	Criterion Score
Simulation runs. Any issues discussed with group members as formative feedback.	Simulation runs ✔	Simulation does not run	1 / 1

Total		28 / 28

Overall Score

Exceeds 24 points minimum ✔	Meets 21 points minimum	Marginal 15 points minimum	Below 0 points minimum