

























Changelog

v0.3 done - prototype ordered 2024-02-07

v0.4 done - prototype ordered 2024-03-06

- swap pins PD5 and PD6 on expansion header (consistency)
- rework USB connector and SD card connector footprints, panel holes
- Use pseudo-SMD pin socket for J1 line in/out to Core
- Use LM4040-10 instead of CJ431 for stabler VREF
- Add extra caps on 3.3V rail
- Use "thin" OLED footprint (slightly different dimensions)
- Adjust Thonkiconn stereo jacks footprint size, position
- Fix encoder pins not connecting to any MCU pin

v0.5 done - Prototype ordered 2024-06

- Add filter caps to pots 1-4
- Rework USB connectors: design breakout board holding two horizontal connectors and pin headers
- Revert OLED to "non-thin" version (same like Gills), run on 5V instead of 3.3V
- Reduce series resistors for optional Gate LEDs to 2.7k

v0.6 done - production

- Further reduce series resistors for optional Gate LEDs to 1k
- Improve SD card and USB breakouts: use 2.0 mm pin headers instead of 1.27 mm ones (a pain to solder)
- Improve Thonkiconn mono and stereo footprints
- Change audio in/out amplification to non-inverted
- Rework power and grounding, add ferrite beads etc.

v0.7 done - production

- Add I2C EEPROM for easy preset memory handling (or other data)
- Add "Ksoloti unified" output insert header. Perhaps for a stereo filter daughterboard?
- BUG: Fix swapped CLOCK and DATA pins for PDM mic header.

v0.8 done - production

- Reduce -10V reference series resistor (R72) to 330 ohms
 (When using 10k pots and R72 is higher than 390, -10V reference may drift)
- Add RC low pass filters to encoder A, B pins
- Increase pot filter caps C63-C66, C11, C14-C16 to 1uF

Sheet: /changelog/ File: changelog.kicad_sch

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