A Pin	A Long Name	A Bus Name	B Pin	B Long Name	B Bus Name	C Pin	C Long Name	C Bus Name	
1	GND	GND	1	GND	GND	1	GND	GND	
2	5V	VCC	2	5V	VCC	2	5V	VCC	
3	ADDRESS 0	ADDR0	3	DATA 12	DATA12	3	ADDRESS REGISTER ID 0	ADDR-REG-ID0	
4	ADDRESS 1	ADDR1	4	DATA 13	DATA13	4	ADDRESS REGISTER ID 1	ADDR-REG-ID1	
5	ADDRESS 2	ADDR2	5	DATA 14	DATA14	5	ADDRESS REGISTER ID 2	ADDR-REG-ID2	
6	ADDRESS 3	ADDR3	6	DATA 15	DATA15	6	ADDRESS REGISTER ID 3	ADDR-REG-ID3	
7	ADDRESS 4	ADDR4	7	-REGISTER FUNCTION READ	-REG-FUNC-RD	7	IO-ADDRESS 0	IOADDR0	
8	ADDRESS 5	ADDR5	8	-REGISTER FUNCTION LOAD	-REG-FUNC-LD	8	IO-ADDRESS 1	IOADDR1	
9	ADDRESS 6	ADDR6	9	REGISTER READ ID 0	REG-RD-ID0	9	IO-ADDRESS 2	IOADDR2	
10	ADDRESS 7	ADDR7	10	REGISTER READ ID 1	REG-RD-ID1	10	IO-ADDRESS 3	IOADDR3	
11	ADDRESS 8	ADDR8	11	REGISTER READ ID 2	REG-RD-ID2	11	-I/O ADDRESS LOAD	-IO-ADDR-LD	
12	ADDRESS 9	ADDR9	12	REGISTER READ ID 3	REG-RD-ID3	12	-VALID MEMORY ADDRESS	-VMA	C12 New to V3.1
13	ADDRESS 10	ADDR10	13	REGISTER LOAD ID 0	REG-LD-ID0	13	-INTERRUPT	-INT	??? Open Collector
14	ADDRESS 11	ADDR11	14	REGISTER LOAD ID 1	REG-LD-ID1	14	-INT ACK	-INTA	
15	ADDRESS 12	ADDR12	15	REGISTER LOAD ID 2	REG-LD-ID2	15	-ALU FUNCTION	-ALU-FUNC	
16	ADDRESS 13	ADDR13	16	REGISTER LOAD ID 3	REG-LD-ID3	16	ALU FUNCTION SELECT 0	ALU0	
17	ADDRESS 14	ADDR14	17	-REGISTER READ LO	-REG-RD-LO	17	ALU FUNCTION SELECT 1	ALU1	
18	ADDRESS 15	ADDR15	18	-REGISTER LOAD LO	-REG-LD-LO	18	ALU FUNCTION SELECT 2	ALU2	
19	DATA 0	DATA0	19	-REGISTER READ HI	-REG-RD-HI	19	ALU FUNCTION SELECT 3	ALU3	
20	DATA 1	DATA1	20	-REGISTER LOAD HI	-REG-LD-HI	20	-ACCUMULATOR LOAD INVERT	-AC-LD-INV	
21	DATA 2	DATA2	21	-REGISTER DOWN	-REG-DN	21	-ACCUMULATOR READ	-AC-RD	
22	DATA 3	DATA3	22	-REGISTER UP	-REG-UP	22	-ACCUMULATOR LOAD	-AC-LD	
23	DATA 4	DATA4	23	-MEMORY READ	-MEM-RD	23	-SHIFT REGISTER LOAD	-SR-LD	
24	DATA 5	DATA5	24	-MEMORY WRITE	-MEM-WR	24	BRANCH CONDITION	BR-COND	
25	DATA 6	DATA6	25	-I/O READ	-IO-RD	25	-HI/LO SWAP	-HL-SWAP	
26	DATA 7	DATA7	26	-I/O WRITE	-IO-WR	26	INPUT	IN	??? Open Collector
27	DATA 8	DATA8	27	-TMP REGISTER READ ID 0	-TMP-REG-RD0	27	OUTPUT	OUT	
28	DATA 9	DATA9	28	-TMP REGISTER LOAD ID 0	-TMP-REG-LD0	28	-BUS ENABLE	-BUS-EN	
29	DATA 10	DATA10	29	-TMP REGISTER READ ID 1	-TMP-REG-RD1	29	-RUN	-RUN	
30	DATA 11	DATA11	30	-TMP REGISTER LOAD ID 1	-TMP-REG-LD1	30	-RESET	-RESET	
31	5V	VCC	31	5V	VCC	31	5V	VCC	
32	GND	GND	32	GND	GND	32	GND	GND	
	Several signals changed to active negative per bus								