YACC1 Connector

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Pin	A	A	A	B	B	B	C	C	C
	Fixed	Bus Name	Notes	Fixed	Bus Name	Notes	Fixed	Bus Name	Notes
1	GND	GND		GND	GND		GND	GND	
2	5V	VCC		5V	VCC		5V	VCC	
3	ADDRESS 0	ADDR0		DATA 12	DATA12		-ADDRESS REGISTER READ 0	-ADDR-REG-RD0	
4	ADDRESS 1	ADDR1		DATA 13	DATA13		-ADDRESS REGISTER LOAD 0	-ADDR-REG-LD0	
5	ADDRESS 2	ADDR2		DATA 14	DATA14		-ADDRESS REGISTER READ 1	-ADDR-REG-RD1	
6	ADDRESS 3	ADDR3		DATA 15	DATA15		-ADDRESS REGISTER LOAD 1	-ADDR-REG-LD1	
7	ADDRESS 4	ADDR4		-REGISTER FUNCTION READ	-REG-FUNC-RD		IO-ADDRESS 0	IOADDR0	
8	ADDRESS 5	ADDR5		-REGISTER FUNCTION LOAD	-REG-FUNC-LD		IO-ADDRESS 1	IOADDR1	
9	ADDRESS 6	ADDR6		REGISTER READ ID 0	REG-RD-ID0		IO-ADDRESS 2	IOADDR2	
10	ADDRESS 7	ADDR7		REGISTER READ ID 1	REG-RD-ID1		IO-ADDRESS 3	IOADDR3	
11	ADDRESS 8	ADDR8		REGISTER READ ID 2	REG-RD-ID2		-I/O ADDRESS LOAD	-IO-ADDR-LD	
12	ADDRESS 9	ADDR9		REGISTER READ ID 3	REG-RD-ID3		UNUSED 0	UNUSED0	reg bus dir??? or VMA
13	ADDRESS 10	ADDR10		REGISTER LOAD ID 0	REG-LD-ID0		-INTERRUPT	-INT	??? Open Collector
14	ADDRESS 11	ADDR11		REGISTER LOAD ID 1	REG-LD-ID1		-INT ACK	-INTA	
15	ADDRESS 12	ADDR12		REGISTER LOAD ID 2	REG-LD-ID2		-ALU FUNCTION	-ALU-FUNC	
16	ADDRESS 13	ADDR13		REGISTER LOAD ID 3	REG-LD-ID3		ALU FUNCTION SELECT 0	ALU0	
17	ADDRESS 14	ADDR14		-REGISTER READ LO	-REG-RD-LO		ALU FUNCTION SELECT 1	ALU1	
18	ADDRESS 15	ADDR15		-REGISTER LOAD LO	-REG-LD-LO		ALU FUNCTION SELECT 2	ALU2	
19	DATA 0	DATA0		-REGISTER READ HI	-REG-RD-HI		ALU FUNCTION SELECT 3	ALU3	
20	DATA 1	DATA1		-REGISTER LOAD HI	-REG-LD-HI		-ACCUMULATOR LOAD INVERT	-AC-LD-INV	
21	DATA 2	DATA2		-REGISTER DOWN	-REG-DN		-ACCUMULATOR READ	-AC-RD	
22	DATA 3	DATA3		-REGISTER UP	-REG-UP		-ACCUMULATOR LOAD	-AC-LD	
23	DATA 4	DATA4		-MEMORY READ	-MEM-RD		-SHIFT REGISTER LOAD	-SR-LD	
24	DATA 5	DATA5		-MEMORY WRITE	-MEM-WR		BRANCH CONDITION	BR-COND	
25	DATA 6	DATA6		-I/O READ	-IO-RD		-HI/LO SWAP	-HL-SWAP	
26	DATA 7	DATA7		-I/O WRITE	-IO-WR		INPUT	IN	??? Open Collector
27	DATA 8	DATA8		-TMP REGISTER READ ID 0	-TMP-REG-RD0		OUTPUT	OUT	
28	DATA 9	DATA9		-TMP REGISTER LOAD ID 0	-TMP-REG-LD0		-BUS ENABLE	-BUS-EN	
29	DATA 10	DATA10		-TMP REGISTER READ ID 1	-TMP-REG-RD1		-RUN	-RUN	
30	DATA 11	DATA11		-TMP REGISTER LOAD ID 1	-TMP-REG-LD1		-RESET	-RESET	
31	5V	VCC		5V	VCC		5V	VCC	
32	GND	GND		GND	GND		GND	GND	

Several signals changed to active negative per bus

YACC 3.0 | YACC 2.0 | YACC 1.1 | YACC 1.11 | YACC 1.2 | YACC 1.21 | YACC 1.22 | BRD Usage | 1.11 CHK | 1.20 CHK | YACC 1.0 | Sheet8

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