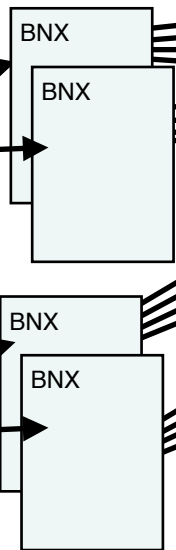


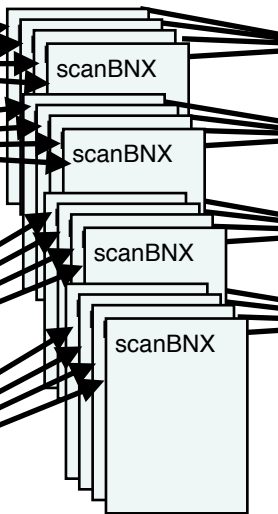
(A) The Irys produces tiff files



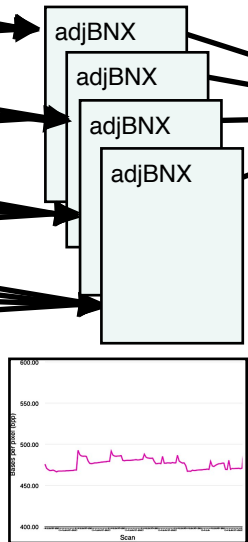
(B) Each chip produces flowcell BNX files



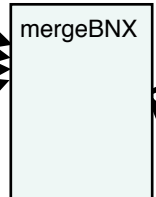
(C) Scan BNX are adjusted



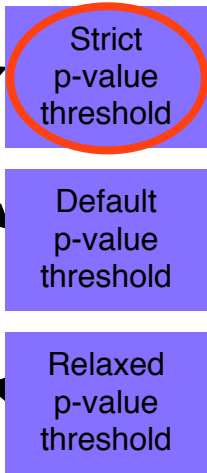
(D) QC graphs for each flowcell



(E) Merge all flowcells



(F) First assemblies



(G) Second assemblies

