



Application Specific – JTAG Overview

The JTAG IP consist of a set of boundary scan IP. The boundary scan IP include a parameterized Test Access Port (TAP) controller (DW_tap) plus a set of boundary scan cells that you can use to implement a custom IEEE 1149.1 boundary scan test solution for your ASIC.

In addition to the datasheets provided in this section, there is an application note for the boundary scan IP [“AN 96-004”](#).

Related Topics

For a listing of Building Block components and associated datasheets, see:

- [DesignWare Building Block IP Documentation Overview](#)

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