# Robert Karmazin

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## Objective

To be incredibly awesome and bring the light of async to all who would listen..

#### Education

2015–2006 **Ph.D.**, Cornell University, Ithaca, NY.

Developed a CAD solution for automating the physical design and implementation of asynchronous circuits

2002–2006 B.S., The Johns Hopkins University, Baltimore, MD.

Description

## Experience

#### Vocational

year-year Job title, Employer, City.

General description no longer than 1-2 lines.

Detailed achievements:

- Achievement 1:
- Achievement 2, with sub-achievements:
  - Sub-achievement (a);
  - Sub-achievement (b), with sub-sub-achievements (don't do this!);
    - · Sub-sub-achievement i;
    - · Sub-sub-achievement ii;
    - · Sub-sub-achievement iii;
  - Sub-achievement (c);
- Achievement 3.

year-year Job title, Employer, City.

Description line 1 Description line 2

Miscellaneous

year-year Job title, Employer, City.

Description

Publications

[4] [5] [1] [2] [3]

Languages

Language 1 Skill level

Language 2 Skill level

Comment

Comment

Language 3 Skill level Comment

## Computer skills

category 1 XXX, YYY, ZZZ
category 2 XXX, YYY, ZZZ
category 3 XXX, YYY, ZZZ
category 3 XXX, YYY, ZZZ
category 6 XXX, YYY, ZZZ

## Interests

hobby 1 Description

hobby 2 Description

hobby 3 Description

#### Extra 1

- o Item 1
- o Item 2
- Item 3. This item is particularly long and therefore normally spans over several lines. Did you notice the indentation when the line wraps?

### Extra 2

- o Item 1
- o Item 2
- o Item 3

- o Item 4
- Item 5 cite book1
- Item 6. Like item 3 in the single column list before, this item is particularly long to wrap over several lines.

## References

Category 1
Category 2
All the rest & some more

Person 1
Person 2
Person 3
Person 3
All the rest & some more

That person, and those also (all available upon request).

Person 2
(more upon request)

#### **Publications**

Benjamin Hill, **Karmazin, Robert**, Carlos Tadeo Ortega Otero, Jonathan Tse, and Rajit Manohar. A split-foundry asynchronous fpga. In *CICC*, pages 1–4, 2013.

Nabil Imam, Kyle Wecker, Jonathan Tse, **Karmazin, Robert**, and Rajit Manohar. Neural spiking dynamics in asynchronous digital circuits. In *IJCNN*, pages 1–8. IEEE, 2013.

Janani Mukundan, Saugata Ghose, **Karmazin, Robert**, Engin Ipek, and José F Martínez. Overcoming single-thread performance hurdles in the core fusion reconfigurable multicore architecture. In *ICS*, pages 101–110. ACM, 2012.

Carlos Tadeo Ortga Otero, Jonathan Tse, **Karmazin, Robert**, Benjamin Hill, and Rajit Manohar. Ulsnap: An ultra-low power event-driven microcontroller for sensor network nodes. In *ISQED*, pages 667–674. IEEE, 2014.

**Karmazin, Robert**, Carlos Tadeo Ortega Otero, and Rajit Manohar. celltk: Automated layout for asynchronous circuits with nonstandard cells. In *ASYNC*, pages 58–66. IEEE, 2013.

January 01, 1984

**Company Recruitment team** 

Company, Inc. 123 somestreet some city

Dear Sir or Madam,

Lorem ipsum dolor sit amet, consectetur adipiscing elit. Duis ullamcorper neque sit amet lectus facilisis sed luctus nisl iaculis. Vivamus at neque arcu, sed tempor quam. Curabitur pharetra tincidunt tincidunt. Morbi volutpat feugiat mauris, quis tempor neque vehicula volutpat. Duis tristique justo vel massa fermentum accumsan. Mauris ante elit, feugiat vestibulum tempor eget, eleifend ac ipsum. Donec scelerisque lobortis ipsum eu vestibulum. Pellentesque vel massa at felis accumsan rhoncus.

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Albert Einstein discovered that  $e = mc^2$  in 1905.

$$e = \lim_{n \to \infty} \left( 1 + \frac{1}{n} \right)^n$$

Yours faithfully,

Robert Karmazin

Attached: curriculum vitæ