Receiver Top

RECEIVER

CLK100MHz → clk

Rxd from tranceiver → rxd

BTNC → rst

rerrcnt → JA[10]

rerr counter

rxd → decoder → rdata

clk → clkenable → sixteenbaudrate

din [7:0] → correlator → l_out, h_out, csum

FSM

EOF

mac → MAC address

rrdy ← From receiver adapter

cardet → To manchester transmitter

rdata [7:0] ←

rrd ←

FIFO BUFFER

CLK100MHz → clk_a

BTNC → clr

BTNC → rst

re → re

din [7:0]

we

empty → LED [3]

full → LED [2]

dout [7:0] → To async transmitter