

The p4ott P4 Formalization

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This is a description of the `p4ott` formalization of P4, which includes a syntax and a strictly small-step style semantics. It is based on [the official P4 specification](#) and inspired by Core P4 [1].

`p4ott` is constructed using the `ott` tool. `ott` files can then be exported to \LaTeX commands (used in this document) as well as to the HOL4, Isabelle/HOL and Coq interactive theorem provers (of which only the first is currently supported).

1 Syntax

1.1 Types

x, f, a, tbl	string
b	boolean
bl	bit-string
i	natural number
m, n, o	indices

Figure 1: Variables

The variables shown in Figure 1 are standard designations for variables of [P4 base types](#) included in `p4ott`, plus the numerals i and the indices m, n, o which are not part of the P4 syntax, but used on a meta-level throughout this formalization. Depending on the context, strings are denoted with a, x (variable or parser state name), msg (error message), tbl (match-action table name) or f (function or field name). bl is a list of Boolean values, used to represent bit-strings of fixed width.

Types are sometimes explicitly referenced in the syntax, e.g. in declaration statements. The notation for this is shown in Figure 2. Subscript t is used to clarify the notation refers to a type, as opposed to a variable of that type. Declared instances of composite types are stored in the type environment T .

1.2 Expressions

`p4ott` includes a subset of the full set of P4 expressions found in [Section 8](#) of the P4 specification, shown in Figure 3.

bt	$::=$	base types
		$bool_t$
		bit_t
t	$::=$	types
		bt
		$struct_t\ t_1, \dots, t_n$
		$header_t\ t_1, \dots, t_n$
		ext

Figure 2: Types

e	$::=$	expression
		v constant value
		var $varn$ variable
		$\{e_1, \dots, e_n\}$ expression list
		$e.x$ field access
		$\ominus e$ unary operation
		$e_1 \oplus e_2$ binary operation
		concat $e_1\ e_2$ concatenation of bit-strings
		$e_1[e_2 : e_3]$ bit-slice
		call $funn(e_1, \dots, e_n)$ function or extern call
		select $e\{v_1 : x_1; \dots; v_n : x_n\}x$ select
		eStruct $\{x_1 = e_1; \dots; x_n = e_n\}$ struct expression
		eHeader $boolv\{x_1 = e_1; \dots; x_n = e_n\}$ header expression
		(e) S

Figure 3: P4 Expressions

First, an expression can be a value: a Boolean or an integer (collectively referred to as constant values v), or a variable or parser state name x . Lists of expressions can be used in declarations of variables of struct types. The fields of these structs may be accessed, which is denoted in the usual manner. There exist unary and binary arithmetic operations, where the semantics of the individual operations are defined on some subset of the constants¹. The function call is built from the function name f , and a list of arguments (expressions).

The **select** expression is similar to a switch statement in C or Java. The expression e is evaluated, and then matched against v_1, \dots, v_n . If some match is successful, the **select** expression evaluates to the string at the corresponding index. If no match occurs, then it instead evaluates to the default string x .

¹The concrete syntax of the many unary and binary operations is found in Appendix A

1.3 Statements

<i>stmt</i>	::=	statement
\emptyset_{stmt}		empty statement
$lval := e$		assignment
if e then $stmt_1$ else $stmt_2$		conditional
$\overrightarrow{\{decl\} stmt}$		block
return e		return
$stmt_1; stmt_2$		sequence
verify $e\ e'$		verify
transition e		transition
apply $tbl(e_1, \dots, e_n)$		apply
\blacksquare		extern function

Figure 4: P4 Statements

`p4ott` includes a subset of the full set of P4 statements found in [Section 11](#) of the P4 specification, shown in [Figure 4](#). They are mostly standard, apart from the following: the in-progress block is an artifact of our small-step semantics. The **verify** statement (here a statement and not an extern function as in [Section 12.7](#) of the P4 specification) can be found uniquely in a parser block. It asserts the expression e and if it holds, does nothing. If e does not hold, it jumps to a rejecting parser state with error message being the result of evaluating e' . The **transition** statement continues execution at a new parser state, the name of which is the result of evaluating e . The **apply** statement applies the match-action table with name tbl (found in the control plane) to the result of evaluating e , thus obtaining an action (modeled as a function call) to execute next.

<i>lval</i>	::=	
$varn$		variable name
null		null variable
$lval.f$		field access
$(lval)$		

Figure 5: P4 l-values

The assignment can assign to *lvals* (shown in [Figure 5](#)), which include variables identified by their names, a null variable (used to model method calls) and struct fields, which are identified by the struct and field names, similar to the field access expression.

1.4 Execution State

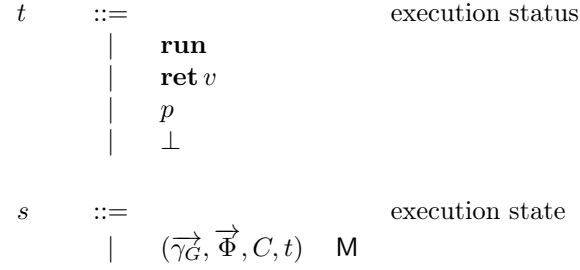


Figure 6: P4 Execution State

The P4 execution state is shown in Figure 6. Note that nothing like this is described in the P4 specification, so it is entirely an artifice of the `p4ott` implementation. In short, the execution state s is a tuple of the state global scope list $\vec{\gamma}_G$, a frame list $\vec{\Phi}$, Control table C , and the state status t .

More formally, a scope $\gamma : X \hookrightarrow V * (X \cup \{\perp\})$ is a partial function from variable names $x \in X$ to constant values $v \in V$. The following operations can be performed on γ :

- $\text{dom}(\gamma)$: Gets the domain of γ : obtains the set of variable names $x \in X$ which are mapped to values in γ .
- $(x \mapsto v) \gamma$: Updates a variable mapping in γ : yields the scope γ' , which is just γ where x instead maps to v . By writing $\forall i \leq n. (x_i \mapsto v_i) \gamma$ we extend this to lists of mappings from variable names to values.

A frame ε is a stack of scopes where the global scope γ_G is located at the bottom; that is, in location $\varepsilon[0]$. When a frame is considered a list the head of the list (i.e $\varepsilon[0]$) represent the bottom of the stack. The current scope - that which was most recently entered by execution - is stored on the top of ε (note that this indexing is the reverse of what you would expect from a list). Whenever a new block (delineated by $\{\}$) is entered, a new fresh scope γ_\emptyset is pushed onto the frame ε .

The following operations can be performed on a frame ε :

- $\gamma :: \varepsilon$: Add a scope γ on bottom of ε (i.e. cons).
- $(i \mapsto \gamma) \varepsilon$: Updates the scope located at index i of ε by setting it to γ .

The call stack E is a stack of frames used whenever a function call occurs. When a function call is executed, the frame ε (minus the global scope γ_G) of the caller will be pushed onto E . When the callee function finishes execution and returns, ε will be popped from E and pushed onto a frame containing only γ_G . Note that this means that the same γ_G is kept throughout function calls, and updates to it are passed along accordingly. The following operations can be performed on E :

- $\varepsilon :: E$: Pushes a frame ε onto the call stack E .

The status **run** represents that the program is executing under regular circumstances. **ret** v is used when the **return** statement returns a constant v at the end of a function call. The status

p signifies transition to a new parser state inside the parser - a named state in the case of **tra** x , or a final state (p_{fin}) in the case of **acc** or **rej**. \perp represents a crash or undefined behaviour, for example caused by some badly-typed part of the program.

In addition to the above, there's also a function map F mapping function names to tuples of their bodies and argument names, a table map Tb mapping table names to tuples of expressions and matching kinds, a parser map P mapping parser state names to their bodies and a type environment T . These are assumed to be static, and are therefore not part of the execution state.

1.5 Calling convention

The calling conventions can be directioned or directionless. The direction can be either IN, OUT or INOUT. IN direction summary:

1. Should not be used on the left hand of assignment
2. Shouldn't be passed to a function without using the proper calling convention IN
3. Initialized by copying the value of the corresponding argument when the invocation is executed.

OUT summary:

1. usually uninitialized, and treated as l-values. after the execution of the call, the value of the OUT parameters copied to the corresponding location of the l-value. OUT parameters are initialized in the following cases
 - (a) if the types are header or header_union, OUT parameter is set to "invalid"
 - (b) if the type is a header stack, then all elements of the header stack set to "invalid" and the next index is initialized to 0.
 - (c) if the type is compound (e.g. struct or tuple) apply the rules recursively to its members.
 - (d) if any any other type than listed above (e.g. bit <W>), then it doesn't need any predictable value.

INOUT summary:

1. this type of parameters are both IN and OUT.
2. it must be an l-value, which means it can be assigned to a value.

NO direction summary:

1. those parameters are known at compile time.
2. it also can be an action parameter, can be set by the control plane.
3. it also can be an action parameter that set directly by an other action, then the behaviour will be like IN parameter.

The direction d can be \downarrow denotes IN, \uparrow denotes OUT, \updownarrow denotes INOUT, \circ denotes directionless. Thus, $d ::= \downarrow \mid \uparrow \mid \updownarrow \mid \circ$

Due to the calling conventions, the scope has the type; $\gamma : X \hookrightarrow V * (X \cup \{\perp\})$.

The list of scopes is identified with an arrow following $\vec{\gamma}$. The local $\vec{\gamma}$ is meant to be a local list of scopes to the frame. The global $\vec{\gamma}_G$ is a list with a length of 2. The first index determines the external architecture scope (can not be defined by a P4 program), while the second index determines the architecture local variables (defined in the architecture level). Operations on list of scopes:

1. Operation $\gamma[x \mapsto v]$ to update a variable name x with value v in the scope γ .
2. Operation $\vec{\gamma}[x \mapsto v]$ to update a variable name x with value v in the most recent scope that contains the variable name x .
3. Operation $lookup_v(\vec{\gamma}, \vec{\gamma}_G, x)$ to return the value v of the tuple $(y, x \cup \perp)$, which is whatever variable x is mapped to in the most recent scope it is defined in after concatenating $\vec{\gamma}_G \# \vec{\gamma}$.

2 Semantics

2.1 Expressions

Expression semantics and reductions are layed out in this section. Overall, the reductions can never alter or have any side effects on the state. The only thing it can perform is a reduction on the expressions -standard small step structural semantics- and also produce a new frame -which occurs only in the function call reduction-.

variable lookup

In the E_LOOKUP rule, the lookup function ensures that the variable name x is evaluated in the uppermost (i.e. most recently scope γ that x is declared or instantiated in). The evaluation will occur in the global list of scopes $\vec{\gamma}_G$ with the local $\vec{\gamma}$ being the most recent scopes. This agrees with the description in Sections 6.8 and 10.2 of the P4 specification. The value of this variable is then resolved, and checked to be a constant.

$$\frac{v = \text{lookup}_v(\vec{\gamma}, \vec{\gamma}_G, \text{varn})}{ctx \vec{\gamma}_G \vec{\gamma} \vdash (\mathbf{var} \text{varn}) \rightsquigarrow (v, [])} \quad E_LOOKUP$$

function call

Note that function calls also include actions as well as extern function calls.

The E_CALL_ARGS is used whenever there is a function call expression with unreduced (in and none directioned arguments). Each function's argument will be checked against the function's direction in the same position. If the direction is in or none, then it will be reduced until it becomes a constant, otherwise if the argument has the direction out or inout then do not reduce it.

The $E_CALL_NEWFRAME$ rule is used when all of the function arguments have been reduced to constants (for non-out directions) or variables (directions with out). The function call reduction will produce a placeholder we call **varstar** and a new frame. The new frame will be later added on top of the state's frames in the statements reduction rules. The way that this new frame is constructed is as follows: The function name $funn$ will be the same as the call in the expression. The function name $funn$ will be looked up in both the function maps and extern maps to retrieve the function body $stmt$ and the signature of the parameters represented as a list of tuples: variable names and their directions $[(x_1, d_1), \dots, (x_n, d_n)]$. Each argument will be checked against the boolean formula to ensure that the arguments were reduced properly: (if d_i in-none then the expression in the same position should be a constant, otherwise a variable name). The new frame's scope γ' is a new fresh empty scope that contains the copied in arguments using the function **copyin**.

headers and structs

The struct can be an expression, thus we need reductions for it. The rule $E_ESTRUCT$ rule will reduce the expression feilds one at a time from left to right. Once all the expressions become constants then we can transform the expression struct to a value struct via $E_ESTRUCT_TO_V$ which will be used mostly in header access. Same exact operations are applied on the headers.

$$\begin{array}{c}
[(x_1, d_1), \dots, (x_n, d_n)] = \mathbf{lookup_funn_sig} (funn, F_g, F_b, X) \\
i = \min \{j. [d_1, \dots, d_n][j] \in \{\circ, \downarrow\} \wedge \neg(\text{is_const } [e_1, \dots, e_n][j])\} \\
e = [e_1, \dots, e_n][i] \\
(X, F_g, F_b, Tb) \xrightarrow{\gamma_G} \vec{\gamma} \vdash (e) \rightsquigarrow (e', \vec{\Phi}) \\
[e'_1, \dots, e'_n] = (i \mapsto e')[e_1, \dots, e_n] \\
\hline
(X, F_g, F_b, Tb) \xrightarrow{\gamma_G} \vec{\gamma} \vdash (\mathbf{call_funn}(e_1, \dots, e_n)) \rightsquigarrow (\mathbf{call_funn}(e'_1, \dots, e'_n), \vec{\Phi}) \quad \text{E_CALL_ARGS}
\end{array}$$

$$\begin{array}{c}
(stmt, [(x_1, d_1), \dots, (x_n, d_n)]) = \mathbf{lookup_funn_sig_body} (funn, F_g, F_b, X) \\
\forall i \leq n. ((d_i \in \{\circ, \downarrow\} \implies \text{is_const } e_i) \wedge (d_i \in \{\uparrow, \uparrow\} \implies \text{is_var } e_i)) \\
\gamma' = \text{copyin}([x_1, \dots, x_n], [e_1, \dots, e_n], [d_1, \dots, d_n], \gamma) \\
\hline
(X, F_g, F_b, Tb) \xrightarrow{\gamma_G} \vec{\gamma} \vdash (\mathbf{call_funn}(e_1, \dots, e_n)) \rightsquigarrow (\mathbf{var}(\mathbf{star}, funn), [(stmt)_{[\gamma']}^{funn}]) \quad \text{E_CALL_NEWFRAME}
\end{array}$$

$$\begin{array}{c}
i = \min \{j. \neg(\text{is_const } [e_1, \dots, e_n][j])\} \\
e = [e_1, \dots, e_n][i] \\
ctx \xrightarrow{\gamma_G} \vec{\gamma} \vdash (e) \rightsquigarrow (e', \vec{\Phi}) \\
[e'_1, \dots, e'_n] = (i \mapsto e')[e_1, \dots, e_n] \\
\hline
ctx \xrightarrow{\gamma_G} \vec{\gamma} \vdash (\mathbf{eStruct} \{f_1 = e_1; \dots; f_n = e_n\}) \rightsquigarrow (\mathbf{eStruct} \{f_1 = e'_1; \dots; f_n = e'_n\}, \vec{\Phi}) \quad \text{E_ESTRUCT}
\end{array}$$

$$\begin{array}{c}
\text{is_consts } e_1, \dots, e_n \\
v_1, \dots, v_n = \text{vl_of_el}(e_1, \dots, e_n) \\
\hline
ctx \xrightarrow{\gamma_G} \vec{\gamma} \vdash (\mathbf{eStruct} \{f_1 = e_1; \dots; f_n = e_n\}) \rightsquigarrow (\mathbf{struct} \{f_1 = v_1; \dots; f_n = v_n\}, []) \quad \text{E_ESTRUCT_TO_V}
\end{array}$$

$$\begin{array}{c}
i = \min \{j. \neg(\text{is_const } [e_1, \dots, e_n][j])\} \\
e = [e_1, \dots, e_n][i] \\
ctx \xrightarrow{\gamma_G} \vec{\gamma} \vdash (e) \rightsquigarrow (e', \vec{\Phi}) \\
[e'_1, \dots, e'_n] = (i \mapsto e')[e_1, \dots, e_n] \\
\hline
ctx \xrightarrow{\gamma_G} \vec{\gamma} \vdash (\mathbf{eHeader} \text{ boolv}\{f_1 = e_1; \dots; f_n = e_n\}) \rightsquigarrow (\mathbf{eHeader} \text{ boolv}\{f_1 = e'_1; \dots; f_n = e'_n\}, \vec{\Phi}) \quad \text{E_EHEADER}
\end{array}$$

$$\begin{array}{c}
\text{is_consts } e_1, \dots, e_n \\
v_1, \dots, v_n = \text{vl_of_el}(e_1, \dots, e_n) \\
\hline
ctx \xrightarrow{\gamma_G} \vec{\gamma} \vdash (\mathbf{eHeader} \text{ boolv}\{f_1 = e_1; \dots; f_n = e_n\}) \rightsquigarrow (\mathbf{header} \text{ boolv}\{f_1 = v_1; \dots; f_n = v_n\}, []) \quad \text{E_EHEADER_TO_V}
\end{array}$$

access headers and structs

The E_S_ACC rule is used to access the values of fields in structs, and the E_H_ACC rule is similarly used for headers.

$$\frac{v = \mathbf{struct} \{f_1 = v_1; \dots; f_n = v_n\}(f)}{ctx \vec{\gamma}_G \vec{\gamma} \vdash (\mathbf{struct} \{f_1 = v_1; \dots; f_n = v_n\}.f) \rightsquigarrow (v, [])} \quad \text{E_S_ACC}$$

$$\frac{v = \mathbf{header} \text{ boolv} \{f_1 = v_1; \dots; f_n = v_n\}(f)}{ctx \vec{\gamma}_G \vec{\gamma} \vdash (\mathbf{header} \text{ boolv} \{f_1 = v_1; \dots; f_n = v_n\}.f) \rightsquigarrow (v, [])} \quad \text{E_H_ACC}$$

select label

The E_SEL_ACC rule is used to match the given value v against the label-value list, in the case a match exists. If the match doesn't exist, then return the default label x .

$$\frac{x' = \{v_1 : x_1; \dots; v_n : x_n; _ : x\}(v)}{ctx \vec{\gamma}_G \vec{\gamma} \vdash (\mathbf{select} v \{v_1 : x_1; \dots; v_n : x_n\}x) \rightsquigarrow (x', [])} \quad \text{E_SEL_ACC}$$

bit slicing

This reduction gives a bitvector $bitv'''$ that is reduced from the slicing operation. It extracts a contiguous list from the original $bitv$ from the lsb $bitv''$ till the msb $bitv'$.

$$\frac{bitv''' = bitv[bitv' : bitv'']}{ctx \vec{\gamma}_G \vec{\gamma} \vdash (bitv[bitv' : bitv'']) \rightsquigarrow (bitv''', [])} \quad \text{E_SLICE_V}$$

concatenation

The reduction produces one bit string $bitv''$ that is the result of concatenating two bit strings $bitv$ and $bitv'$.

$$\frac{bitv'' = bitv \mathbin{++} bitv'}{ctx \vec{\gamma}_G \vec{\gamma} \vdash (\mathbf{concat} bitv bitv') \rightsquigarrow (bitv'', [])} \quad \text{E_CONCAT_V}$$

unary and binary

Unary and binary operations are basically standard operations on bit vectors, therefore are removed from this section.

2.2 Statement (single frame) semantics

The semantics of the statements are presented in this section. ².

²Rules for reducing expressions in all contexts are found in Appendix B

assignment

The assignment can assign to *lvals* (shown in Figure 5), they can be either variable identified by their names, a null variable (used to model method calls) or struct fields, which are identified by the struct and field names, similar to the field access expression. Whenever an *lval* is assigned to an expression, that expression shall be reduced until it becomes a constant value. Then the appropriate scope in the current frame of execution will be updated with the mapping $x \mapsto v$. With appropriate we mean that the topmost (most recent) scope that the *lval* is declared in. The reduction results in the empty statement and an updated local or global scope lists. Note that this doesn't apply on **null** since it is used for method and action calls with no return values in mind.

$$\begin{array}{c}
\vec{\gamma}' = (\vec{\gamma}_G + + \vec{\gamma})[lval \mapsto v] \\
\gamma_G = \vec{\gamma}'[0] \\
\gamma'_G = \vec{\gamma}'[1] \\
\vec{\gamma}'' = \text{tl}(\text{tl } \vec{\gamma}')
\end{array}
\quad \frac{}{ctx \vdash (\vec{\gamma}_G, [[lval := v]]_{\vec{\gamma}}^{funn}, C, \mathbf{run}) \rightarrow ([\gamma_G, \gamma'_G], [[\emptyset_{\text{stmt}}]]_{\vec{\gamma}''}^{funn}, C, \mathbf{run})} \text{ STMT_ASS_V}$$

if then else

The STMT_COND2 and STMT_COND3 rules are the standard ones for conditional statements.

$$\frac{}{ctx \vdash (\vec{\gamma}_G, [[\mathbf{if} \top \mathbf{then} stmt_1 \mathbf{else} stmt_2]]_{\vec{\gamma}}^{funn}, C, \mathbf{run}) \rightarrow (\vec{\gamma}_G, [[stmt_1]]_{\vec{\gamma}}^{funn}, C, \mathbf{run})} \text{ STMT_COND2}$$

$$\frac{}{ctx \vdash (\vec{\gamma}_G, [[\mathbf{if} \perp \mathbf{then} stmt_1 \mathbf{else} stmt_2]]_{\vec{\gamma}}^{funn}, C, \mathbf{run}) \rightarrow (\vec{\gamma}_G, [[stmt_2]]_{\vec{\gamma}}^{funn}, C, \mathbf{run})} \text{ STMT_COND3}$$

block

The $\{\}$ brackets indicate a block, while the $[\]$ brackets indicate a block in progress or being executed.

Once a block statement is encountered the STMT_BLOCK_ENTER reduction will be used, it entails the \vec{decl} being transformed and replicated into a scope that will be appended to the local $\vec{\gamma}$ of the frame, and then the $\{\}$ brackets are switched to the in-progress ones $[\]$ to signify that the block is currently being executed.

The STMT_BLOCK_EXEC rule simply describes small-step reduction of the block contents, and the STMT_BLOCK_EXIT rule is used in the case where the end of a block is reached, i.e. whenever a block contains only an empty statement: it pops the scope corresponding to the block (the most recent one) from the scope list $\vec{\gamma}$. The block will be exited and reduced to an empty statement \emptyset_{stmt} .

$$\frac{\gamma = \text{replicate}(\overrightarrow{decl})}{\overrightarrow{\gamma}' = \overrightarrow{\gamma} + +[\gamma]} \quad \text{STMT_BLOCK_ENTER}$$

$$ctx \vdash (\overrightarrow{\gamma}_G, [(\overrightarrow{decl} \text{ stmt})]_{\overrightarrow{\gamma}}^{funn}, C, \mathbf{run}) \rightarrow (\overrightarrow{\gamma}_G, [(stmt :: [\emptyset_{\text{stmt}}])_{\overrightarrow{\gamma}'}^{funn}], C, \mathbf{run})$$

not_empty stmt

$$\frac{(X, F_g, F_b, Tb) \vdash (\overrightarrow{\gamma}_G, [([stmt])_{\overrightarrow{\gamma}}^{funn}], C, t) \rightarrow (\overrightarrow{\gamma}_G'', \overrightarrow{\Phi}' + [([stmt'])_{\overrightarrow{\gamma}'}^{funn}], C', t')}{ctx \vdash (\overrightarrow{\gamma}_G, [(stmt :: \overrightarrow{stmt})_{\overrightarrow{\gamma}}^{funn}], C, t) \rightarrow (\overrightarrow{\gamma}_G', \overrightarrow{\Phi}' + [(stmt' :: \overrightarrow{stmt})_{\overrightarrow{\gamma}'}^{funn}], C', t')} \quad \text{STMT_BLOCK_EXEC}$$

$$\frac{\overrightarrow{\gamma}' = \text{rev}((\text{tl}(\text{rev}(\overrightarrow{\gamma}))))}{ctx \vdash (\overrightarrow{\gamma}_G, [(\emptyset_{\text{stmt}} :: \overrightarrow{stmt})_{\overrightarrow{\gamma}}^{funn}], C, t) \rightarrow (\overrightarrow{\gamma}_G, [(\overrightarrow{stmt})_{\overrightarrow{\gamma}'}^{funn}], C, t)} \quad \text{STMT_BLOCK_EXIT}$$

apply

The STMT_APPLY_V describes the apply table statement. Each apply statement has a table name *tbl* with the a list of key expressions e_1, \dots, e_n to match on. This list have been previously reduced to constants one at a time in the rule STMT_APPLY_E. The Tb will return a list of match kind mk_1, \dots, mk_n . The ctrl is a function that maps table names *tbl*, expressions, and the match kinds to action that should be implemented as a method call, with a list of the arguments v_1, \dots, v_m .

$$\frac{\begin{aligned} i &= \min \{j. \neg(\text{is_const}[e_1, \dots, e_n][j])\} \\ e &= [e_1, \dots, e_n][i] \\ ctx \overrightarrow{\gamma}_G \overrightarrow{\gamma} &\vdash (e) \rightsquigarrow (e', \overrightarrow{\Phi}) \\ [e'_1, \dots, e'_n] &= (i \mapsto e')[e_1, \dots, e_n] \end{aligned}}{ctx \vdash (\overrightarrow{\gamma}_G, [([\mathbf{apply} \text{ tbl}(e_1, \dots, e_n))]_{\overrightarrow{\gamma}}^{funn}], C, \mathbf{run}) \rightarrow (\overrightarrow{\gamma}_G, \overrightarrow{\Phi} + [([\mathbf{apply} \text{ tbl}(e'_1, \dots, e'_n))]_{\overrightarrow{\gamma}}^{funn}], C, \mathbf{run})} \quad \text{STMT_APPLY_TABL}$$

$$\frac{\begin{aligned} \text{is_consts } &e_1, \dots, e_n \\ \text{Tb } &\text{tbl} = ([mk_1, \dots, mk_n]) \\ \text{C } &(\text{tbl}, (e_1, \dots, e_n), ([mk_1, \dots, mk_n])) = (f, (v_1, \dots, v_m)) \end{aligned}}{(X, F_g, F_b, Tb) \vdash (\overrightarrow{\gamma}_G, [([\mathbf{apply} \text{ tbl}(e_1, \dots, e_n))]_{\overrightarrow{\gamma}}^{funn}], C, \mathbf{run}) \rightarrow (\overrightarrow{\gamma}_G, [([\mathbf{null} := (\mathbf{call} f(v_1, \dots, v_m))]_{\overrightarrow{\gamma}}^{funn}], C, \mathbf{run})} \quad \text{STMT_}$$

return

Once return's expression is reduced to a constant value, the status is changed to a **ret** *v*, and the statement becomes \emptyset_{stmt} . The rest of the return operatoin will be handeled in the sequence and composition rules.

$$\frac{}{(X, F_g, F_b, Tb) \vdash (\vec{\gamma}_G, [([\mathbf{return} \ v])_{\vec{\gamma}}^{funn}], C, \mathbf{run}) \rightarrow (\vec{\gamma}_G, [([\emptyset_{\text{stmt}}])_{\vec{\gamma}}^{funn}], C, \mathbf{ret} \ v)} \quad \text{STMT_RETURN_V}$$

sequential statements

The sequential statements rules STMT_SEQ1 and STMT_SEQ2 are standard. The status t must be **run** to start with. The part that is different is that if any new frame being created by any function call, it will be added on top of the frame list.

Otherwise, the STMT_SEQ3 rule is used whenever the status is **ret**, **acc**, **rej** or **tra** to indicate either a transition to a parser state or a return from a function call. The rest of the sequential composition statements are not considered, this will give flexibility to the return or transition statements in the body rather than only at the end. STMT_SEQ1 and STMT_SEQ3 are able to change the tables that are populated by the control plane.

$$\frac{ctx \vdash (\vec{\gamma}_G, [([stmt_1])_{\vec{\gamma}}^{funn}], C, \mathbf{run}) \rightarrow (\vec{\gamma}_G', \vec{\Phi} + [([stmt_1'])_{\vec{\gamma}'}^{funn}], C', \mathbf{run})}{ctx \vdash (\vec{\gamma}_G, [([stmt_1; stmt_2])_{\vec{\gamma}}^{funn}], C, \mathbf{run}) \rightarrow (\vec{\gamma}_G', \vec{\Phi} + [([stmt_1'; stmt_2])_{\vec{\gamma}'}^{funn}], C', \mathbf{run})} \quad \text{STMT_SEQ1}$$

$$\frac{}{ctx \vdash (\vec{\gamma}_G, [([\emptyset_{\text{stmt}}; stmt])_{\vec{\gamma}}^{funn}], C, \mathbf{run}) \rightarrow (\vec{\gamma}_G, [([stmt])_{\vec{\gamma}}^{funn}], C, \mathbf{run})} \quad \text{STMT_SEQ2}$$

$$\frac{ctx \vdash (\vec{\gamma}_G, [([stmt_1])_{\vec{\gamma}}^{funn}], C, \mathbf{run}) \rightarrow (\vec{\gamma}_G', [([stmt_1'])_{\vec{\gamma}'}^{funn}], C', t) \quad t \neq \mathbf{run}}{ctx \vdash (\vec{\gamma}_G, [([stmt_1; stmt_2])_{\vec{\gamma}}^{funn}], C, \mathbf{run}) \rightarrow (\vec{\gamma}_G', [([stmt_1'])_{\vec{\gamma}'}^{funn}], C', t)} \quad \text{STMT_SEQ3}$$

extern

The symbol \blacksquare represents a statement capturing the semantics of an extern function other than copy-in copy-out and return behaviour. \blacksquare is able to modify the global scope list $\vec{\gamma}_G$, the local scope list $\vec{\gamma}$ and the control plane configuration C . The exact behaviour is determined by looking up the entry of the name $funn$ associated with the current frame in the extern function map X : since \blacksquare is used in extern function call bodies, $funn$ is the name of the extern function.

Note that calling an extern function works the same as calling any other function, as described in the function call subsection of Section 2.1. However, by convention the body of the extern function consists only of \blacksquare , possibly followed by a **return** statement if the extern function in question has any return value.

$$\frac{\begin{array}{l} ext_fun = lookup_ext_fun(funn, X) \\ (\vec{\gamma}_{G'}, \vec{\gamma}', C') = ext_fun(\vec{\gamma}_G, \vec{\gamma}, C) \end{array}}{(X, F_g, F_b, Tb) \vdash (\vec{\gamma}_G, [([\blacksquare])_{\vec{\gamma}}^{funn}], C, \mathbf{run}) \rightarrow (\vec{\gamma}_{G'}, [([\emptyset_{\text{stmt}}])_{\vec{\gamma}'}^{funn}], C', \mathbf{run})} \text{ STMT_EXT}$$

transition

The transition statement **transition** x makes a change to the state's status t based on the state name x . The STMT_TRANS_1 used if the state name is not a final one, then it transitions to an \emptyset_{stmt} and changes the t of the state to **tra** x . Otherwise, if the state is final (i.e. accept **acc** or reject **rej**) the rules STMT_TRANS_2 and STMT_TRANS_3 are used accordingly.

$$\frac{\text{not_final_state}(x)}{ctx \vdash (\vec{\gamma}_G, [([\mathbf{transition} \ x])_{\vec{\gamma}}^{funn}], C, \mathbf{run}) \rightarrow (\vec{\gamma}_G, [([\emptyset_{\text{stmt}}])_{\vec{\gamma}}^{funn}], C, \mathbf{tra} \ x)} \text{ STMT_TRANS_1}$$

$$\frac{x = \text{"accept"}}{ctx \vdash (\vec{\gamma}_G, [([\mathbf{transition} \ x])_{\vec{\gamma}}^{funn}], C, \mathbf{run}) \rightarrow (\vec{\gamma}_G, [([\emptyset_{\text{stmt}}])_{\vec{\gamma}}^{funn}], C, \mathbf{acc})} \text{ STMT_TRANS_2}$$

$$\frac{x = \text{"reject"}}{ctx \vdash (\vec{\gamma}_G, [([\mathbf{transition} \ x])_{\vec{\gamma}}^{funn}], C, \mathbf{run}) \rightarrow (\vec{\gamma}_G, [([\emptyset_{\text{stmt}}])_{\vec{\gamma}}^{funn}], C, \mathbf{rej})} \text{ STMT_TRANS_3}$$

verify

The **verify** ee' usually used to check the validity of the boolean expression e , if true then no error as can be seen in STMT_VERIFY_3 rule. Otherwise it assigns the error "parseError" to "reject" then it will be followed by a **transition** statement to the state reject.

$$\frac{}{ctx \vdash (\vec{\gamma}_G, [([\mathbf{verify} \top (\text{errmsg} \ x)])_{\vec{\gamma}}^{funn}], C, \mathbf{run}) \rightarrow (\vec{\gamma}_G, [([\emptyset_{\text{stmt}}])_{\vec{\gamma}}^{funn}], C, \mathbf{run})} \text{ STMT_VERIFY_3}$$

$$\frac{\begin{array}{l} x' = \text{"parseError"} \\ x'' = \text{"reject"} \end{array}}{ctx \vdash (\vec{\gamma}_G, [([\mathbf{verify} \perp (\text{errmsg} \ x)])_{\vec{\gamma}}^{funn}], C, \mathbf{run}) \rightarrow (\vec{\gamma}_G, [([x' := (\text{errmsg} \ x); \mathbf{transition} \ x''])_{\vec{\gamma}}^{funn}], C, \mathbf{run})} \text{ STMT_VER.}$$

2.3 list of frames semantics

The previous semantics operate on a single frame for a single statement, however whenever a function being called, then it means that the state will contain two frames. The top frame that

will be executed as can be seen in STMT_COMP1 reduction, where the previous frame Φ is not empty (otherwise, we can use the previous rules above). The rule STMT_COMP2 is used specifically whenever there is a return occurring in a frame in that is being reduced. Here the caller frame's scopelist $\vec{\gamma}'$ or the global scope list $\vec{\gamma}_G$ should initialize the **varstar** with the return that can be found in the state's status **ret** v . the proper scope will be updated based on the topmost scope ever that can be found after concatenating $\vec{\gamma}_G ++ \vec{\gamma}'$ to result the scope list $\vec{\gamma}''$. The copy out function takes the callee function signature, the "possibly updated" global scope list (index 0 and 1 from $\vec{\gamma}''$) the top most scope in the $\vec{\gamma}''$ and the callee's current scope list $\vec{\gamma}$. It returns a new updated scopes list for both the global and caller's frame. In the copyout function, each parameter with (out or inout directions) should be copied out to the variable name it is mapping to. i.e. the mapping of a single scope looks as $x \mapsto (v, y)$ so for each variable name x that is part of the current argument list in the signature we will check the direction of it, if the direction is (none or in) then we do not copy them out. However, if the direction is (out or inout) we will look into the mapping of it, and retrieve a tuple (v, y) which is the value and the oprginal argument of the function to be copied out in the propoer scope (top most one it is defined in).

$$\begin{array}{c}
\vec{\gamma}_G' = \text{scopes_to_pass}(funn, F_g, F_b, \vec{\gamma}_G) \\
t' \neq \mathbf{ret} \mathbf{v} \\
(X, F_g, F_b, Tb) \vdash (\vec{\gamma}_G', [(\overrightarrow{stmt})_{\vec{\gamma}}^{funn}], C, t) \rightarrow (\vec{\gamma}_G'', \vec{\Phi}', C', t') \\
\vec{\gamma}_G''' = \text{scopes_to_retrieve}(funn, F_g, F_b, \vec{\gamma}_G, \vec{\gamma}_G'') \\
\hline
(X, F_g, F_b, Tb) \vdash (\vec{\gamma}_G, [(\overrightarrow{stmt})_{\vec{\gamma}}^{funn}] ++ \vec{\Phi}'', C, t) \rightarrow_{\Phi} (\vec{\gamma}_G''', \vec{\Phi}' ++ \vec{\Phi}'', C', t') \quad \text{FRAMES_COMP1}
\end{array}$$

$$\begin{array}{c}
(X, F_g, F_b, Tb) \vdash (\vec{\gamma}_G, [(\overrightarrow{stmt})_{\vec{\gamma}}^{funn}], C, \mathbf{run}) \rightarrow (\vec{\gamma}_G, [(\overrightarrow{stmt})_{\vec{\gamma}}^{funn}], C, \mathbf{ret} v) \\
\vec{\gamma}'' = (\vec{\gamma}_G ++ \vec{\gamma}')[(\mathbf{star}, funn) \mapsto v] \\
\gamma_G = \vec{\gamma}''[0] \\
\gamma_G' = \vec{\gamma}''[1] \\
\vec{\gamma}''' = \text{tl}(\text{tl} \vec{\gamma}'') \\
(stmt''', [(x_1, d_1), \dots, (x_n, d_n)]) = \mathbf{lookup_funn_sig_body}(funn, F_g, F_b, X) \\
\vec{\gamma}_G' = \text{scopes_to_retrieve}(funn, F_g, F_b, \vec{\gamma}_G, [\gamma_G, \gamma_G']) \\
(\vec{\gamma}_G'', \vec{\gamma}''') = \text{copyout}([x_1, \dots, x_n], [d_1, \dots, d_n], \vec{\gamma}_G', \vec{\gamma}''', \vec{\gamma}) \\
\hline
(X, F_g, F_b, Tb) \vdash (\vec{\gamma}_G, [(\overrightarrow{stmt})_{\vec{\gamma}}^{funn}] ++ [(\overrightarrow{stmt}')_{\vec{\gamma}'}^{funn'}] ++ \vec{\Phi}, C, \mathbf{run}) \rightarrow_{\Phi} (\vec{\gamma}_G'', [(\overrightarrow{stmt}')_{\vec{\gamma}'''}^{funn'}] ++ \vec{\Phi}, C, \mathbf{run}) \quad \text{FRAMES_COMP2}
\end{array}$$

We define $\text{scopes_to_pass}(funn, F_g, F_b, \vec{\gamma}_G)$ as :

$$\vec{\gamma}_G' = \begin{cases} \text{if } funn \in \text{dom}(F_g) \text{ , then } [\vec{\gamma}_G[0]; \emptyset_{\gamma}] \\ \text{otherwise} \text{ , then } \vec{\gamma}_G \end{cases}$$

Where $\vec{\gamma}_G$ is a list of two elements, index 0 is the global scope, and index 1 is the programmable block global scope, it is fetched from the frame reduction rule. $\vec{\gamma}_G'$ is the scope that will be used to implement one frame at a time (statement reduction) in comp2.

We define $\text{scopes_to_retrieve}(funn, F_g, F_b, \vec{\gamma}_G, \vec{\gamma}_G')$ as :

$$\vec{\gamma}_G'' = \begin{cases} \text{if } funn \in dom(F_g) & , \text{ then } [\vec{\gamma}_G'[0], \vec{\gamma}_G[1]] \\ \text{otherwise} & , \text{ then } \vec{\gamma}_G' \end{cases}$$

Where $\vec{\gamma}_G''$ is a list of two elements, index 0 is the global scope, and index 1 is the programmable block global scope, and we will retrieve it to be added as the resulted scope of the frame reduction. $\vec{\gamma}_G'$ is the scope that is resulted from the statement reduction in comp2. $\vec{\gamma}_G$ is the original starting state's global list of the frame reduction.

2.4 Parser Block Semantics

The parser programmable block is a part of the P4 pipeline which is generally used to parse packets from bit-string representations to structures of parsed headers, described in [Section 13](#) of the P4 specification. It can be thought of as describing a state machine with three unique states: a *start* state, an **acc** state and a **rej msg** state. A parser state p (including *start*, but not the abstract final states of **acc** and **rej msg**) consists of a list of statements to be executed, with a transition statement at the end which decides the parser state to jump to next.

$$\boxed{[stmt]s \rightarrow [stmt']s'} \quad \text{statement semantics}$$

$$\frac{}{ctx \vdash (\vec{\gamma}_G, [[\mathbf{verify} \top (\mathbf{errmsg} x)]]_{\vec{\gamma}}^{funn}, C, \mathbf{run}) \rightarrow (\vec{\gamma}_G, [[[\emptyset]_{\text{stmt}}]]_{\vec{\gamma}}^{funn}, C, \mathbf{run})} \text{STMT_VERIFY_3}$$

$$\begin{array}{c}
x' = \text{"parseError"} \\
x'' = \text{"reject"}
\end{array}$$

$$\frac{}{ctx \vdash (\vec{\gamma}_G, [[\mathbf{verify} \perp (\mathbf{errmsg} x)]]_{\vec{\gamma}}^{funn}, C, \mathbf{run}) \rightarrow (\vec{\gamma}_G, [[x' := (\mathbf{errmsg} x); \mathbf{transition} x'']]_{\vec{\gamma}}^{funn}, C, \mathbf{run})} \text{STMT_VERIFY_4}$$

$$\frac{\mathbf{not_final_state}(x)}{ctx \vdash (\vec{\gamma}_G, [[[\mathbf{transition} x]]_{\vec{\gamma}}^{funn}, C, \mathbf{run}) \rightarrow (\vec{\gamma}_G, [[[\emptyset]_{\text{stmt}}]]_{\vec{\gamma}}^{funn}, C, \mathbf{tra} x)} \text{STMT_TRANS_1}$$

$$\frac{x = \text{"accept"}}{ctx \vdash (\vec{\gamma}_G, [[[\mathbf{transition} x]]_{\vec{\gamma}}^{funn}, C, \mathbf{run}) \rightarrow (\vec{\gamma}_G, [[[\emptyset]_{\text{stmt}}]]_{\vec{\gamma}}^{funn}, C, \mathbf{acc})} \text{STMT_TRANS_2}$$

$$\frac{x = \text{"reject"}}{ctx \vdash (\vec{\gamma}_G, [[[\mathbf{transition} x]]_{\vec{\gamma}}^{funn}, C, \mathbf{run}) \rightarrow (\vec{\gamma}_G, [[[\emptyset]_{\text{stmt}}]]_{\vec{\gamma}}^{funn}, C, \mathbf{rej})} \text{STMT_TRANS_3}$$

Figure 7: P4 Parser-Specific Statement Execution Semantics

The parser-specific statement semantics is shown in Figure 7. The STMT_VERIFY_3 and STMT_VERIFY_4 rules describe the semantics of **verify**, the expressions having been reduced to values. If the condition holds, the reduction is to the empty statement (i.e. nothing happens and execution continues). If the condition does not hold, reduction is also to the empty statement, but state status is set to **Reject** x . The STMT_TRANS rules describe reduction of the **transition** statement, whose only effect on the state is to set status to indicate next parser state (the PARS_STATE or PARS_T_FIN rules can then be used next)

The parser state machine semantics is shown in Figure 8.

The PARS_STMT rule performs a single small-step reduction of the current statement (the body of the current parser state), while the PARS_STATE rule governs transition to the next parser state: if the current statement $stmt$ is reduced to $stmt'$ with the status being **tra** x , the next statement is the body of the parser state with name x , obtained from the map P from parser state names to parser bodies.

The PARS_T_FIN rule says that when reduction using the statement semantics of the current statement results in a status with a final parser state p_{fin} , this is also set as the status in the parser semantics. The PARS_T_EMPTY rule covers the special case when the statement semantics runs out of statements in a parser state, in which case the status is set to **rej** ParserStateEnd.

$$\boxed{ctx_P \vdash s \longrightarrow_P s'} \quad \text{parser block semantics}$$

$$\frac{\text{parser_not_finished } \vec{\Phi} \quad (X, F_g, F_b, \mathbf{empty}) \vdash (\vec{\gamma}_G, \vec{\Phi}, C, \mathbf{run}) \longrightarrow_{\Phi} s'}{(X, F_g, F_b, P) \vdash (\vec{\gamma}_G, \vec{\Phi}, C, \mathbf{run}) \longrightarrow_P s'} \quad \text{PARS_STMT}$$

$$\frac{stmt'' = P(x)}{(X, F_g, F_b, P) \vdash (\vec{\gamma}_G, \vec{\Phi}, C, \mathbf{tra } x) \longrightarrow_P (\vec{\gamma}_G, [([stmt''])_{[\gamma_\emptyset]}^x], C, \mathbf{run})} \quad \text{PARS_STATE}$$

$$\frac{x = \text{"reject"}}{(X, F_g, F_b, P) \vdash (\vec{\gamma}_G, [([\emptyset_{\text{stmt}}])_{\vec{\gamma}}^{funn}], C, \mathbf{run}) \longrightarrow_P (\vec{\gamma}_G, [([\mathbf{transition } x])_{\vec{\gamma}}^{funn}], C, \mathbf{run})} \quad \text{PARS_EMPTY}$$

Figure 8: Parser Block-Level Semantics

2.5 Control Block Semantics

The control block is a part of the P4 pipeline which is generally used to decide which actions to take (typically forwarding) based on the metadata (headers) which was extracted by the parser, as described in [Section 12](#) of the P4 specification. The two main components of a control block are the match-action tables and the actions themselves. Note that part of the functionality is separated into the control plane, which is interfaced with here using the `ctrl(table_name, v, m_kind)` function that takes a table name, constant value and matching kind and obtains an action name f and a list of function arguments v_1, \dots, v_n . Actions can be thought of roughly as functions with no return values. The action can be called implicitly from the match-action process (i.e. in the table application), or explicitly from another action or a control block, as described in [Section 13.1.1](#) of the P4 specification.

The `APPLY_TABLE_E` rule performs small-step evaluation of the header expression used for the matching.

The `APPLY_TABLE_V` looks up the table name in the table name map, then uses the result together with the header to be looked up to obtain an action (together with action arguments) from the control plane.

The statement semantics unique to the control block is shown in [Figure 9](#). Note that the body of the control block consists of the statements inside the apply block, and that executing a return statement at this level signifies return from the control block as a whole. The block-level semantics of the control block are shown in [Figure 10](#). It's almost the same as the statement semantics, the only difference being that the return statement causes an immediate reduction to the empty statement.

2.6 Architecture-Level Semantics

The architecture-level semantics is the topmost-level semantics, and it describes the entirety of the P4 pipeline from input packets to output packets. The judgment form, shown at the top of [Figure 11](#), consists of multiple components: starting from the left, an *architectural context* ctx_A (which contains everything not changed by reduction steps) on the left-hand side of the turnstile, which contains the following:

$$\begin{array}{c}
i = \min \{j. \neg (\text{is_const } [e_1, \dots, e_n][j])\} \\
e = [e_1, \dots, e_n][i] \\
ctx \vec{\gamma}_G \vec{\gamma} \vdash (e) \rightsquigarrow (e', \vec{\Phi}) \\
[e'_1, \dots, e'_n] = (i \mapsto e')[e_1, \dots, e_n] \\
\hline
ctx \vdash (\vec{\gamma}_G, [[\text{apply tbl}(e_1, \dots, e_n)]^{\text{funn}}]_{\vec{\gamma}}, C, \text{run}) \rightarrow (\vec{\gamma}_G, \vec{\Phi} + [[\text{apply tbl}(e'_1, \dots, e'_n)]^{\text{funn}}]_{\vec{\gamma}}, C, \text{run}) \quad \text{STMT_APPLY_TABL}
\end{array}$$

$$\begin{array}{c}
\text{is_consts } e_1, \dots, e_n \\
Tb \text{tbl} = ([mk_1, \dots, mk_n]) \\
C(\text{tbl}, (e_1, \dots, e_n), ([mk_1, \dots, mk_n])) = (f, (v_1, \dots, v_m)) \\
\hline
(X, F_g, F_b, Tb) \vdash (\vec{\gamma}_G, [[\text{apply tbl}(e_1, \dots, e_n)]^{\text{funn}}]_{\vec{\gamma}}, C, \text{run}) \rightarrow (\vec{\gamma}_G, [[\text{null} := (\text{call } f(v_1, \dots, v_m))]^{\text{funn}}]_{\vec{\gamma}}, C, \text{run}) \quad \text{STMT_}
\end{array}$$

Figure 9: Match Action Execution Semantics

$$\boxed{ctx_C \vdash s \longrightarrow_c s'} \quad \text{control block semantics}$$

$$\frac{(X, F_g, F_b, Tb) \vdash (\vec{\gamma}_G, \vec{\Phi}, C, \text{run}) \longrightarrow_{\Phi} (\vec{\gamma}_G', \vec{\Phi}', C', t')}{(X, F_g, F_b, Tb) \vdash (\vec{\gamma}_G, \vec{\Phi}, C, \text{run}) \longrightarrow_c (\vec{\gamma}_G', \vec{\Phi}', C', t')} \quad \text{CTRL_STMT}$$

Figure 10: Control Block-Level Semantics

1. The *architectural block list* \overline{ab} : an architectural block represents a stage of packet processing. There are four fundamental stages of packet processing in a P4-compatible architecture. First, there are the input (**inp**) and output (**out**) stages: these stages just perform translation between the architectural packet format and the generic I/O format (consisting of a list of ports, each with pending packets to be arbitrated/sent off)³. Second, there are the programmable blocks (parser blocks and control blocks, which are described in more detail elsewhere in this document) and the fixed-function blocks. Fixed-function blocks, like their name implies, perform the parts of packet processing in the P4-programmable network element that are actually not P4-programmable.
2. The *programmable block map* B_p , which is a partial map between names of programmable block names (strings) and the all necessary items that model the block in question. For parser blocks, this is the list of directed parameters, a statement representing the declarations and instantiations done at the block-global level, and the parser state map P between parser state names and their bodies (statements)⁴. For control blocks, this is the list of directed parameters, a statement representing the declarations and instantiations done at the block-global level, the body of the control block (found in the topmost apply statement)⁵ and a table map Tb between names of tables and tuples of expressions and matching kinds.

³The demux block functionality may be modeled as part of the output stage, or it may be its own fixed-function block preceding the output stage

⁴Note that **p4ott** represents all parser state bodies as encased in a block

⁵Note that **p4ott** represents the content of the apply as encased in a block

DL: This currently just a list, not a list of lists. Fix the Ott+HOL4, then env_A below to agree with the description here.

3. The *fixed-function block map* B_{ff} , which is a partial map between names of fixed-function blocks to the implementation of the fixed function itself, which is a partial function that maps the architectural scope to an updated architectural scope.
4. The *input function* f_{in} and the *output function* f_{out} , which are both partial functions from an IO list and the architectural scope to an updated IO list and architectural scope. They are used in the input and output stages.
5. The *programmable block copy-in function* $copyin_{pbl}$ and the *programmable block copy-out function* $copyout_{pbl}$: $copyin_{pbl}$ is a partial function from a list of directed arguments, a list of expressions and the architectural scope to a scope (the block-global scope of the programmable block). $copyout_{pbl}$ is a partial function from the global scopes list, a list of directed arguments and the architectural scope to an updated architectural scope.
6. The *extern object map* X , which is a partial map from extern object names to tuples of extern function maps (a map holding the extern functions of the object: tuples of directed parameter lists, function bodies, and the extern implementation), and an optional constructor (holding a tuple same as that of the extern function map).
7. The *function map* F , which is a partial map from function names to tuples of function bodies (statements) and their directed parameter lists.

DL: Why not include control plane configuration?

The left-hand and right-hand sides of the reduction both have the same elements.

1. The *architectural environment* env_A has five components:
 - (a) The *architectural block index*: This is an index which informs us of which architectural block in \overline{ab} is currently being reduced.
 - (b) The *input list*, which is a list of incoming packets (represented as tuples of lists of Booleans and numbers signifying input ports)
 - (c) The *output list*, which is the same as the input list, but used for output.
 - (d) The *architectural scope* γ_A , which is of polymorphic type, and is used for storing things in-between the programmable blocks, as well as things that are not accessible directly by P4 code.
2. The global scope list $\bar{\gamma}_G$ containing the top-level global scope with constants common to all programmable blocks as well as the block-global scope containing variables declared at the start of programmable blocks.
3. The *architecture-level frame list*: this contains either a regular frame list, or a special empty architecture-level frame list ($[]_A$).

DL: Three solutions to the problem of recognizing in-progress blocks: (1) add a Boolean in-progress flag to the state, (2) use an architecture-level frame list with this special symbol (2) use a regular empty frame list and special checks in the EXEC rules. Solution 2 is probably best, since it takes up the least additional space in the rules (no additional premise or tuple element)

DL: This could be assigned as symbol instead of being called "arch_frame_list" in the judgment

4. The control-plane configuration C , a partial function from strings, values and match kinds to tuples of strings and expression lists.
5. The status, which (other than the functionality on other levels of the semantics) informs us of whether a parser state machine is finished, or the apply of a control block is finished.

The rules of the architecture-level semantics are the following:

1. **ARCH_IN**: The first antecedent requires that the pending architecture block is **inp**, and then f_{in} updates the input list and the architectural scope.
2. **ARCH_OUT**: Similar to the above, but the pending architecture block must be **out**, and then the output list and the architectural scope are updated by f_{out} .
3. **ARCH_PARSER_INIT**: when the pending architecture block is a parser block. Then, a new block-global scope γ' is created and initialised with the arguments of the parser block as well as *parseError*, which is initially set to *NoError*. The result is appended to the top-level global scope. The architecture frame list is set to a single frame where the statement is the declarations of the parser block *stmt* followed by the body of the *start* block *stmt'*, the scopes stack is the empty list and the current function name is the name of the parser block.
4. **ARCH_CONTROL_INIT** is similar, but simpler: here, *parseError* is not set, and the body of the *start* block is replaced with the body of the apply *stmt'*.
5. **ARCH_FFBL** handles the fixed-function block: when the pending architecture block is a fixed-function block with name x , the implementation ff of the fixed-function is looked-up in B_{ff} using x , after which it is used to update the architectural scope. Also, the architecture block index is incremented.
6. **ARCH_PARSER_EXEC** describes the reduction of the parser block: the first two premises are there to ensure the current block is a parser block. It is also required that the status t not be *pars_fin*, i.e., the parser has not yet finished. Then, the global block list, the frame list, the control plane configuration and the status are updated by a parser reduction step \rightarrow_p .
7. **ARCH_CONTROL_EXEC** is similar to the above, but ensures the current block is a control block, after which the control reduction step \rightarrow_c is used to update the architectural state.
8. **ARCH_PARSER_RET** rule describes the final step of parser block reduction. The first two premises ensure the current block is a parser block and provide the directed parameters for $copyout_{pbl}$ to update the architectural scope. Furthermore, the architecture block list index is incremented by 1 and the block-global scope is dropped from the global scopes list.
9. **ARCH_CONTROL_RET** is analogous to the above in every aspect, but for control blocks.

$$\boxed{ctx_A \vdash (env_A, \vec{\gamma}_G, arch_frame_list, C, t) \longrightarrow_A (env_{A'}, \vec{\gamma}_{G'}, arch_frame_list', C', t')}$$

architecture-level semantics

$$\begin{array}{c}
\text{inp} = \overline{ab}[i] \\
(\vec{i\overline{o}}'', \gamma_{A'}) = in_A(\vec{i\overline{o}}, \gamma_A) \\
\hline
(\overline{ab}, B_p, B_{ff}, in_A, out_A, in_p, out_p, X, F_g) \vdash ((i, \vec{i\overline{o}}, \vec{i\overline{o}}', \gamma_A), \vec{\gamma}_G, [\]_A, C, \mathbf{run}) \longrightarrow_A ((i+1, \vec{i\overline{o}}'', \vec{i\overline{o}}', \gamma_{A'}), \vec{\gamma}_{G'}, [\]_A, C, \mathbf{run}) \quad \text{ARCH_IN}
\end{array}$$

$$\begin{array}{c}
\text{pbl } f(e_1, \dots, e_n) = \overline{ab}[i] \\
\text{parser}((x_1, d_1), \dots, (x_n, d_n)) F_b \overrightarrow{decl} stmt P = B_p(f) \\
x = \text{"start"} \\
stmt' = P(x) \\
\gamma' = in_p((x_1, \dots, x_n), [d_1, \dots, d_n], [e_1, \dots, e_n], \gamma_A) \\
v = NoError \\
x' = parseError \\
[\gamma''] = initialise([\gamma'], x', v) \\
\gamma''' = replicate(decl, \gamma'') \\
\gamma_G'' = \vec{\gamma}_G[0] \\
\vec{\gamma}_{G'}' = [\gamma_G''] + +[\gamma'''] \\
\hline
(\overline{ab}, B_p, B_{ff}, in_A, out_A, in_p, out_p, X, F_g) \vdash ((i, \vec{i\overline{o}}, \vec{i\overline{o}}', \gamma_A), \vec{\gamma}_G, [\]_A, C, \mathbf{run}) \longrightarrow_A ((i, \vec{i\overline{o}}, \vec{i\overline{o}}', \gamma_A), \vec{\gamma}_{G'}'', [[stmt; stmt']]_{[\gamma_\emptyset]}^f, C, \mathbf{run}) \quad \text{ARCH_PARSER_INIT}
\end{array}$$

$$\begin{array}{c}
\text{pbl } f(e_1, \dots, e_n) = \overline{ab}[i] \\
\text{control}((x_1, d_1), \dots, (x_n, d_n)) F_b \overrightarrow{decl} stmt stmt' Tb = B_p(f) \\
\gamma' = in_p((x_1, \dots, x_n), [d_1, \dots, d_n], [e_1, \dots, e_n], \gamma_A) \\
\gamma'' = replicate(decl, \gamma') \\
\gamma_G'' = \vec{\gamma}_G[0] \\
\vec{\gamma}_{G'}' = [\gamma_G''] + +[\gamma''] \\
\hline
(\overline{ab}, B_p, B_{ff}, in_A, out_A, in_p, out_p, X, F_g) \vdash ((i, \vec{i\overline{o}}, \vec{i\overline{o}}', \gamma_A), \vec{\gamma}_G, [\]_A, C, \mathbf{run}) \longrightarrow_A ((i, \vec{i\overline{o}}, \vec{i\overline{o}}', \gamma_A), \vec{\gamma}_{G'}'', [[stmt; stmt']]_{[\gamma_\emptyset]}^f, C, \mathbf{run}) \quad \text{ARCH_CONTROL_INIT}
\end{array}$$

$$\begin{array}{c}
\text{ffbl } x = \overline{ab}[i] \\
ff = B_{ff}(x) \\
\gamma_{A'}' = ff(\gamma_A) \\
\hline
(\overline{ab}, B_p, B_{ff}, in_A, out_A, in_p, out_p, X, F_g) \vdash ((i, \vec{i\overline{o}}, \vec{i\overline{o}}', \gamma_A), \vec{\gamma}_G, [\]_A, C, \mathbf{run}) \longrightarrow_A ((i+1, \vec{i\overline{o}}, \vec{i\overline{o}}', \gamma_{A'}'), \vec{\gamma}_{G'}, [\]_A, C, \mathbf{run}) \quad \text{ARCH_FFBL}
\end{array}$$

$$\begin{array}{c}
\text{out} = \overline{ab}[i] \\
(\vec{i\overline{o}}'', \gamma_{A'}) = out_A(\vec{i\overline{o}}, \gamma_A) \\
\hline
(\overline{ab}, B_p, B_{ff}, in_A, out_A, in_p, out_p, X, F_g) \vdash ((i, \vec{i\overline{o}}, \vec{i\overline{o}}', \gamma_A), \vec{\gamma}_G, [\]_A, C, \mathbf{run}) \longrightarrow_A ((0, \vec{i\overline{o}}, \vec{i\overline{o}}'', \gamma_{A'}'), \vec{\gamma}_{G'}, [\]_A, C, \mathbf{run}) \quad \text{ARCH_OUT}
\end{array}$$

$$\begin{array}{c}
\text{pbl } x(e_1, \dots, e_n) = \overline{ab}[i] \\
\text{parser}((x_1, d_1), \dots, (x_n, d_n)) F_b \overrightarrow{decl} stmt P = B_p(x) \\
t \neq \text{pars_fin} \\
(X, F_g, F_b, P) \vdash (\vec{\gamma}_G, \vec{\Phi}, C, t) \longrightarrow_p (\vec{\gamma}_{G'}', \vec{\Phi}', C', t') \\
\hline
(\overline{ab}, B_p, B_{ff}, in_A, out_A, in_p, out_p, X, F_g) \vdash ((i, \vec{i\overline{o}}, \vec{i\overline{o}}', \gamma_A), \vec{\gamma}_G, \vec{\Phi}, C, t) \longrightarrow_A ((i, \vec{i\overline{o}}, \vec{i\overline{o}}', \gamma_A), \vec{\gamma}_{G'}', \vec{\Phi}', C', t') \quad \text{ARCH_PARSER_EXEC}
\end{array}$$

$$\begin{array}{c}
\text{pbl } x(e_1, \dots, e_n) = \overline{ab}[i] \\
\text{control}((x_1, d_1), \dots, (x_n, d_n)) F_b \overrightarrow{decl} stmt stmt' Tb = B_p(x) \\
(X, F_g, F_b, Tb) \vdash (\vec{\gamma}_G, \vec{\Phi}, C, \mathbf{run}) \longrightarrow_c (\vec{\gamma}_{G'}', \vec{\Phi}', C', t') \\
\hline
(\overline{ab}, B_p, B_{ff}, in_A, out_A, in_p, out_p, X, F_g) \vdash ((i, \vec{i\overline{o}}, \vec{i\overline{o}}', \gamma_A), \vec{\gamma}_G, \vec{\Phi}, C, \mathbf{run}) \longrightarrow_A ((i, \vec{i\overline{o}}, \vec{i\overline{o}}', \gamma_A), \vec{\gamma}_{G'}', \vec{\Phi}', C', t') \quad \text{ARCH_CONTROL_EXEC}
\end{array}$$

$$\begin{array}{c}
\text{pbl } f(e_1, \dots, e_n) = \overline{ab}[i] \\
\text{parser}((x_1, d_1), \dots, (x_n, d_n)) F_b \overrightarrow{decl} stmt P = B_p(f) \\
\gamma_{A'}' = out_p([\gamma_G, \gamma_G'], \gamma_A, [d_1, \dots, d_n], (x_1, \dots, x_n)) \\
\hline
(\overline{ab}, B_p, B_{ff}, in_A, out_A, in_p, out_p, X, F_g) \vdash ((i, \vec{i\overline{o}}, \vec{i\overline{o}}', \gamma_A), [\gamma_G, \gamma_G'], \vec{\Phi}, C, p_{\text{fin}}) \longrightarrow_A ((i+1, \vec{i\overline{o}}, \vec{i\overline{o}}', \gamma_{A'}'), [\gamma_G], [\]_A, C, \mathbf{run}) \quad \text{ARCH_PARSER_RET}
\end{array}$$

$$\begin{array}{c}
\text{pbl } f(e_1, \dots, e_n) = \overline{ab}[i] \\
\text{control}((x_1, d_1), \dots, (x_n, d_n)) F_b \overrightarrow{decl} stmt stmt' Tb = B_p(f) \\
\gamma_{A'}' = out_p([\gamma_G, \gamma_G'], \gamma_A, [d_1, \dots, d_n], (x_1, \dots, x_n)) \\
\hline
(\overline{ab}, B_p, B_{ff}, in_A, out_A, in_p, out_p, X, F_g) \vdash ((i, \vec{i\overline{o}}, \vec{i\overline{o}}', \gamma_A), [\gamma_G, \gamma_G'], \vec{\Phi}, C, \mathbf{ret } v) \longrightarrow_A ((i+1, \vec{i\overline{o}}, \vec{i\overline{o}}', \gamma_{A'}'), [\gamma_G], [\]_A, C, \mathbf{run}) \quad \text{ARCH_CONTROL_RET}
\end{array}$$

Figure 11: Architecture-Level Semantics

A Concrete Syntax of Operations

\ominus	$::=$	
		!
		\neg
		$-$
		$+$
		negation
		bitwise complement
		signed negation
		unary plus

Figure 12: P4 Unary Operations

The unary expressions included are shown in Figure 12. These include all of the unary operations in P4. Boolean negation is only defined on Booleans, the other operations have their standard meanings (note that [unary plus is a no-op](#)).

\oplus	$::=$	
		\times multiplication
		$/$ division
		mod modulo
		$+$ addition
		$-$ subtraction
		\ll logical left-shift
		\gg logical right-shift
		\leq less or equal
		\geq greater or equal
		$<$ less
		$>$ greater
		\neq not equal
		$=$ equal
		$\&$ bitwise and
		$\underline{\vee}$ bitwise xor
		$ $ bitwise or
		\wedge binary and
		\vee binary or

Figure 13: P4 Binary Operations

The binary expressions included are shown in Figure 12. These include all of the binary operations in P4.

B Semantics of Expression Reduction

This appendix describes semantics for reducing expressions in certain contexts. The expression semantics are shown in Figure 14. The statement semantics are shown in Figure 15.

The `E_FUNC_CALL_ARGS` rule reduces the leftmost function argument which has yet to be reduced to a constant with one expression evaluation step. The first two antecedents divide the list of arguments into two sub-lists, where the prefix must contain all constants. The head of the suffix is then reduced with one step, after which the corresponding index in the original list of arguments is update with the resulting expression.

8.1 of the P4 specification states that expressions are evaluated left-to-right. Accordingly, the rules for binary operations - `E_BINOP1` and `E_BINOP2` - are split up so that reduction of the second operand requires that the first operand has been completely reduced to a constant. This is trivial for unary operations (`E_UNOP`).

References

- [1] Ryan Doenges et al. “Petr4: formal foundations for p4 data planes”. In: *Proceedings of the ACM on Programming Languages* 5.POPL (2021), pp. 1–32.

$$\boxed{[e](\sigma) \rightsquigarrow [e'](\sigma')} \quad \text{expression semantics}$$

$$\frac{ctx \, \overrightarrow{\gamma}_G \, \overrightarrow{\gamma} \vdash (e) \rightsquigarrow (e', \overrightarrow{\Phi})}{ctx \, \overrightarrow{\gamma}_G \, \overrightarrow{\gamma} \vdash (\text{select } e\{v_1 : x_1; \dots; v_n : x_n\}x) \rightsquigarrow (\text{select } e'\{v_1 : x_1; \dots; v_n : x_n\}x, \overrightarrow{\Phi})} \quad \text{E_SEL_ARG}$$

$$\frac{ctx \, \overrightarrow{\gamma}_G \, \overrightarrow{\gamma} \vdash (e) \rightsquigarrow (e', \overrightarrow{\Phi})}{ctx \, \overrightarrow{\gamma}_G \, \overrightarrow{\gamma} \vdash (\ominus e) \rightsquigarrow (\ominus e', \overrightarrow{\Phi})} \quad \text{E_UNOP_ARG}$$

$$\frac{ctx \, \overrightarrow{\gamma}_G \, \overrightarrow{\gamma} \vdash (e) \rightsquigarrow (e'', \overrightarrow{\Phi})}{ctx \, \overrightarrow{\gamma}_G \, \overrightarrow{\gamma} \vdash (e \oplus e') \rightsquigarrow (e'' \oplus e', \overrightarrow{\Phi})} \quad \text{E_BINOP_ARG1}$$

$$\frac{\text{is_short_circuit}(\oplus) \quad ctx \, \overrightarrow{\gamma}_G \, \overrightarrow{\gamma} \vdash (e) \rightsquigarrow (e', \overrightarrow{\Phi})}{ctx \, \overrightarrow{\gamma}_G \, \overrightarrow{\gamma} \vdash (v \oplus e) \rightsquigarrow (v \oplus e', \overrightarrow{\Phi})} \quad \text{E_BINOP_ARG2}$$

Figure 14: Expression Reduction-of-Argument Semantics

$$\boxed{[stmt]s \rightarrow [stmt']s'} \quad \text{statement semantics}$$

$$\frac{ctx \, \overrightarrow{\gamma}_G \, \overrightarrow{\gamma} \vdash (e) \rightsquigarrow (e', \overrightarrow{\Phi})}{ctx \vdash (\overrightarrow{\gamma}_G, [([\text{return } e)]_{\overrightarrow{\gamma}}^{funn}], C, \text{run}) \rightarrow (\overrightarrow{\gamma}_G, \overrightarrow{\Phi} + [([\text{return } e')]_{\overrightarrow{\gamma}}^{funn}], C, \text{run})} \quad \text{STMT_RET_E}$$

$$\frac{ctx \, \overrightarrow{\gamma}_G \, \overrightarrow{\gamma} \vdash (e) \rightsquigarrow (e', \overrightarrow{\Phi})}{ctx \vdash (\overrightarrow{\gamma}_G, [([lval := e])_{\overrightarrow{\gamma}}^{funn}], C, \text{run}) \rightarrow (\overrightarrow{\gamma}_G, \overrightarrow{\Phi} + [([lval := e'])_{\overrightarrow{\gamma}}^{funn}], C, \text{run})} \quad \text{STMT_ASS_E}$$

$$\frac{ctx \, \overrightarrow{\gamma}_G \, \overrightarrow{\gamma} \vdash (e) \rightsquigarrow (e', \overrightarrow{\Phi})}{ctx \vdash (\overrightarrow{\gamma}_G, [([\text{if } e \text{ then } stmt_1 \text{ else } stmt_2])_{\overrightarrow{\gamma}}^{funn}], C, \text{run}) \rightarrow (\overrightarrow{\gamma}_G, \overrightarrow{\Phi} + [([\text{if } e' \text{ then } stmt_1 \text{ else } stmt_2])_{\overrightarrow{\gamma}}^{funn}], C, \text{run})} \quad \text{STMT_C}$$

$$\frac{ctx \, \overrightarrow{\gamma}_G \, \overrightarrow{\gamma} \vdash (e) \rightsquigarrow (e'', \overrightarrow{\Phi})}{ctx \vdash (\overrightarrow{\gamma}_G, [([\text{verify } e \, e'])_{\overrightarrow{\gamma}}^{funn}], C, \text{run}) \rightarrow (\overrightarrow{\gamma}_G, \overrightarrow{\Phi} + [([\text{verify } e'' \, e'])_{\overrightarrow{\gamma}}^{funn}], C, \text{run})} \quad \text{STMT_VERIFY_E1}$$

$$\frac{ctx \, \overrightarrow{\gamma}_G \, \overrightarrow{\gamma} \vdash (e) \rightsquigarrow (e', \overrightarrow{\Phi})}{ctx \vdash (\overrightarrow{\gamma}_G, [([\text{verify } b \, e])_{\overrightarrow{\gamma}}^{funn}], C, \text{run}) \rightarrow (\overrightarrow{\gamma}_G, \overrightarrow{\Phi} + [([\text{verify } b \, e'])_{\overrightarrow{\gamma}}^{funn}], C, \text{run})} \quad \text{STMT_VERIFY_E2}$$

Figure 15: Statement Reduction-of-Argument Semantics