

The HOL4P4 P4 Formalization

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This is a description of the HOL4P4 formalization of P4, which includes a syntax and a strictly small-step style semantics. It is based on [the official P4 specification](#) and inspired by Core P4 [1].

HOL4P4 is constructed using the `ott` tool. `ott` files can then be exported to L^AT_EX commands (used in this document) as well as to the HOL4, Isabelle/HOL and Coq interactive theorem provers (of which only the first is currently supported).

1 Syntax

1.1 Types

x, f, tbl	string
b	boolean
bl	bit-string
i, w	natural number
m, n, o	indices

Figure 1: Variables

The variables shown in Figure 1 are standard designations for variables of [P4 base types](#) included in HOL4P4, plus the numerals i (sometimes w when referring to width) and the indices m, n, o which are not part of the P4 syntax, but used on a meta-level throughout this formalization. Depending on the context, strings are denoted with x (variable or parser state name), tbl (match-action table name) or f (function or field name). bl is a list of Boolean values, used to represent bit-strings of fixed width.

P4 types are sometimes explicitly referenced in the syntax, e.g. in declaration statements. The notation for this is shown in Figure 2.

1.2 Expressions

HOL4P4 includes a subset of the full set of P4 expressions found in [Section 8](#) of the P4 specification, shown in Figure 4.

First, an expression can be a value v , the types of which are shown in Figure 3. Lists of expressions can be used in initialisation of variables of struct types. The fields of these structs

τ	$::=$	type
	bool	boolean
	bs^{num_exp}	bit-string
	\perp	no value
	$struct \ \overline{x_list} \ ty[x_1 \ \tau_1, \dots, x_n \ \tau_n]$	struct
	err	
	ext	

Figure 2: Types

v	$::=$	value
$boolv$		boolean value
$bitv$		bit-string
x		string literal
struct $\{x_1 = v_1; \dots; x_n = v_n\}$		struct
header $boolv\{x_1 = v_1; \dots; x_n = v_n\}$		header
errmsg x		error message
ext_ref i		extern object reference
bot		no value

Figure 3: P4 Values

e	$::=$	expression
v		value
var $varn$		variable
$\{e_1, \dots, e_n\}$		expression list
$e.x$		field access
$\ominus e$		unary operation
$e_1 \oplus e_2$		binary operation
concat $e_1 \ e_2$		concatenation of bit-strings
$e_1[e_2 : e_3]$		bit-slice
call $funn(e_1, \dots, e_n)$		function or extern call
select $e\{v_1 : x_1; \dots; v_n : x_n\}x$		select
eStruct $\{x_1 = e_1; \dots; x_n = e_n\}$		struct
eHeader $boolv\{x_1 = e_1; \dots; x_n = e_n\}$		header
(e)		S

Figure 4: P4 Expressions

may be accessed, which is denoted in the usual manner. There exist unary and binary arithmetic operations, where the semantics of the individual operations are defined on some subset of the constants¹. Bitstrings can be concatenated and sliced. The function call is built from the abstract function name *funn*, and a list of arguments (expressions).

The **select** expression is similar to a switch statement in C or Java. The expression *e* is

¹The concrete syntax of the many unary and binary operations is found in Appendix A

evaluated, and then matched against v_1, \dots, v_n . If some match is successful, the **select** expression evaluates to the string at the corresponding index. If no match occurs, then it instead evaluates to the default string x .

Furthermore, structs and headers can also have expression values assigned to their keys, and so be expressions (separate from the struct and header values).

1.3 Statements

<i>stmt</i>	$::=$	statement
	\emptyset_{stmt}	empty statement
	$lval := e$	assignment
	if e then $stmt_1$ else $stmt_2$	conditional
	$\{\overline{decl} \text{ } stmt\}$	block
	return e	return
	$stmt_1; stmt_2$	sequence
	verify $e \text{ } e'$	verify
	transition e	transition
	apply $tbl(e_1, \dots, e_n)$	apply
	■	extern function

Figure 5: P4 Statements

HOL4P4 includes a subset of the full set of P4 statements found in [Section 11](#) of the P4 specification, shown in [Figure 5](#). The statements in [Figure 5](#) can be found in the specification, with the following exceptions: the block, verify, apply and extern statements. The block statement features an additional list of declarations (note the absence of declarations from this syntax, in contrast to the P4 specification), the apply statement (replacing the apply method in [Section 11](#) of the P4 specification) features the list of arguments that are to be matched to the table *tbl* (which is possible to resolve at compile time), and the extern captures the semantics of an extern function or method. The **verify** statement (modeled as a statement and not as an extern function as in [Section 12.7](#) of the P4 specification) can be found uniquely in a parser block. It asserts the expression e and if it holds, does nothing. If e does not hold, it jumps to the ‘reject’ parser state with the error message being the result of evaluating e' .

<i>lval</i>	$::=$	
	$varn$	variable name
	null	null variable
	$lval.f$	field access
	$(lval)$	

Figure 6: P4 l-values

lvals are shown in [Figure 6](#) and include variables identified by their names, a null variable (used to model method calls) and struct fields, which are identified by the struct and field names, similar to the field access expression.

1.4 Execution State

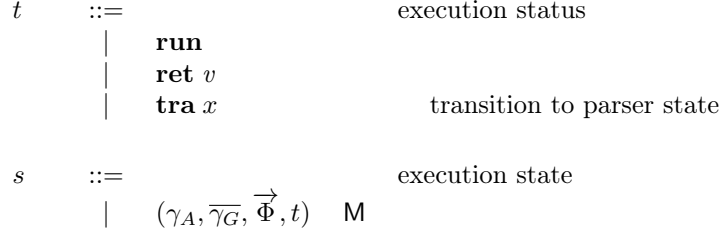


Figure 7: P4 Execution State

The P4 execution state is shown in Figure 7. Note that a “P4 execution state” is not defined in the P4 specification, so it is entirely an artifice of the H0L4P4 implementation. In short, the execution state s is a tuple of the architectural state γ_A , the global scope list $\overline{\gamma_G}$, a frame list $\overrightarrow{\Phi}$, and the state status t .

Single scope

More formally, a scope $\gamma : X \hookrightarrow V * (X \cup \{\perp\})$ is a partial function from variable names $x \in X$ to constant values $v \in V$. The following operations can be performed on γ :

- $\text{dom}(\gamma)$: Gets the domain of γ : obtains the set of variable names $x \in X$ which are mapped to values in γ .
- $(x \mapsto v) \gamma$: Updates a variable mapping in γ : yields the scope γ' , which is just γ where x instead maps to v . By writing $\forall i \leq n. (x_i \mapsto v_i) \gamma$ we extend this to lists of mappings from variable names to values.

Scope list

$\overline{\gamma}$ is a list of scopes $\gamma_1, \dots, \gamma_n$ where the index 1 is the most recently entered scope, and n is the oldest.

Global scope list

The global scope list $\overline{\gamma_G}$ contains two elements; index 0 represents the programmable block local scope, while index 1 represents the global scope of the architecture. Initially when a programmable block is entered, for each function declared globally in the architecture or locally in a programmable block a return variable is declared in the programmable block local scope.

List of statements

The list of statement \overline{stmt} is simply a list of $stmt_1, \dots, stmt_n$ where $stmt_1$ belongs to the most recent block we have entered, and $stmt_n$ is the oldest one.

Whenever a block $\{\overline{decl } stmt_b\}; stmt$ is encountered, the block’s statement $stmt_b$ will be appended to the old statement $stmt$ leaving an empty statement in its original place. So the next transition will become $stmt_b :: (\emptyset_{stmt}; stmt$

Function name

Function name $funn$ identifies the function name or the extern object instantiation. In P4, functions can be declared globally, or locally in the programmable block (actions). We model the actions exactly as the functions, and to distinguish between them we store their signatures and bodies in two different locations. The (programmable block locally declared functions) signature and body in the mapping F_b . The (globally declared functions and actions) signatures and bodies are stored in the mapping F_g . Both function block mapping F_b and global block mapping F_g are stored in the context ctx which will be discussed later.

Frame

The frame Φ is a tuple of three members: a statement list \overline{stmt} , a function name $funn$, and a local to the frame scope list $\bar{\gamma}$. Overall, a frame Φ is represented as $\overline{stmt}_{\bar{\gamma}}^{funn}$. Whenever a function call occurs, the callee function will be appended to the caller as $\Phi_{callee} :: \bar{\Phi}$.

Control plane configuration

It is a function that takes a table name tbl , the key list expressions e_1, \dots, e_n , and a list of matching kinds mk_1, \dots, mk_n then returns the actions and their arguments. This will be explained further in the statements semantics.

Status

Represented in the state with t . The status **run** represents that the program is executing under regular circumstances. **ret** v is used when the **return** statement returns a constant v at the end of a function call. The status **tra** x signifies transition to a new parser state. The new parser state can be a final state in the case of **tra** x_{accept} or **tra** x_{reject} , or otherwise a state defined by the programmer. \perp represents a crash or undefined behaviour, for example caused by some badly-typed part of the program.

Context

The ctx is a tuple of the following:

1. X external object map. It maps extern object names to tuples of constructors and their respective function maps.
2. F_g globally declared function map. It maps global function and actions names to tuples of their bodies, argument names and directions.
3. F_b programmable block locally declared functions. It maps local function names to tuples of their bodies, argument names and directions.
4. P parser states map. It maps the name of the parser state to the body of that state name.
5. Tb the table match-kind map. It maps the table name to its list of matching kinds.

The context ctx assumed to be static, therefore not part of the execution state.

1.5 Calling convention

The calling conventions can be directioned or directionless. The direction can be either IN, OUT or INOUT. IN direction summary:

1. Should not be used on the left hand of assignment
2. Shouldn't be passed to a function without using the proper calling convention IN
3. Initialized by copying the value of the corresponding argument when the invocation is executed.

OUT summary:

1. usually uninitialized, and treated as l-values. after the execution of the call, the value of the OUT parameters copied to the corresponding location of the l-value. OUT parameters are initialized in the following cases
 - (a) if the types are header or header_union, OUT parameter is set to "invalid"
 - (b) if the type is a header stack, then all elements of the header stack set to "invalid" and the next index is initialized to 0.
 - (c) if the type is compound (e.g. struct or tuple) apply the rules recursively to its members.
 - (d) if any any other type than listed above (e.g. bit $\langle W \rangle$), then it doesn't need any predictable value.

INOUT summary:

1. this type of parameters are both IN and OUT.
2. it must be an l-value, which means it can be assigned to a value.

NO direction summary:

1. those parameters are known at compile time.
2. it also can be an action parameter, can be set by the control plane.
3. it also can be an action parameter that set directly by an other action, then the behaviour will be like IN parameter.

The direction d can be \downarrow denotes IN, \uparrow denotes OUT, \updownarrow denotes INOUT, \circ denotes directionless. Thus, $d ::= \downarrow \mid \uparrow \mid \updownarrow \mid \circ$

Due to the calling conventions, the scope has the type; $\gamma : X \hookrightarrow V * (X \cup \{\perp\})$.

The list of scopes is identified with an arrow following $\bar{\gamma}$. The local $\bar{\gamma}$ is meant to be a local list of scopes to the frame. The global $\bar{\gamma}_G$ is a list with a length of 2. The first index determines the external architecture scope (can not be defined by a P4 program), while the second index determines the architecture local variables (defined in the architecture level). Operations on list of scopes:

1. Operation $\gamma[x \mapsto v]$ to update a variable name x with value v in the scope γ .
2. Operation $\bar{\gamma}[x \mapsto v]$ to update a variable name x with value v in the most recent scope that contains the variable name x .
3. Operation $lookup_v(\bar{\gamma}, \bar{\gamma}_G, x)$ to return the value v of the tuple $(y, x \cup \perp)$, which is whatever variable x is mapped to in the most recent scope it is defined in after concatenating $\bar{\gamma}_G \uparrow \bar{\gamma}$.

2 Semantics

2.1 Expressions

Expression semantics and reductions are layed out in this section. Overall, the reductions can never alter or have any side effects on the state. The only thing it can perform is a reduction on the expressions -standard small step structural semantics- and also produce a new frame -which occurs only in the function call reduction-.

Variable lookup

In the E_LOOKUP rule, the lookup function ensures that the variable name x is evaluated in the uppermost (i.e. most recently scope γ that x exists in). The evaluation will occur using the function $lookup_v$. It will sweep the list of scopes $\bar{\gamma} \vdash \bar{\gamma}_G$ and finds the most recent scopes -most left- that contains variable x and this is because the scopes grows from right to left in the list. This agrees with the description in Sections 6.8 and 10.2 of the P4 specification. The constant value of this variable is then returned to the expression rediction.

$$\frac{v = lookup_v(\bar{\gamma}, \bar{\gamma}_G, varn)}{ctx \bar{\gamma}_G \bar{\gamma} \vdash (\mathbf{var} \ varn) \rightsquigarrow (v, [])} \quad E_LOOKUP$$

function call

Note that function calls also include actions as well as extern function calls.

The E_CALL_ARGS is used whenever there is a function call expression with unreduced (in and none directioned arguments). We fetch the function name's argument names and directions using **lookup_funn_sig**, then each function's argument will be checked against the function's direction in the same position. If the direction is in or none, then it will be reduced until it becomes a constant, otherwise if the argument has the direction out or inout then do not reduce it.

The $E_CALL_NEWFRAME$ rule is used when all of the function arguments have been reduced to constants (for non-out directions) or variables (directions with out). The function call reduction will produce a placeholder we call **var(star, funn)** and a new frame. The new frame will be later added on top of the state's frames in the statements reduction rules. The way that this new frame is constructed is as follows: The function name *funn* will be the same as the call in the expression. The function name *funn* will be looked up in both the function maps and extern maps to retrieve the function body *stmt* and the signature of the parameters represented as a list of tupels: variable names and their directions $[(x_1, d_1), \dots, (x_n, d_n)]$. Each argument will be checked against the boolean formula to ensure that the arguments were reduced properly: (if d_i in-none then the expression in the same position should be a constant, otherwise a variable name). The new frame's scope γ' is a new fresh empty scope that contains the copied in arguments using the function **copyin**.

Headers and structs

The struct can be an expression, thus we need reductions for it. The rule $E_ESTRUCT$ rule will reduce the expression feilds one at a time from left to right. One all the expressions become

$$\begin{array}{c}
[(x_1, d_1), \dots, (x_n, d_n)] = \mathbf{lookup_funn_sig}(funn, F_g, F_b, X) \\
i = \min \{j. [d_1, \dots, d_n][j] \in \{\circ, \downarrow\} \wedge \neg(\text{is_const}[e_1, \dots, e_n][j])\} \\
e = [e_1, \dots, e_n][i] \\
(\text{apply_table}, X, F_g, F_b, P, Tb) \overline{\gamma_G} \overline{\gamma} \vdash (e) \rightsquigarrow (e', \overrightarrow{\Phi}) \\
[e'_1, \dots, e'_n] = (i \mapsto e')[e_1, \dots, e_n]
\end{array}
\frac{}{(\text{apply_table}, X, F_g, F_b, P, Tb) \overline{\gamma_G} \overline{\gamma} \vdash (\mathbf{call_funn}(e_1, \dots, e_n)) \rightsquigarrow (\mathbf{call_funn}(e'_1, \dots, e'_n), \overrightarrow{\Phi})} \text{E_CALL_ARGS}$$

$$\begin{array}{c}
(stmt, [(x_1, d_1), \dots, (x_n, d_n)]) = \mathbf{lookup_funn_sig_body}(funn, F_g, F_b, X) \\
\forall i \leq n. ((d_i \in \{\circ, \downarrow\} \implies \text{is_const } e_i) \wedge (d_i \in \{\uparrow, \uparrow\} \implies \text{is_var } e_i)) \\
\gamma' = \text{copyin}([x_1, \dots, x_n], [e_1, \dots, e_n], [d_1, \dots, d_n], \gamma)
\end{array}
\frac{}{(\text{apply_table}, X, F_g, F_b, P, Tb) \overline{\gamma_G} \overline{\gamma} \vdash (\mathbf{call_funn}(e_1, \dots, e_n)) \rightsquigarrow (\mathbf{var}(\mathbf{star}, funn), [(stmt)_{[\gamma']}^{funn}])} \text{E_CALL_NEWFRAME}$$

constants then we can transform the expression struct to a value struct via E_ESTRUCT_TO_V which will be used mostly in header access. Same exact operations are applied on the headers.

$$\begin{array}{c}
i = \min \{j. \neg(\text{is_const}[e_1, \dots, e_n][j])\} \\
e = [e_1, \dots, e_n][i] \\
ctx \overline{\gamma_G} \overline{\gamma} \vdash (e) \rightsquigarrow (e', \overrightarrow{\Phi}) \\
[e'_1, \dots, e'_n] = (i \mapsto e')[e_1, \dots, e_n]
\end{array}
\frac{}{ctx \overline{\gamma_G} \overline{\gamma} \vdash (\mathbf{eStruct}\{f_1 = e_1; \dots; f_n = e_n\}) \rightsquigarrow (\mathbf{eStruct}\{f_1 = e'_1; \dots; f_n = e'_n\}, \overrightarrow{\Phi})} \text{E_ESTRUCT}$$

$$\begin{array}{c}
\text{is_consts } e_1, \dots, e_n \\
v_1, \dots, v_n = \text{vl_of_el}(e_1, \dots, e_n)
\end{array}
\frac{}{ctx \overline{\gamma_G} \overline{\gamma} \vdash (\mathbf{eStruct}\{f_1 = e_1; \dots; f_n = e_n\}) \rightsquigarrow (\mathbf{struct}\{f_1 = v_1; \dots; f_n = v_n\}, [])} \text{E_ESTRUCT_TO_V}$$

$$\begin{array}{c}
i = \min \{j. \neg(\text{is_const}[e_1, \dots, e_n][j])\} \\
e = [e_1, \dots, e_n][i] \\
ctx \overline{\gamma_G} \overline{\gamma} \vdash (e) \rightsquigarrow (e', \overrightarrow{\Phi}) \\
[e'_1, \dots, e'_n] = (i \mapsto e')[e_1, \dots, e_n]
\end{array}
\frac{}{ctx \overline{\gamma_G} \overline{\gamma} \vdash (\mathbf{eHeader}\text{ boolv}\{f_1 = e_1; \dots; f_n = e_n\}) \rightsquigarrow (\mathbf{eHeader}\text{ boolv}\{f_1 = e'_1; \dots; f_n = e'_n\}, \overrightarrow{\Phi})} \text{E_EHEADER}$$

$$\begin{array}{c}
\text{is_consts } e_1, \dots, e_n \\
v_1, \dots, v_n = \text{vl_of_el}(e_1, \dots, e_n)
\end{array}
\frac{}{ctx \overline{\gamma_G} \overline{\gamma} \vdash (\mathbf{eHeader}\text{ boolv}\{f_1 = e_1; \dots; f_n = e_n\}) \rightsquigarrow (\mathbf{header}\text{ boolv}\{f_1 = v_1; \dots; f_n = v_n\}, [])} \text{E_EHEADER_TO_V}$$

Access headers and structs

The E_S_ACC rule is used to access the values of fields in structs, and the E_H_ACC rule is similarly used for headers.

$$\frac{v = \mathbf{struct} \{f_1 = v_1; \dots; f_n = v_n\}(f)}{ctx \overline{\gamma_G} \overline{\gamma} \vdash (\mathbf{struct} \{f_1 = v_1; \dots; f_n = v_n\}.f) \rightsquigarrow (v, [])} \quad E_S_ACC$$

$$\frac{v = \mathbf{header} \mathit{boolv} \{f_1 = v_1; \dots; f_n = v_n\}(f)}{ctx \overline{\gamma_G} \overline{\gamma} \vdash (\mathbf{header} \mathit{boolv} \{f_1 = v_1; \dots; f_n = v_n\}.f) \rightsquigarrow (v, [])} \quad E_H_ACC$$

Select label

The E_SEL_ACC rule is used to match the given value v against the label-value list, in the case a match exists. If the match doesn't exist, then return the default label x .

$$\frac{x' = \{v_1 : x_1; \dots; v_n : x_n; _ : x\}(v)}{ctx \overline{\gamma_G} \overline{\gamma} \vdash (\mathbf{select} v \{v_1 : x_1; \dots; v_n : x_n\}x) \rightsquigarrow (x', [])} \quad E_SEL_ACC$$

Bit slicing

This reduction gives a bitvector $bitv'''$ that is reduced from the slicing operation. It extracts a contiguous list from the original $bitv$ from the lsb $bitv''$ till the msb $bitv'$.

$$\frac{bitv''' = bitv[bitv' : bitv'']}{ctx \overline{\gamma_G} \overline{\gamma} \vdash (bitv[bitv' : bitv'']) \rightsquigarrow (bitv''', [])} \quad E_SLICE_V$$

Concatenation

The reduction produces one bit string $bitv''$ that is the result of concatenating two bit strings $bitv$ and $bitv'$.

$$\frac{bitv'' = bitv \mathbin{++} bitv'}{ctx \overline{\gamma_G} \overline{\gamma} \vdash (\mathbf{concat} bitv bitv') \rightsquigarrow (bitv'', [])} \quad E_CONCAT_V$$

Unary and binary arithmetic operations

Unary and binary operations are basically standard operations on bit vectors, therefore are removed from this section.

2.2 Statement Semantics

Note that the statement semantics operates on single frames. The semantics of the statements are presented in this section².

Assignment

The assignment can assign to *lvals* (shown in Figure 6), they can be either variable identified by their names, a null variable (used to model method calls) or struct fields, which are identified by the struct and field names, similar to the field access expression. Whenever an expression is assigned to an *lval*, that expression shall be reduced until it becomes a constant value. Then the appropriate scope in the current frame of execution will be updated with the mapping $x \mapsto v$. With appropriate we mean that the topmost (most recent) scope that the *lval* is declared in. The reduction results in the empty statement and an updated local or global scope lists. Note that this doesn't apply on **null** since it is used for method and action calls with no return values in mind.

$$\frac{\begin{array}{l} \bar{\gamma}' = (\bar{\gamma} + +\bar{\gamma}_G)[lval \mapsto v] \\ ((\bar{\gamma}_G', \bar{\gamma}'') = \text{separate}\bar{\gamma}') \end{array}}{ctx \vdash (\gamma_A, \bar{\gamma}_G, [([lval := v])_{\bar{\gamma}}^{funn}], \mathbf{run}) \rightarrow (\gamma_A, \bar{\gamma}_G', [([\emptyset_{\text{stmt}}])_{\bar{\gamma}''}^{funn}], \mathbf{run})} \text{ STMT_ASS_V}$$

If-then-else

The STMT_COND2 and STMT_COND3 rules are the standard ones for conditional statements.

$$\frac{}{ctx \vdash (\gamma_A, \bar{\gamma}_G, [([\mathbf{if} \top \mathbf{then} stmt_1 \mathbf{else} stmt_2])_{\bar{\gamma}}^{funn}], \mathbf{run}) \rightarrow (\gamma_A, \bar{\gamma}_G, [([stmt_1])_{\bar{\gamma}}^{funn}], \mathbf{run})} \text{ STMT_COND2}$$

$$\frac{}{ctx \vdash (\gamma_A, \bar{\gamma}_G, [([\mathbf{if} \perp \mathbf{then} stmt_1 \mathbf{else} stmt_2])_{\bar{\gamma}}^{funn}], \mathbf{run}) \rightarrow (\gamma_A, \bar{\gamma}_G, [([stmt_2])_{\bar{\gamma}}^{funn}], \mathbf{run})} \text{ STMT_COND3}$$

Block

Once a block statement is encountered $\{\overline{decl} \text{ } stmt\}$ the STMT_BLOCK_ENTER reduction will be used, it entails the \overline{decl} being transformed and replicated into a scope that will be appended to the local $\bar{\gamma}$ of the frame, and then the body *stmt* of the block will be appended to the empty statement.

The STMT_BLOCK_EXIT rule is used in the case where the end of a block is reached, i.e. whenever a block contains only an empty statement: it pops the scope corresponding to the block (the most recent one) from the scope list $\bar{\gamma}$. The block will be exited and reduced to an empty statement \emptyset_{stmt} .

The STMT_BLOCK_EXEC rule simply describes small-step reduction of the block contents. We also check if the previous list \overline{stmt} is empty or not, if it is empty, then directly apply the other

²Rules for reducing expressions in all contexts are found in Appendix B

single block statement rules. It is also important to check that the head of the list $stmt :: \overline{stmt}$ is not empty statement \emptyset_{stmt} otherwise it is non-deterministic with the block exit rule.

$$\frac{\begin{array}{l} \gamma = \text{replicate}(\overline{decl}) \\ \overline{\gamma}' = [\gamma] + +\overline{\gamma} \end{array}}{ctx \vdash (\gamma_A, \overline{\gamma}_G, [(\{\overline{decl} \text{ } stmt\})]_{\overline{\gamma}}^{funn}, \mathbf{run}) \rightarrow (\gamma_A, \overline{\gamma}_G, [(stmt :: [\emptyset_{stmt}])_{\overline{\gamma}'}^{funn}], \mathbf{run})} \text{ STMT_BLOCK_ENTER}$$

$$\frac{\begin{array}{l} \mathbf{not_empty} \text{ } stmt \\ \mathbf{not_empty} \text{ } \overrightarrow{stmt} \end{array}}{(apply_table, X, F_g, F_b, P, Tb) \vdash (\gamma_A, \overline{\gamma}_G, [(stmt)_{\overline{\gamma}}^{funn}], t) \rightarrow (\gamma_{A'}, \overline{\gamma}_{G'}, \overrightarrow{\Phi}' + [(\overrightarrow{stmt}')_{\overline{\gamma}'}^{funn}], t')} \\ (apply_table, X, F_g, F_b, P, Tb) \vdash (\gamma_A, \overline{\gamma}_G, [(stmt :: \overrightarrow{stmt})_{\overline{\gamma}}^{funn}], t) \rightarrow (\gamma_{A'}, \overline{\gamma}_{G'}, \overrightarrow{\Phi}' + [(\overrightarrow{stmt}')_{\overline{\gamma}'}^{funn} + +\overrightarrow{stmt})_{\overline{\gamma}'}^{funn}], t') \text{ STMT_BLOCK_ENTER}$$

$$\frac{\begin{array}{l} \mathbf{not_empty} \text{ } \overrightarrow{stmt} \\ \overline{\gamma}' = \text{tl} \overline{\gamma} \end{array}}{ctx \vdash (\gamma_A, \overline{\gamma}_G, [(\emptyset_{stmt} :: \overrightarrow{stmt})_{\overline{\gamma}}^{funn}], t) \rightarrow (\gamma_{A'}, \overline{\gamma}_G, [(\overrightarrow{stmt})_{\overline{\gamma}'}^{funn}], t)} \text{ STMT_BLOCK_EXIT}$$

Apply

The STMT_APPLY_V describes the apply table statement. Each apply statement has a table name tbl with the a list of key expressions e_1, \dots, e_n to match on. This list have been previously reduced to constants one at a time in the rule STMT_APPLY_E. The Tb will return a list of match kind mk_1, \dots, mk_n . The ctrl is a function that maps table names tbl , expressions, and the match kinds to action that should be implemented as a method call, with a list of the arguments v_1, \dots, v_m .

$$\frac{\begin{array}{l} i = \min \{j. \neg(\text{is_const}[e_1, \dots, e_n][j])\} \\ e = [e_1, \dots, e_n][i] \\ ctx \overline{\gamma}_G \overline{\gamma} \vdash (e) \rightsquigarrow (e', \overrightarrow{\Phi}) \\ [e'_1, \dots, e'_n] = (i \mapsto e')[e_1, \dots, e_n] \end{array}}{ctx \vdash (\gamma_A, \overline{\gamma}_G, [([\mathbf{apply} \text{ } tbl(e_1, \dots, e_n)])_{\overline{\gamma}}^{funn}], \mathbf{run}) \rightarrow (\gamma_A, \overline{\gamma}_G, \overrightarrow{\Phi} + [([\mathbf{apply} \text{ } tbl(e'_1, \dots, e'_n)])_{\overline{\gamma}}^{funn}], \mathbf{run})} \text{ STMT_APPLY_TABLE}$$

$$\frac{\begin{array}{l} \text{is_consts } e_1, \dots, e_n \\ Tb \text{ } tbl = ([mk_1, \dots, mk_n], (f', [e'_1, \dots, e'_n])) \\ apply_table(tbl, (e_1, \dots, e_n), ([mk_1, \dots, mk_n]), (f', [e'_1, \dots, e'_n]), \gamma_A) = (f, (v_1, \dots, v_m)) \end{array}}{(apply_table, X, F_g, F_b, P, Tb) \vdash (\gamma_A, \overline{\gamma}_G, [([\mathbf{apply} \text{ } tbl(e_1, \dots, e_n)])_{\overline{\gamma}}^{funn}], \mathbf{run}) \rightarrow (\gamma_A, \overline{\gamma}_G, [([\mathbf{null} := (\mathbf{call} \text{ } f(v_1, \dots, v_m))]_{\overline{\gamma}}^{funn}], \mathbf{run})} \text{ STMT_APPLY_TABLE}$$

Return

Once return's expression is reduced to a constant value, the status is changed to a **ret** v , and the statement becomes \emptyset_{stmt} . The rest of the return operation will be handled in the sequence and composition rules.

$$\frac{}{ctx \vdash (\gamma_A, \overline{\gamma_G}, [([\mathbf{return} \ v)]_{\overline{\gamma}}^{funn}], \mathbf{run}) \rightarrow (\gamma_A, \overline{\gamma_G}, [([\emptyset_{\text{stmt}})]_{\overline{\gamma}}^{funn}], \mathbf{ret} \ v)} \text{ STMT_RET_V}$$

Sequence

The sequential statements rules STMT_SEQ1 and STMT_SEQ2 are standard. The status t must be **run** to start with. The part that is different is that if any new frame being created by any function call or a statement added to the statement list, it will be added on top of the frame list.

Otherwise, the STMT_SEQ3 rule is used whenever the status is **ret**, or **tra** to indicate either a transition to a parser state or a return from a function call. The rest of the sequential composition statements are not considered, this will give flexibility to the return or transition statements in the body rather than only at the end. STMT_SEQ1 and STMT_SEQ3 are able to change the tables that are populated by the control plane.

$$\frac{ctx \vdash (\gamma_A, \overline{\gamma_G}, [([stmt_1])_{\overline{\gamma}}^{funn}], \mathbf{run}) \rightarrow (\gamma_{A'}, \overline{\gamma_{G'}}, \overrightarrow{\Phi} + [(\overrightarrow{stmt'_1} + + [stmt'_1]_{\overline{\gamma'}}^{funn}), \mathbf{run})}{ctx \vdash (\gamma_A, \overline{\gamma_G}, [([stmt_1; stmt_2])_{\overline{\gamma}}^{funn}], \mathbf{run}) \rightarrow (\gamma_{A'}, \overline{\gamma_{G'}}, \overrightarrow{\Phi} + [(\overrightarrow{stmt'_1} + + [stmt'_1; stmt_2]_{\overline{\gamma'}}^{funn}), \mathbf{run})} \text{ STMT_SEQ1}$$

$$\frac{}{ctx \vdash (\gamma_A, \overline{\gamma_G}, [([\emptyset_{\text{stmt}}; stmt]_{\overline{\gamma}}^{funn}], \mathbf{run}) \rightarrow (\gamma_A, \overline{\gamma_G}, [([stmt]_{\overline{\gamma}}^{funn}], \mathbf{run})} \text{ STMT_SEQ2}$$

$$\frac{ctx \vdash (\gamma_A, \overline{\gamma_G}, [([stmt_1])_{\overline{\gamma}}^{funn}], \mathbf{run}) \rightarrow (\gamma_{A'}, \overline{\gamma_{G'}}, [([stmt'_1]_{\overline{\gamma'}}^{funn}], t) \quad t \neq \mathbf{run}}{ctx \vdash (\gamma_A, \overline{\gamma_G}, [([stmt_1; stmt_2]_{\overline{\gamma}}^{funn}], \mathbf{run}) \rightarrow (\gamma_{A'}, \overline{\gamma_{G'}}, [([stmt'_1]_{\overline{\gamma'}}^{funn}], t)} \text{ STMT_SEQ3}$$

Extern

The symbol \blacksquare represents a statement capturing the semantics of an extern function other than copy-in, copy-out and return behaviour. \blacksquare is able to modify the architectural state, the global scope list $\overline{\gamma_G}$, the local scope list $\overline{\gamma}$ and the execution status (which is typically set to **ret** v). The exact behaviour is determined by looking up the entry of the name $funn$ associated with the current frame in the extern function map X : since \blacksquare is used in extern function call bodies, $funn$ is the name of the extern function.

Note that calling an extern function works the same as calling any other function, as described in the function call subsection of Section 2.1. However, by convention the body of the extern function consists only of \blacksquare .

$$\begin{array}{c}
\text{ext_fun} = \text{lookup_ext_fun}(\text{funn}, X) \\
(\gamma_{A'}, \overline{\gamma_G'}, \overline{\gamma'}, t') = \text{ext_fun}(\gamma_A, \overline{\gamma_G}, \overline{\gamma}, t) \\
\hline
(\text{apply_table}, X, F_g, F_b, P, Tb) \vdash (\gamma_A, \overline{\gamma_G}, [([\blacksquare])_{\overline{\gamma}}^{\text{funn}}], \mathbf{run}) \rightarrow (\gamma_{A'}, \overline{\gamma_G'}, [([\emptyset_{\text{stmt}}])_{\overline{\gamma'}}^{\text{funn}}], t')
\end{array}
\quad \text{STMT_EXT}$$

Transition

The transition statement **transition** x , whose semantics is captured by the STMT_TRANS rule, makes a change to the state's status t based on the state name x .

$$\frac{}{ctx \vdash (\gamma_A, \overline{\gamma_G}, [([\mathbf{transition} \ x])_{\overline{\gamma}}^{\text{funn}}], \mathbf{run}) \rightarrow (\gamma_A, \overline{\gamma_G}, [([\emptyset_{\text{stmt}}])_{\overline{\gamma}}^{\text{funn}}], \mathbf{tra} \ x)}
\quad \text{STMT_TRANS}$$

Verify

The **verify** $e \ e'$ statement is used to check the whether the boolean expression e holds; if it does, then nothing happens, as in the STMT_VERIFY_3 rule. Otherwise, it assigns the error in e' to "parseError" and reduces the statement to a **transition** statement to the state 'reject'.

$$\frac{}{ctx \vdash (\gamma_A, \overline{\gamma_G}, [([\mathbf{verify} \ \top \ (\mathbf{errmsg} \ x)])_{\overline{\gamma}}^{\text{funn}}], \mathbf{run}) \rightarrow (\gamma_A, \overline{\gamma_G}, [([\emptyset_{\text{stmt}}])_{\overline{\gamma}}^{\text{funn}}], \mathbf{run})}
\quad \text{STMT_VERIFY_3}$$

$$\begin{array}{c}
x' = \text{"parseError"} \\
x'' = \text{"reject"} \\
\hline
ctx \vdash (\gamma_A, \overline{\gamma_G}, [([\mathbf{verify} \ \perp \ (\mathbf{errmsg} \ x)])_{\overline{\gamma}}^{\text{funn}}], \mathbf{run}) \rightarrow (\gamma_A, \overline{\gamma_G}, [([x' := (\mathbf{errmsg} \ x); \mathbf{transition} \ x''])_{\overline{\gamma}}^{\text{funn}}], \mathbf{run})
\end{array}
\quad \text{STMT_VE}$$

2.3 Frame-Level Semantics

The previous semantics operate on a single frame for a list of statements, however when a function being called, then it means that the state will contain two frames. The top frame that will be executed as can be seen in FRAMES_COMP1 reduction, where the previous frame Φ is not empty (otherwise, we can use the previous rules above).

The rule FRAMES_COMP2 is used specifically whenever there is a return occurring in a frame in that is being reduced. Here the the global scope list $\overline{\gamma_G}$ must contain a **var**(**star**, funn) declared for every function funn . This variable should be updated with the value v that is found in the state's status **ret** v . The copy out function takes the callee function signature, the "possibly updated" global scope list (index 0 or 1 from depends on the location of **var**(**star**, funn)), the top most scope in the $\overline{\gamma''}$ and the callee's current scope list $\overline{\gamma}$. It returns a new updated scopes list for both the global and caller's frame.

In the copyout function, each parameter with (out or inout directions) should be copied out to the variable name it is mapping to. i.e. the mapping of a single scope looks as $x \mapsto (v, y)$ so for each variable name x that is part of the current argument list in the signature we will check the direction of it, if the direction is (none or in) then we do not copy them out. However, if the direction is (out or inout) we will look into the mapping of it, and retrieve a tuple (v, y) which is the value and the original argument of the function to be copied out in the proper scope (top most one it is defined in).

$$\begin{array}{c}
\overrightarrow{\gamma}_G' = \text{scopes_to_pass}(funn, F_g, F_b, \overrightarrow{\gamma}_G) \\
(\text{apply_table}, X, F_g, F_b, P, Tb) \vdash (\gamma_A, \overrightarrow{\gamma}_G', [(\overrightarrow{stmt})_{\overrightarrow{\gamma}}^{funn}], t) \rightarrow (\gamma_A', \overrightarrow{\gamma}_G'', \overrightarrow{\Phi}', t') \\
\overrightarrow{\Phi}'' \neq [] \Rightarrow t' \neq \mathbf{ret} \mathbf{v} \\
\overrightarrow{\gamma}_G''' = \text{scopes_to_retrieve}(funn, F_g, F_b, \overrightarrow{\gamma}_G, \overrightarrow{\gamma}_G'') \\
\hline
(\text{apply_table}, X, F_g, F_b, P, Tb) \vdash (\gamma_A, \overrightarrow{\gamma}_G, [(\overrightarrow{stmt})_{\overrightarrow{\gamma}}^{funn}] ++ \overrightarrow{\Phi}'', t) \rightarrow_{\Phi} (\gamma_A', \overrightarrow{\gamma}_G''', \overrightarrow{\Phi}' ++ \overrightarrow{\Phi}'', t') \quad \text{FRAMES_COMP1}
\end{array}$$

$$\begin{array}{c}
\overrightarrow{\gamma}_G' = \text{scopes_to_pass}(funn, F_g, F_b, \overrightarrow{\gamma}_G) \\
(\text{apply_table}, X, F_g, F_b, P, Tb) \vdash (\gamma_A, \overrightarrow{\gamma}_G', [(\overrightarrow{stmt})_{\overrightarrow{\gamma}}^{funn}], \mathbf{run}) \rightarrow (\gamma_A', \overrightarrow{\gamma}_G'', [(\overrightarrow{stmt}'')_{\overrightarrow{\gamma}''}^{funn}], \mathbf{ret} \mathbf{v}) \\
\overrightarrow{\gamma}_G''' = (\overrightarrow{\gamma}_G'')[(\mathbf{star}, funn) \mapsto v] \\
(stmt''', [(x_1, d_1), \dots, (x_n, d_n)]) = \mathbf{lookup_funn_sig_body}(funn, F_g, F_b, X) \\
\overrightarrow{\gamma}_G'''' = \text{scopes_to_retrieve}(funn, F_g, F_b, \overrightarrow{\gamma}_G, \overrightarrow{\gamma}_G''') \\
(\overrightarrow{\gamma}_G''''', \overrightarrow{\gamma}''') = \text{copyout}([x_1, \dots, x_n], [d_1, \dots, d_n], \overrightarrow{\gamma}_G'', \overrightarrow{\gamma}', \overrightarrow{\gamma}) \\
\hline
(\text{apply_table}, X, F_g, F_b, P, Tb) \vdash (\gamma_A, \overrightarrow{\gamma}_G, [(\overrightarrow{stmt})_{\overrightarrow{\gamma}}^{funn}] ++ [(\overrightarrow{stmt}')_{\overrightarrow{\gamma}'}^{funn'}] ++ \overrightarrow{\Phi}, \mathbf{run}) \rightarrow_{\Phi} (\gamma_A', \overrightarrow{\gamma}_G''''', [(\overrightarrow{stmt}')_{\overrightarrow{\gamma}'''}^{funn'}] ++ \overrightarrow{\Phi}, \mathbf{run})
\end{array}$$

We define $\text{scopes_to_pass}(funn, F_g, F_b, \overrightarrow{\gamma}_G)$ as :

$$\overrightarrow{\gamma}_G' = \begin{cases} \text{if } funn \in \text{dom}(F_g) & , \text{ then } [\emptyset_{\gamma}; \overrightarrow{\gamma}_G[1]] \\ \text{otherwise} & , \text{ then } \overrightarrow{\gamma}_G \end{cases}$$

Where $\overrightarrow{\gamma}_G$ is a list of two elements, index 1 is the global scope, and index 0 is the programmable block global scope, it is fetched from the frame reduction rule and considered to be the initial global scope list. $\overrightarrow{\gamma}_G'$ is the scope that will be used to implement one frame at a time (statement reduction) in FRAMES_COMP1.

We define $\text{scopes_to_retrieve}(funn, F_g, F_b, \overrightarrow{\gamma}_G, \overrightarrow{\gamma}_G')$ as :

$$\overrightarrow{\gamma}_G'' = \begin{cases} \text{if } funn \in \text{dom}(F_g) & , \text{ then } [\overrightarrow{\gamma}_G[0]; \overrightarrow{\gamma}_G'[1]] \\ \text{otherwise} & , \text{ then } \overrightarrow{\gamma}_G' \end{cases}$$

Where $\overrightarrow{\gamma}_G''$ is a list of two elements, index 1 is the global scope, and index 0 is the programmable block global scope, and we will retrieve it to be added as the resulted scope of the frame reduction. $\overrightarrow{\gamma}_G'$ is the scope that is resulted from the statement reduction. $\overrightarrow{\gamma}_G$ is the original (initial) starting state's global list of the frame reduction.

2.4 Architecture-Level Semantics

The architecture-level semantics is the topmost-level semantics, describing the entirety of the P4 pipeline from input packets to output packets, and it uses the frame semantics for reduction steps inside programmable blocks. The judgment form, shown at the top of Figure 8, consists of multiple components: starting from the left, an *architectural context* ctx_A (which contains the static components not changed by reduction steps) on the left-hand side of the turnstile, which contains the following:

1. The *architectural block list* \overline{ab} : an architectural block represents a stage of packet processing. There are four fundamental stages of packet processing in a P4-compatible architecture. First, there are the input (**inp**) and output (**out**) stages: these stages just perform translation between the architectural packet format and the generic I/O format (consisting of a list of ports, each with pending packets to be arbitrated/sent off)³. Second, there are the programmable blocks (parser blocks and control blocks) and the fixed-function block stages. Fixed-function blocks, like their name implies, perform the parts of packet processing in the P4-programmable network element that are actually not P4-programmable.
2. The *programmable block map* B_p , which is a partial map between names of programmable block names (strings) and the all necessary items that model the block in question. This is the block type (parser or control), the list of directed parameters, the block-local function map containing function declared inside the block, a list of declarations of variables done at a block-global level, a statement representing initialisations and instantiations of these block-global variables followed by the body (for parsers, a transition statement pointing to “start”, for control blocks the content of the apply statement encased in a block), and the parser state map P between parser state names and their bodies (statements)⁴ and a table map Tb between names of tables and tuples of expressions and matching kinds. Note that the parser state map is empty for control blocks, as is the table map for parser blocks.
3. The *fixed-function block map* B_{ff} , which is a partial map between names of fixed-function blocks to the implementation of the fixed function itself, which is a partial function that maps the architectural scope to an updated architectural scope.
4. The *input function* f_{in} and the *output function* f_{out} , which are both partial functions from an IO list and the architectural scope to an updated IO list and architectural scope. They are used in the input and output stages.
5. The *programmable block copy-in function* $copyin_{pbl}$ and the *programmable block copy-out function* $copyout_{pbl}$: $copyin_{pbl}$ is a partial function from a list of directed parameters, a list of expression arguments, the architectural scope and the block type to a scope (the block-global scope of the programmable block). $copyout_{pbl}$ is a partial function from the global scopes list, a list of directed parameters, the architectural scope, the block type and status to an updated architectural scope. Note that these functions are responsible for the copy-in copy-out behaviour of the *parseError* variable in parser blocks.

³The demux block functionality may be modeled as part of the output stage, or it may be its own fixed-function block preceding the output stage

⁴Note that HOL4P4 represents all parser state bodies as encased in a block by convention

6. The *extern object map* X , which is a partial map from extern object names to tuples of extern function maps (a map holding the extern functions of the object: tuples of directed parameter lists, function bodies, and the extern semantics), and an optional constructor (holding a tuple same as that of the extern function map).
7. The *function map* F , which is a partial map from globally-declared function names to tuples of function bodies (statements) and their directed parameter lists.

The states on the left-hand and right-hand sides of the reduction both have the same elements.

1. The *architectural environment* env_A , which in turn has four components:
 - (a) The *architectural block index*: This is an index which informs us of which architectural block in \overline{ab} is currently being reduced.
 - (b) The *input list*, which is a list of incoming packets (represented as tuples of lists of Booleans and numbers signifying input ports)
 - (c) The *output list*, which is the same as the input list, but used for output.
 - (d) The *architectural scope* γ_A , which is of polymorphic type, and is used for storing things in-between the programmable blocks, as well as things that are not accessible directly by P4 code.
2. The global scope list $\overline{\gamma_G}$, which contains the top-level global scope with constants common to all programmable blocks as well as the block-global scope containing variables declared at the start of programmable blocks.
3. The *architecture-level frame list*: this contains either a regular frame list (as described in the statement semantics), or it is a special empty architecture-level frame list ($[]_A$).
4. The control-plane configuration C , a partial function from strings, values and match kinds to tuples of strings and expression lists.
5. The status, which informs us of whether a parser state machine is finished, or the apply of a control block is finished.

The rules of the architecture-level semantics are the following:

1. ARCH_IN: The first antecedent requires that the pending architecture block is **inp**, and then f_{in} updates the input list and the architectural scope.
2. ARCH_PBL_INIT: when the pending architecture block is a programmable block, a new block-global scope γ' is created and initialised with the arguments of the programmable block (in the case of a parser block, *parseError* is also initially set to *NoError*). After this, the variables in the list of declarations are declared in γ' . The final result γ''' is appended to the top-level global scope $\overline{\gamma_G}[0]$, forming a new global scopes list $\overline{\gamma_G}'$. Also, the empty architecture frame list is changed to a single frame where the singleton statement list contains the initialisations of the parser block *stmt* followed by the body of the programmable block⁵, the scopes stack contains a single empty scope and the current function name is the name of the programmable block.

⁵Conventions for how these are represented are mentioned in the explanation of B_p

3. ARCH_FFBL handles the fixed-function block: when the pending architecture block is a fixed-function block with name x , the implementation ff of the fixed-function is looked-up in B_{ff} using x , after which it is used to update the architectural scope. Also, the architecture block index is incremented.
4. ARCH_OUT: Similar to the above, but the pending architecture block must be **out**, and then the output list and the architectural scope are updated by f_{out} .
5. ARCH_PARSER_TRANS: In case the block at the block index i is a parser block and the current status is **tra** x' , this rule will obtain the parser state body $stmt'$ of $P(x')$ (where P is the parser state map of the current parser block), and set the next frame to the singleton list of $stmt'$ with $[\gamma_\emptyset]$ as the scope stack and x' as the current function name, as well as set the status to **run**.
6. ARCH_PBL_EXEC describes a reduction step inside a programmable block: the first two premises are there to obtain the content of the statement semantics context. Then, the global block list, the frame list, the control plane configuration and the status are updated by a statement reduction step \longrightarrow .
7. ARCH_PBL_RET rule describes the final step of programmable block reduction that reduces to an empty frame list. The first two premises obtain the directed parameters of the block. If $state_fin$ (termination conditions of the two programmable blocks) holds of the status and frame list (if status was **run**, it is then set to **tra** “*reject*” if we are in a parser block). The block output function out_p proceeds to update the architectural scope (notably dependent on architecture, it copies out the value of $parseError$). Furthermore, the architecture block list index is incremented by 1 and the block-global scope is dropped from the global scopes list.

$$\boxed{ctx_A \vdash s_A \longrightarrow_A s_A'} \quad \text{architecture-level semantics}$$

$$\frac{
\begin{array}{l}
\mathbf{inp} = \overline{ab}[i] \\
(\overline{io}'', \gamma_A') = in_A(\overline{io}, \gamma_A)
\end{array}
}{
(\overline{ab}, B_p, B_{ff}, in_A, out_A, in_p, out_p, apply_table, X, F_g) \vdash ((i, \overline{io}, \overline{io}', \gamma_A), \overline{\gamma_G}, [\]_A, \mathbf{run}) \longrightarrow_A ((i+1, \overline{io}'', \overline{io}', \gamma_A'), \overline{\gamma_G}, [\]_A, \mathbf{run})
} \quad \text{ARCH_IN}$$

$$\frac{
\begin{array}{l}
\mathbf{pbl} f(e_1, \dots, e_n) = \overline{ab}[i] \\
pbl_type((x_1, d_1), \dots, (x_n, d_n)) F_b \overline{decl} stmt P Tb = B_p(f) \\
\gamma' = in_p((x_1, \dots, x_n), [d_1, \dots, d_n], [e_1, \dots, e_n], \gamma_A, pbl_type) \\
\gamma'' = replicate(\overline{decl}, \gamma') \\
\overline{\gamma_G}' = \text{lastn}(1, \overline{\gamma_G}) \\
\overline{\gamma_G}'' = [\gamma''] + +\overline{\gamma_G}' \\
\overline{\gamma_G}''' = \text{initialise_var_stars}(F_g, F_b, X, \overline{\gamma_G}'')
\end{array}
}{
(\overline{ab}, B_p, B_{ff}, in_A, out_A, in_p, out_p, apply_table, X, F_g) \vdash ((i, \overline{io}, \overline{io}', \gamma_A), \overline{\gamma_G}, [\]_A, \mathbf{run}) \longrightarrow_A ((i, \overline{io}, \overline{io}', \gamma_A), \overline{\gamma_G}''', [([stmt])_{[\gamma_0]}^f], \mathbf{run})
} \quad \text{ARCH_PBL_INIT}$$

$$\frac{
\begin{array}{l}
\mathbf{ffbl} x = \overline{ab}[i] \\
ff = B_{ff}(x) \\
\gamma_A' = ff(\gamma_A)
\end{array}
}{
(\overline{ab}, B_p, B_{ff}, in_A, out_A, in_p, out_p, apply_table, X, F_g) \vdash ((i, \overline{io}, \overline{io}', \gamma_A), \overline{\gamma_G}, [\]_A, \mathbf{run}) \longrightarrow_A ((i+1, \overline{io}, \overline{io}', \gamma_A'), \overline{\gamma_G}, [\]_A, \mathbf{run})
} \quad \text{ARCH_FFBL}$$

$$\frac{
\begin{array}{l}
\mathbf{out} = \overline{ab}[i] \\
(\overline{io}'', \gamma_A') = out_A(\overline{io}', \gamma_A)
\end{array}
}{
(\overline{ab}, B_p, B_{ff}, in_A, out_A, in_p, out_p, apply_table, X, F_g) \vdash ((i, \overline{io}, \overline{io}', \gamma_A), \overline{\gamma_G}, [\]_A, \mathbf{run}) \longrightarrow_A ((0, \overline{io}, \overline{io}'', \gamma_A'), \overline{\gamma_G}, [\]_A, \mathbf{run})
} \quad \text{ARCH_OUT}$$

$$\frac{
\begin{array}{l}
\mathbf{pbl} x(e_1, \dots, e_n) = \overline{ab}[i] \\
\mathbf{parser}((x_1, d_1), \dots, (x_n, d_n)) F_b \overline{decl} stmt P Tb = B_p(x) \\
\text{not_final_state}(x') \\
stmt' = P(x')
\end{array}
}{
(\overline{ab}, B_p, B_{ff}, in_A, out_A, in_p, out_p, apply_table, X, F_g) \vdash ((i, \overline{io}, \overline{io}', \gamma_A), \overline{\gamma_G}, \vec{\Phi}, \mathbf{tra} x') \longrightarrow_A ((i, \overline{io}, \overline{io}', \gamma_A), \overline{\gamma_G}', [([stmt'])_{[\gamma_0]}^{x'}], \mathbf{run})
} \quad \text{ARCH_PARSER_TRAN}$$

$$\frac{
\begin{array}{l}
\mathbf{pbl} x(e_1, \dots, e_n) = \overline{ab}[i] \\
pbl_type((x_1, d_1), \dots, (x_n, d_n)) F_b \overline{decl} stmt P Tb = B_p(x) \\
(apply_table, X, F_g, F_b, P, Tb) \vdash (\gamma_A, \overline{\gamma_G}, \vec{\Phi}, \mathbf{run}) \longrightarrow_{\Phi} (\gamma_A', \overline{\gamma_G}', \vec{\Phi}', t')
\end{array}
}{
(\overline{ab}, B_p, B_{ff}, in_A, out_A, in_p, out_p, apply_table, X, F_g) \vdash ((i, \overline{io}, \overline{io}', \gamma_A), \overline{\gamma_G}, \vec{\Phi}, \mathbf{run}) \longrightarrow_A ((i, \overline{io}, \overline{io}', \gamma_A'), \overline{\gamma_G}', \vec{\Phi}', t')
} \quad \text{ARCH_PBL_EXEC}$$

$$\frac{
\begin{array}{l}
\mathbf{pbl} f(e_1, \dots, e_n) = \overline{ab}[i] \\
pbl_type((x_1, d_1), \dots, (x_n, d_n)) F_b \overline{decl} stmt P Tb = B_p(f) \\
\text{state_fin}(t, \vec{\Phi}) \\
t' = \text{set_fin_status}(pbl_type, t) \\
\gamma_A' = out_p(\overline{\gamma_G}, \gamma_A, [d_1, \dots, d_n], (x_1, \dots, x_n), pbl_type, t')
\end{array}
}{
(\overline{ab}, B_p, B_{ff}, in_A, out_A, in_p, out_p, apply_table, X, F_g) \vdash ((i, \overline{io}, \overline{io}', \gamma_A), \overline{\gamma_G}, \vec{\Phi}, t) \longrightarrow_A ((i+1, \overline{io}, \overline{io}', \gamma_A'), \text{lastn}(1, \overline{\gamma_G}), [\]_A, \mathbf{run})
} \quad \text{ARCH_PBL_RET}$$

Figure 8: Architecture-Level Semantics

A Concrete Syntax of Operations

\ominus	$::=$		
		!	negation
		\neg	bitwise complement
		-	signed negation
		+	unary plus

Figure 9: P4 Unary Operations

The unary expressions included are shown in Figure 9. These include all of the unary operations in P4. Boolean negation is only defined on Booleans, the other operations have their standard meanings (note that [unary plus is a no-op](#)).

\oplus	$::=$		
		\times	multiplication
		$/$	division
		mod	modulo
		$+$	addition
		$-$	subtraction
		\ll	logical left-shift
		\gg	logical right-shift
		\leq	less or equal
		\geq	greater or equal
		$<$	less
		$>$	greater
		\neq	not equal
		$=$	equal
		$\&$	bitwise and
		$\underline{\vee}$	bitwise xor
		$ $	bitwise or
		\wedge	binary and
		\vee	binary or

Figure 10: P4 Binary Operations

The binary expressions included are shown in Figure 9. These include all of the binary operations in P4.

B Semantics of Expression Reduction

This appendix describes semantics for reducing expressions in certain contexts. The expression semantics are shown in Figure 11. The statement semantics are shown in Figure 12.

The $E_FUNC_CALL_ARGS$ rule reduces the leftmost function argument which has yet to be reduced to a constant with one expression evaluation step. The first two antecedents divide the list of arguments into two sub-lists, where the prefix must contain all constants. The head of the suffix is then reduced with one step, after which the corresponding index in the original list of arguments is update with the resulting expression.

8.1 of the P4 specification states that expressions are evaluated left-to-right. Accordingly, the rules for binary operations - E_BINOP1 and E_BINOP2 - are split up so that reduction of the second operand requires that the first operand has been completely reduced to a constant. This is trivial for unary operations (E_UNOP).

References

- [1] Ryan Doenges et al. “Petr4: formal foundations for p4 data planes”. In: *Proceedings of the ACM on Programming Languages* 5.POPL (2021), pp. 1–32.

$$\boxed{[e](\sigma) \rightsquigarrow [e'](\sigma')} \quad \text{expression semantics}$$

$$\frac{ctx \, \overline{\gamma}_G \, \overline{\gamma} \vdash (e) \rightsquigarrow (e', \overrightarrow{\Phi})}{ctx \, \overline{\gamma}_G \, \overline{\gamma} \vdash (\text{select } e \{v_1 : x_1; \dots; v_n : x_n\} x) \rightsquigarrow (\text{select } e' \{v_1 : x_1; \dots; v_n : x_n\} x, \overrightarrow{\Phi})} \quad \text{E_SEL_ARG}$$

$$\frac{ctx \, \overline{\gamma}_G \, \overline{\gamma} \vdash (e) \rightsquigarrow (e', \overrightarrow{\Phi})}{ctx \, \overline{\gamma}_G \, \overline{\gamma} \vdash (\ominus e) \rightsquigarrow (\ominus e', \overrightarrow{\Phi})} \quad \text{E_UNOP_ARG}$$

$$\frac{ctx \, \overline{\gamma}_G \, \overline{\gamma} \vdash (e) \rightsquigarrow (e'', \overrightarrow{\Phi})}{ctx \, \overline{\gamma}_G \, \overline{\gamma} \vdash (e \oplus e') \rightsquigarrow (e'' \oplus e', \overrightarrow{\Phi})} \quad \text{E_BINOP_ARG1}$$

$$\frac{\text{is_short_circuit}(\oplus) \quad ctx \, \overline{\gamma}_G \, \overline{\gamma} \vdash (e) \rightsquigarrow (e', \overrightarrow{\Phi})}{ctx \, \overline{\gamma}_G \, \overline{\gamma} \vdash (v \oplus e) \rightsquigarrow (v \oplus e', \overrightarrow{\Phi})} \quad \text{E_BINOP_ARG2}$$

Figure 11: Expression Reduction-of-Argument Semantics

$$\boxed{[stmt]s \rightarrow [stmt']s'} \quad \text{statement semantics}$$

$$\frac{ctx \, \overline{\gamma}_G \, \overline{\gamma} \vdash (e) \rightsquigarrow (e', \overrightarrow{\Phi})}{ctx \vdash (\gamma_A, \overline{\gamma}_G, [[\text{return } e]]_{\overline{\gamma}}^{funn}, \text{run}) \rightarrow (\gamma_A, \overline{\gamma}_G, \overrightarrow{\Phi} \uparrow [[\text{return } e']]_{\overline{\gamma}}^{funn}, \text{run})} \quad \text{STMT_RET_E}$$

$$\frac{ctx \, \overline{\gamma}_G \, \overline{\gamma} \vdash (e) \rightsquigarrow (e', \overrightarrow{\Phi})}{ctx \vdash (\gamma_A, \overline{\gamma}_G, [[lval := e]]_{\overline{\gamma}}^{funn}, \text{run}) \rightarrow (\gamma_A, \overline{\gamma}_G, \overrightarrow{\Phi} \uparrow [[lval := e']]_{\overline{\gamma}}^{funn}, \text{run})} \quad \text{STMT_ASS_E}$$

$$\frac{ctx \, \overline{\gamma}_G \, \overline{\gamma} \vdash (e) \rightsquigarrow (e', \overrightarrow{\Phi})}{ctx \vdash (\gamma_A, \overline{\gamma}_G, [[\text{if } e \text{ then } stmt_1 \text{ else } stmt_2]]_{\overline{\gamma}}^{funn}, \text{run}) \rightarrow (\gamma_A, \overline{\gamma}_G, \overrightarrow{\Phi} \uparrow [[\text{if } e' \text{ then } stmt_1 \text{ else } stmt_2]]_{\overline{\gamma}}^{funn}, \text{run})} \quad \text{STMT_IF_E}$$

$$\frac{ctx \, \overline{\gamma}_G \, \overline{\gamma} \vdash (e) \rightsquigarrow (e'', \overrightarrow{\Phi})}{ctx \vdash (\gamma_A, \overline{\gamma}_G, [[\text{verify } e \, e']]_{\overline{\gamma}}^{funn}, \text{run}) \rightarrow (\gamma_A, \overline{\gamma}_G, \overrightarrow{\Phi} \uparrow [[\text{verify } e'' \, e']]_{\overline{\gamma}}^{funn}, \text{run})} \quad \text{STMT_VERIFY_E1}$$

$$\frac{ctx \, \overline{\gamma}_G \, \overline{\gamma} \vdash (e) \rightsquigarrow (e', \overrightarrow{\Phi})}{ctx \vdash (\gamma_A, \overline{\gamma}_G, [[\text{verify } b \, e]]_{\overline{\gamma}}^{funn}, \text{run}) \rightarrow (\gamma_A, \overline{\gamma}_G, \overrightarrow{\Phi} \uparrow [[\text{verify } b \, e']]_{\overline{\gamma}}^{funn}, \text{run})} \quad \text{STMT_VERIFY_E2}$$

Figure 12: Statement Reduction-of-Argument Semantics