Instructions for generating, launching, and testing executables for SIMM and FPGA simulation.

1. Save all .c and .h files provided in the <install dir>/SW/simm/src directory.
2. Save simm_tester.py in the <install dir>/SW/test/simm directory
3. Save the simm.py in the <install dir>/SW/test/utils directory
4. Save makefile in the <install dir>/SW/simm directory

   To build the FPGA simulation executable, go to step 5.

   To build the SIMM executable, go to step 6.

5. Execute the command, "**gcc -o fpga fake_fpga_v2.c**" This will generate the fpga executable that simulates FPGA data and interrupt.
6. From the <install dir>/SW/simm directory, run make. This will generate and store simm_app executable in the <install dir>/SW/simm/build directory.
7. Copy the simm_app executable from the <install dir>/SW/simm/build directory to the <install dir>/SW/simm directory. If your current directory is <install dir>/SW/simm, execute the command, "**cp build/simm_app simm_app**"

   To launch simm_app and fpga executables without test scripts, go to step 8.

   To launch simm_app and fpga executables with test scritps, go to step 12.

8. Open a new terminal window and change directories to: <install dir>/SW/simm/src

9. Launch the fpga executable from this directory. The user should see simulated data displayed in the terminal window.
10. Open a new terminal window and change directories to: <install dir>/SW/simm
11. Launch the simm_app executable from this directory.
12. Follow steps 8 and 9.
13. Open a new terminal window and change directories to: <install dir>/SW/test/simm
14. Launch simm_tester.py. This application will launch simm_app automatically and generate responses for TCP and UDP messages.