5.102 cutset

DESCRIPTION	LINKS	GRAPH
-------------	-------	-------

Origin [156]

Constraint cutset(SIZE_CUTSET, NODES)

Arguments SIZE_CUTSET : dvar

NODES : collection(index-int, succ-sint, bool-dvar)

Restrictions

```
\begin{split} & \texttt{SIZE\_CUTSET} \geq 0 \\ & \texttt{SIZE\_CUTSET} \leq |\texttt{NODES}| \\ & \texttt{required}(\texttt{NODES}, [\texttt{index}, \texttt{succ}, \texttt{bool}]) \\ & \texttt{NODES}.\texttt{index} \geq 1 \\ & \texttt{NODES}.\texttt{index} \leq |\texttt{NODES}| \\ & \texttt{distinct}(\texttt{NODES}, \texttt{index}) \\ & \texttt{NODES}.\texttt{bool} \geq 0 \\ & \texttt{NODES}.\texttt{bool} \leq 1 \end{split}
```

Purpose

Consider a digraph G with n vertices described by the NODES collection. Enforces that the subset of kept vertices of cardinality $n-{\tt SIZE_CUTSET}$ and their corresponding arcs form a graph without circuit.

Example

```
\left(\begin{array}{cccc} {\rm index} - 1 & {\rm succ} - \{2,3,4\} & {\rm bool} - 1, \\ {\rm index} - 2 & {\rm succ} - \{3\} & {\rm bool} - 1, \\ {\rm index} - 3 & {\rm succ} - \{4\} & {\rm bool} - 1, \\ {\rm index} - 4 & {\rm succ} - \{1\} & {\rm bool} - 0 \end{array}\right)
```

The cutset constraint holds since the vertices of the NODES collection for which the bool attribute is set to 1 correspond to a graph without circuit and since exactly one (SIZE_CUTSET = 1) vertex has its bool attribute set to 0.

Typical

```
\begin{split} & \texttt{SIZE\_CUTSET} > 0 \\ & \texttt{SIZE\_CUTSET} \leq |\texttt{NODES}| \\ & |\texttt{NODES}| > 1 \end{split}
```

Symmetry

Items of NODES are permutable.

Usage

The article [156] introducing the cutset constraint mentions applications from various areas such that deadlock breaking or program verification.

Remark

The undirected version of the cutset constraint corresponds to the minimum feedback vertex set problem.

Algorithm

The filtering algorithm presented in [156] uses graph reduction techniques inspired from Levy and Low [260] as well as from Lloyd, Soffa and Wang [264].

20030820 971

Keywords

application area: deadlock breaking, program verification.

constraint type: graph constraint.

final graph structure: circuit, directed acyclic graph, acyclic, no loop.

problems: minimum feedback vertex set.

Arc input(s)	NODES
Arc generator	$CLIQUE \mapsto \texttt{collection}(\texttt{nodes1}, \texttt{nodes2})$
Arc arity	2
Arc constraint(s)	 in_set(nodes2.index, nodes1.succ) nodes1.bool = 1 nodes2.bool = 1
Graph property(ies)	• MAX_NSCC≤ 1 • NVERTEX= NODES - SIZE_CUTSET
Graph class	• ACYCLIC • NO_LOOP

Graph model

We use a set of integers for representing the successors of each vertex. Because of the arc constraint, all arcs such that the bool attribute of one extremity is equal to 0 are eliminated; Therefore all vertices for which the bool attribute is equal to 0 are also eliminated (since they will correspond to isolated vertices). The graph property $\mathbf{MAX_NSCC} \leq 1$ enforces the size of the largest strongly connected component to not exceed 1; Therefore, the final graph cannot contain any circuit.

Part (A) of Figure 5.244 shows the initial graph from which we have chosen to start. It is derived from the set associated with each vertex. Each set describes the potential values of the succ attribute of a given vertex. Part (B) of Figure 5.244 gives the final graph associated with the **Example** slot. Since we use the **NVERTEX** graph property, the vertices of the final graph are stressed in bold. The cutset constraint holds since the final graph does not contain any circuit and since the number of removed vertices SIZE_CUTSET is equal to 1.

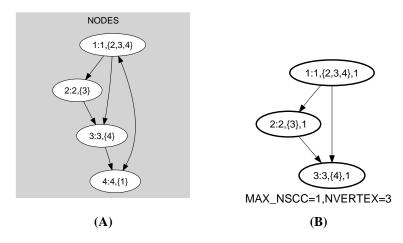


Figure 5.244: Initial and final graph of the cutset set constraint

20030820 973