

5.109 dag

	DESCRIPTION	LINKS	GRAPH
Origin	[142]		
Constraint	dag(NODES)		
Argument	NODES : collection(index—int, succ—svar)		
Restrictions	<pre> required(NODES, [index, succ]) NODES.index ≥ 1 NODES.index ≤ NODES distinct(NODES, index) NODES.succ ≥ 1 NODES.succ ≤ NODES </pre>		
Purpose	<p>Consider a digraph G described by the NODES collection. Select a subset of arcs of G so that the corresponding graph does not contain any circuit.</p>		
Example	$\left(\begin{array}{ll} \text{index} - 1 & \text{succ} - \{2, 4\}, \\ \text{index} - 2 & \text{succ} - \{3, 4\}, \\ \text{index} - 3 & \text{succ} - \emptyset, \\ \text{index} - 4 & \text{succ} - \emptyset, \\ \text{index} - 5 & \text{succ} - \{6\}, \\ \text{index} - 6 & \text{succ} - \emptyset \end{array} \right)$		
	The dag constraint holds since the NODES collection depicts a graph without circuit.		
Typical	$ \text{NODES} > 2$		
Symmetry	Items of NODES are permutable .		
Algorithm	A filtering algorithm for the dag constraint is given in [142, page 90]. It removes potential arcs that would create a circuit of mandatory arcs.		
See also	used in graph description: in_set .		
Keywords	constraint arguments: constraint involving set variables . constraint type: graph constraint .		

Arc input(s)	NODES
Arc generator	<i>SELF</i> \mapsto collection(nodes)
Arc arity	1
Arc constraint(s)	in_set(nodes.key, nodes.succ)
Graph property(ies)	<i>NARC</i> = 0
Arc input(s)	NODES
Arc generator	<i>CLIQUE</i> \mapsto collection(nodes1, nodes2)
Arc arity	2
Arc constraint(s)	in_set(nodes2.index, nodes1.succ)
Graph property(ies)	<i>MAX_NSCC</i> \leq 1

Graph model

The first graph constraint removes the loop of each vertex. The second graph constraint forbids the creation of circuits involving more than one vertex.

Part (A) of Figure 5.258 shows the initial graph associated with the second graph constraint of the **Example** slot. This initial graph from which we start is derived from the set associated with each vertex. Each set describes the potential values of the succ attribute of a given vertex. Part (B) of Figure 5.258 gives the final graph associated with the **Example** slot.

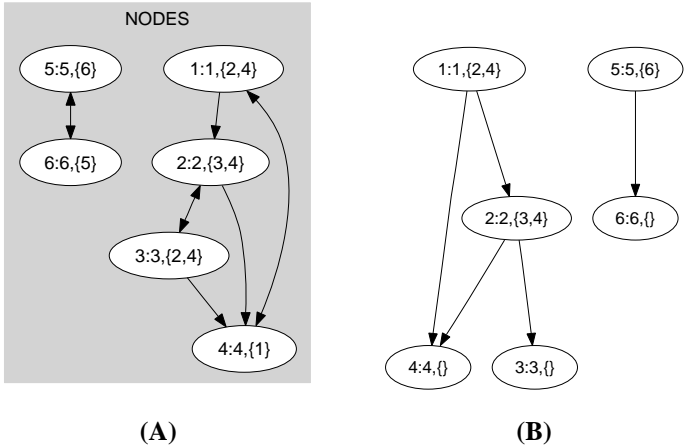


Figure 5.258: Initial and final graph of the dag set constraint