CS4211: Formal Methods for Software Engineering Lecture Notes

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Introduction to Formal Methods

- Requirements are difficult to define because its written in **Natural Language** which can be imprecise and ambiguous at times.
- As we cannot anticipate the ways a system may be used, written test cases only covers a small subset of use cases.
- We want to verify if a system always satisify a certain property, in possible all cases.
- In Formal Methods, we use Mathematics to define the structure and behaviour of our software because it is precise and unambiguous
- Eventually, we can use a model checker to automatically verify the software by checking if a certain property holds in all cases.

Latex Setup

- The Latex package that we will be using is zed-csp
- Setup Code:

```
1 % For latex document
2 \documentstyle[12pt,zed]{article}
3
4 % For beamer slides
5 \usepackage{zed-csp}
6
7 \begin{document}
8 \end{document}
```

• Reference: https://sg.mirrors.cicku.me/ctan/macros/latex/contrib/zed-csp/zed2e.pdf

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The Z Specification Language

- Based on set theory and mathematical logic
 - We will be doing a recap on predicates, set theory, functions and relations next.
- Uses schemas to declare object properties
 - Schemas are similar to defining the structure of a class and its properties in an Object-Oriented Programming Language.
- Uses operations to describe state transitions
 - Each object has a state representing the values it's properties hold at a certain moment in time.
 - Operations are similar to methods of a class.
 - Operations modify the state of an object.
 - We use predicates to describe state transitions in an operation.
- We can then prove that a certain property holds manually



Recap on Predicates and Logic

Predicate

A statement that is either true or false.

- There are 365 days in 2024. (false)
- **2** Let P(x, y) be x + y = 9
 - P(4,5) is **true**.
 - P(3,7) is **false**.

Logic Operators

- **1** Not (¬) Eg:
- ② And (∧)
- Or (∨)
- Implies (\Rightarrow)
- Equivalence (⇔)

Recap on Quantifiers

- Universal Quantifier (\forall)
 - Example: All natural numbers are greater than -1.
 - ullet Mathematically, we would write $\forall \, n \in \mathbb{N}, n > -1$
 - In Z Specification, we would write $\forall n : \mathbb{N} \bullet n > -1$
 - $\forall n : \mathbb{N} \bullet n > 0$
 - In general, $\exists x : X \bullet P(x)$ abbreviates $P(a) \land P(b) \land P(c) \land \dots$
- ② Existential Quantifier (∃)
 - Example: There exists a natural number more than 0.
 - In Z Specification, we would write $\exists n : \mathbb{N} \bullet n > 0$
 - In general, $\exists x : X \bullet P(x)$ abbreviates $P(a) \lor P(b) \lor P(c) \lor \dots$

Differences between Mathematical Notations and Z Specification

- In Mathemical Notation, : or | means "such that" when used in Set Expressions.
- In Z Specification, : means "belongs to".
 - ullet The difference between : and \in will be explained later.
- In Z Specification, means "such as" when writing predicates.

- A set is a collection of elements (or members)
 - Elements are not ordered: $\{a, b, c\} = \{b, a, c\}$
 - Elements are not repeated" $\{a, a, b\} = \{a, b\}$
 - Given Sets
 - $\mathbb{N} = \{0, 1, 2, 3, \ldots\}$ (The set of all natural numbers)
 - $\mathbb{N}_1 = \{1, 2, 3, \ldots\}$
 - $\mathbb{Z} = \{0, 1, -1, 2, -2, \ldots\}$ (The set of all integers)
 - \bullet \mathbb{R} (The set of all real numbers)
 - Ø (Empty Set: The set with no elements)
- Membership: $x \in \mathbb{X}$ is a predicate which is
 - true if x is in the set \mathbb{X} . Eg: $a \in \{a, b, c\}$
 - false if x is not in the set \mathbb{X} . Eg: $d \in \{a, b, c\}$

Difference between ':' and ' \in '

Example: $\forall x : \mathbb{Z} \bullet x > 5 \Rightarrow x \in \mathbb{N}$

- ullet $x:\mathbb{Z}$ declares a new variable x of type \mathbb{Z}
- $x \in \mathbb{N}$ is a predicate which is either true or false depending on the value of the declared x.

- Set Expressions
 - We can express a set by listing its elements if the set is finite and small.
 - $\{a, b, c, d\}$ is a finite set.
 - If a set is large or infinite, we can definite a set by giving a predicate which specifies precisely those elements in a set.
 - N is an infinite set.
 - The set of natural numbers less than 99 is $\{n : \mathbb{N} \mid n < 99\}$
 - In general the set $\{x : \mathbb{X} \mid P(x)\}$ is the set of elements of \mathbb{X} for which predicate P is true.
- Set Examples
 - The set of even integers is $\{z : \mathbb{Z} \mid \exists k : \mathbb{Z} \bullet z = 2k\}$
 - The set of natural numbers which when divided by 7 leave a remaineder of 4 is $\{n: \mathbb{N} \mid \exists m: \mathbb{N} \bullet n = 7m + 4\}$
 - $\{n: \mathbb{N} \mid \exists m: \mathbb{N} \bullet n = Im +$
 - \mathbb{N} is the set $\{z : \mathbb{Z} \mid z \geq 0\}$
 - \mathbb{N}_1 is the set $\{n : \mathbb{N} \mid n \geq 1\}$
 - If a, b are any natural numbers, then a.. b is defined as the set of all natural numbers between a and b inclusive.
 - a ... b is the set $\{n : \mathbb{N} \mid a < n < b\}$



- Subset (\subseteq): If S and T are sets, $S \subset T$ is a predicate equivalent to $\forall s : S \bullet s \in T$.
 - The following predicates are true:
 - $\{0,1,2\}\subseteq\mathbb{N}$
 - 2..3 ⊆ 1..5
 - $\{a, b\} \subseteq \{a, b, c\}$
 - $\emptyset \subseteq X$ for any set X
 - $\{x\} \subseteq X \Leftrightarrow x \in X$
- Proper Subset (\subset): If S and T are sets, $S \subset T$ is a predicate equivalent to $S \subset T \land S \neq T$.
- Power Set (\mathbb{P}): If X is a set, \mathbb{P} X (the power set of X) is the set of all subsets of X.
 - $A \in \mathbb{P} \ B = A \subseteq B$
 - The following predicates are true:
 - $\mathbb{P}{a,b} = {\emptyset, {a}, {b}, {a,b}}$
 - $\mathbb{P} \emptyset = \{\emptyset\} \neq \emptyset$
 - $1..5 \in \mathbb{P} \mathbb{N}$
 - $2..5 \in \mathbb{P}(1..5)$
 - If X has k elements, then $\mathbb{P} X$ has 2^k elements.



- Set Operations
 - Set Union: Suppose $S, T : \mathbb{P}X$ or $S \subseteq X, T \subseteq X$, then $S \cup T = \{x : X \mid x \in S \lor x \in T\}$
 - $\{a, b, c\} \cup \{b, g, h\} = \{a, b, c, g, h\}$
 - $A \cup \emptyset = A$ (for any set A)
 - Set Intersection: Suppose $S, T : \mathbb{P}X$, then $S \cap T = \{x : X \mid x \in S \land x \in T\}$
 - $\{a,b\} \cap \{b,c\} = \{b\}$
 - $\{a,b,c\} \cap \{d,g\} = \emptyset$ (disjoint sets)
 - $A \cap \emptyset = \emptyset$ (for any set A)
 - Set Difference: Suppose $S, T : \mathbb{P} X$, then $S T = \{x : X \mid x \in S \land x \notin T\}$
 - $\{a, b, c\} \{b, g, h\} = \{a, c\}$
 - $\bullet \ \mathbb{N}_1 = \mathbb{N} = \{0\}$
 - Cartesian Product: If A and B are sets, then $A \times B$ is the set of all ordered pairs (a, b) with $a \in A$ and $b \in B$.
 - $\{a,b\} \times \{a,c\} = \{(a,a),(a,c),(b,a),(b,c)\}$
 - Cardinality: #X is a natural number denoting the cardinality of (number of elements in) a finite set X.
 - $\#\{a,b,c\}=3$



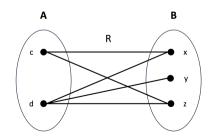
Tuples and Cartesian Product

- An n-tuple (x_1, \ldots, x_n) is present in the Cartesian Product $\mathbf{a_1} \times \ldots \times \mathbf{a_n}$ if and only if each element x_i is an element of the corresponding set $\mathbf{a_i}$.
- \bullet To refer to a particular component of a tuple t, we use the projection notation (.)
- Suppose we have $t = (x_1, x_2, \dots, x_n)$
 - The first component of the tuple t is written as t.1 which is the value x_1 .
 - The second component of the tuple t is written as t.2 which is the value x_2 .
 - The n-th component of the tuple t is written as t.n which is the value x_n

Recap on Relations

- A relation R from sets A to B, is declared as $R: A \leftrightarrow B$ is a subset of $A \times B$
- Example: $R = \{(c, x), (c, z), (d, x), (d, y), (d, z)\}$
 - The following predicates are equivalent

 - $2 c \rightarrow z \in R$
 - cRz
- **Domain:** dom R is the set $\{a: A \mid \exists b: B \bullet aRb\}$
- Range: ran R is the set $\{b : B \mid \exists a : A \bullet aRb\}$





Types in Z Specification

- Z specification language is strongly typed.
- Every expression is given a type.
- Any set can be used as a type.
- The following are equivalent declarations of variables x and y of types A and B respectively.
 - \bullet $(x, y) : A \times B$
 - x : A, y : B
 - x, y : A (only when B = A)

Modelling using Z Specification

- When we write a program, we can write code procedurally, functionally or in an object oriented manner.
- Z Specification can help us model our code using two distinct sections.
 - Declaration: To define variables.
 - Predicate: Often used to define behaviours or invariants.
- Example #1: We can define the relation divides between two natural numbers.

• **Example #2:** We can define the relation \leq between two natural numbers.

The relation <= is the infinite subset of ordered pairs in $\mathbb{N} \times \mathbb{N}$.

$$\{(0,0),(0,1),(1,1),(0,2)(1,2),(2,2),\ldots\}$$

Domain and Range Restriction

- Let *A*, *B*, *R*, *S*, *T* be sets.
- A is the domain set, B is the range set and R is the relation set.
- Note that S is a subset of the domain set and T is the subset of the range set.
- Suppose $R: A \leftrightarrow B, S \subseteq A$ and $T \subseteq B$.
 - **Domain Restriction:** $S \triangleleft R$ is the set $\{(a,b) : R \mid a \in S\}$
 - Range Restriction: $R \rhd T$ is the set $\{(a,b): R \mid b \in T\}$
- Notice that both $S \lhd R \in A \leftrightarrow B$ and $R \rhd T \in A \leftrightarrow B$, meaning that both domain restriction and range restrictions are relations from sets A to B.
- If has_sibling: People ↔ People then
 - \bullet female \lhd has_sibling is the relationship is_sister_of.
 - has_sibling
 Female is the relationship has_sister.



Domain and Range Subtraction

- Let *A*, *B*, *R*, *S*, *T* be sets.
- A is the domain set, B is the range set and R is the relation set.
- Note that S is a subset of the domain set and T is the subset of the range set.
- Suppose $R: A \leftrightarrow B, S \subseteq A$ and $T \subseteq B$.
 - **Domain Subtraction:** $S \triangleleft R$ is the set $\{(a,b) : R \mid a \notin S\}$
 - Range Subtraction: $R \triangleright T$ is the set $\{(a,b) : R \mid b \notin T\}$
- The following predicates are true.
 - $S \triangleleft R = (A S) \triangleleft R$
 - $R \triangleright T = R \triangleright (B T)$
 - $S \triangleleft R \in A \leftrightarrow B$
 - $R \Rightarrow T \in A \leftrightarrow B$
- If has_sibling: People ↔ People then
 - female ← has_sibling is the relationship is_brother_of.



Relational Image

- Suppose the relation $R: A \leftrightarrow B$ and $S \leftrightarrow A$
- $R(|S|) = \{b : B \mid \exists a : S \bullet aRb\}$
- $R(|S|) \leftrightarrow B$
- Example
 - $divides(\{8,9\}) = \{x : \mathbb{N} \mid \exists k : \mathbb{N} \bullet x = 8 \cdot k \lor 9 \cdot k\} = \{0,8,9,16,18,\ldots\}$
 - $<= (|3,7,21|) = \{x : \mathbb{N} \mid x >= 3\}$
- In summary, the relational image returns the set of all elements $b \in B$ such that there exists an $a \in S$ with $(a, b) \in R$.
- The difference between relational image and range restriction is that range restriction returns the subset of R which are ordered pairs of (a, b) where $a \in A$ and $b \in B$ and the first element a of the ordered pair is in S. The relational image simply just returns the set of all second elements b.



Inverse and Relational Composition

- Inverse: R^{-1} is the set $\{(b,a): B \times A \mid aRb\}$ or $R^{-1} \in B \leftrightarrow A$
 - $has_sibling^{-1} = has_sibling$
 - $divisor^{-1} = has_divisor$
- Example: $succ^{-1} = pred$

I succ: $\mathbb{N} \leftrightarrow \mathbb{N}$

|-----

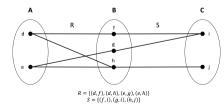
 $\mid \ \forall \, x,y : \mathbb{N} \, \bullet \, x \, \, \, \underline{\mathtt{succ}} \, \, \, y \leftrightarrow x+1 = y$

Relational Composition (§)

- Suppose $R: A \leftrightarrow B$ and $S: B \leftrightarrow C$ are two relations.
- $R \circ S = \{(a,c) : A \times C \mid \exists b : B \mid aRb \wedge bSc\}$
- $R \circ S \in A \leftrightarrow C$

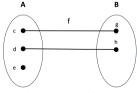
Examples

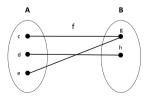
- is_parent_of g is_parent_of = is_grandparent_of
- $R^0 = id[A], R^1 = R$
- $R^2 = R \, {}_{0}^{2} \, R$
- $R^3 = R : R : R$



Recap on Functions

- A (partial) function from a set A to a set B, denoted by $f:A \rightarrow B$ is a subset f of $A \times B$ with the property that for each $a \in A$, there is **at most one** $b \in B$ with $(a, b) \in f$.
- dom f is the set $\{a: A \mid \exists b: B \bullet (a, b) \in f\}$
- ran f is the set $\{b: B \mid \exists a: A \bullet (a, b) \in f\}$
- Suppose $f: A \to B$ and $a \in \text{dom } f$, then f(a) denotes the unique image $b \in B$ that a is mapped to by f.
- $(a, b) \in f$ is equivalent to f(a) = b
- **Total Function:** If the function $f: A \rightarrow B$ is a total function, then $f: A \rightarrow B$ if and only if dom f = A





Function Overriding

- Suppose $f, g : A \rightarrow B$, then $f \oplus g$ is the function $(\operatorname{dom} g \triangleleft f) \cup g$.
- The following predicates are true:

 - 2 $a : \operatorname{dom} g \bullet (f \oplus g)(a) = g(a)$
- Examples

 - odouble \oplus root = $\{(0,0),(1,1),(2,4),(3,6),(4,2),\ldots\}$ (Note: (4,8) was replaced with (4,2) as the domain 4 is both in f and g, so the range was replaced with 2 that was in g)

Specifying Functions

- Using a look-up table
 - If a function $f: A \rightarrow B$ is finite (and not too large), we can specify the function explicitly by listing all pairs (a, b) in the subset $A \times B$.
 - - G707165 19 Mail Street
- ② Declaring Axioms: A function can be specified by giving a **predicate** determining which pairs (a, b) are in the function.
 - Example: The root function that calculates the square root of a natural number



Specifying Functions

Using Recursion: For functions defined recursively in terms of itself.

```
\begin{array}{l} | \text{ fact } \mathbb{N}_1 \rightarrow \mathbb{N} \\ |-----| \\ | \text{ fact(1) = 1} \\ | \forall \, n : \mathbb{N}_1 - \{1\} \bullet \textit{fact}(n) = \textit{n} * \textit{fact}(n-1) \end{array}
```

3 Giving an Algorithm: A function $f: A \rightarrow B$ is specified by an algorithm such that given any element a in the domain of f, the element f(a) can be computed using the algorithm.

```
input n : N
var x, y: integer;
begin
    x := n; y:= 0;
while x != 0 do
begin
    x := x - 1; y:= y + 2
end;
write(y)
end
```

Sequences

• A sequence s of elements of a set A, denoted s : seq A, is a function $s : \mathbb{N} \to A$ where $\text{dom } s = 1 \dots n$ for some natural number n.

Example

- < b, c, a, b > denotes the sequence (function) $\{1 \rightarrow b, 2 \rightarrow c, 3 \rightarrow a, 4 \rightarrow b\}$
- The empty sequence is denoted by <>
- The set of all sequences of elements from A is denoted as $\operatorname{seq} A$ and is defined to be $\operatorname{seq} A = \{s : \mathbb{N} \to A \mid \exists n : \mathbb{N} \bullet \operatorname{dom} s = 1 \dots n\}$
- $seq_1 A = seq A \{ < > \}$ is defined as the set of non-empty sequences.
- Since sequences are ordered mapping, $\langle a, b, a \rangle \neq \langle a, a, b \rangle \neq \langle a, b \rangle$

Special Functions for Sequence

- Concatenation
 - $< a, b > ^ < b, a, c > = < a, b, b, a, c >$
- 4 Head

 - head < c, b, b >= c
- Tail

 - tail < c, b, b > = < b, b >
- Filter
 - $\langle a, b, c, d, e, d, c, b, a \rangle | \{a, d\} = \langle a, d, d, a \rangle$
 - Filter only keeps the element in the specified set, preserves order in the original sequence and outputs a new sequence.

Z Specification

- As explained in an earlier slide, we can write Z Specification to formally specify requirements in two distinct sections
 - Declaration: To define variables.
 - Predicate: Often used to restrain the possible values of the declared variables to define behaviours or invariants.
- Formal Specification can be done in the form of both axioms and schemas.
- When we were specifying functions earlier, we were formally specifying them in the form of **axioms** in the **axiom environment**.
- Later we will introduce how to formally specify **schemas** which are used to specify relationships between variable values.
- There are two main type of schemas in the schema environment
 - State Schema
 - Operation Schema



The zed-csp Package

- We can use the zed-csp package to formally specify requirements in Z Specification and render axioms and schemas them in TeX.
- There are **two types of environment** in the zed-csp package.
- Axiom Environment

```
\begin{array}{|c|c|c|}\hline \textit{limit}: \mathbb{N}\\\hline\hline\\ \textit{limit} \leq 65535\\\hline\\ \end{array}
```

```
1 \begin{axdef}
2     limit: \nat
3     \where
4     limit \leq 65535
5 \end{axdef}
```

Schema Environment

```
1 \begin{schema}{PhoneDB}
2 known: \power NAME \\ phone: NAME \pfun PHONE
3 \where
4 known = \dom phone
5 \end{schema}
```

State Schema

- A state schema specifies a relationship between variable values
- It specifies a snapshot of a system
- Variables are declared and typed in the top part of the schema.
- A **predicate (axiom)** restraining the possible values of the declared variables are given in the **bottom part of the schema**.
- An instance of a schema is an assignment of values to variables consistent with their type declaration and satisfying the predicate.

Operation Schema

- The state schema provides a static view of the system.
- To specify how the system can change, we need to specify the operation schema.
- An operation can be thought as taking an instance of the state schema and producing a new instance.
- To specify such an operation, we express as a predicate the relationship between the instance of the state before the operation and the instance after the operation.

Convention

- The value of the state variables before the operation are denoted by **unprimed identifiers**.
 - Example: items : seq MSG
- Values after the operation are denoted by **primed identifiers**.
 - Example: items': seq MSG
- Note: Let MSG be the set of all possible messages that can be transmitted.

Case Study: A Message Buffer

- We are going to model a message buffer to learn about schema specification.
- A message buffer stores messages within a queue data structure and operates on a first in / first out (FIFO) principle.
- Suppose we have a line which might be occupied by traffic, many messages join the buffer and but only the first message in the queue leaves the buffer when the line is free.
- The buffer may contain several messages at any time, but there is a fixed upper limit on the number of messages the buffer may contain.
- To model a message buffer, we minimally need the following:
 - States: Buffer (to store messages)
 - ② Operations: Join (new message joins the buffer), Leave (message leaves the buffer)



Formal Specification: Message Buffer

State Schema: Buffer

- Let MSG be the set of all possible messages that can be transmitted.
- Let max: N be the constant maximum number of messages that can be held in the buffer at any one time.
- Example: Let $MSG = \{m1, m2, m3\}$ and max = 4
 - ① $items = \langle m1, m2 \rangle$ is a valid instance.
 - items =< m3, m1, m1, m2, m2 > is an invalid instance.

Operation Schema: Join

- The decoration ? denotes an input
- There is an implicit ∧ between each line in the predicate section.

```
\_Join \_
\_items, items' : seq MSG
msg? : MSG
\#items \le max
\#items' \le max
\#items < max
items' = items < msg? >
```

Explanation: Predicate of the Join Operation

```
- Join \_
items, items' : seq MSG
msg? : MSG
\# items \le max
\# items' \le max
\# items < max
items' = items ^ < msg? >
```

- The first two lines of the predicate indicate that we have a valid instance of the state schema Buffer both before and after the operation.
- The third line of the predicate is a pre-condition for the operation. It indicates that for the **Join** operation to be possible, the buffer must not be completely full.
- The last line of the predicate specifies the relationship between the buffer contents before and after the operation which is that the input message is already appended to the sequence of messages already in the buffer.

Formal Specification: Message Buffer (Continued)

Operation Schema: Leave

- The decoration ! denotes an output
- There is an implicit ∧ between each line in the predicate section.
- Explanation for Leave Operation Predicate
 - The first two lines of the predicate indicate that we have a valid instance of the state schema **Buffer** both before and after the operation.
 - The third line of the predicate is a pre-condition for the operation. It indicates that for the Leave operation to be possible, the buffer must not be empty.
 - The last line of the predicate specifies the relationship between the buffer contents before and after the operation. The output message is taken from the head of the sequence of messages in the buffer, leaving just the tail of the sequence of buffers.

Special States: Delta (Δ) and Initial State $({}_{\mathit{INIT}})$

1 Delta (Δ) : To specify a **before** and **after** instance of the state schema for any operation.

```
\Delta Buffer
items, items' : seq MSG
\#items \leq max
\#items' \leq max
```

Initial State (INIT): To specify a state when an instance of a state is first initialized.

- Initially the buffer would be empty.
- Then, the operations of Join and Leave can occur whenever they are enabled.
- Operations are assumed to be atomic.
- At all times, an observer will notice that the state schema is satisfied.



items = <>

Schema Inclusion

- Schema Inclusion is the act of including a schema in the declaration of another schema.
- It means the included schema has its declaration added to the new schema, and its predicate cojoined to the predicate of the new schema.
- The first "S" Schema is the **short form**, while the second "S" Schema is the **long form**.

 $\begin{array}{c}
A \\
x: T_1 \\
y: T_2
\end{array}$ P(x, y)

 $\begin{array}{c|c}
S \\
A \\
z: T_3 \\
\hline
Q(x, y, z)
\end{array}$

 $\begin{array}{c}
S \\
x: T_1 \\
y: T_2 \\
z: T_3
\end{array}$ $P(x, y) \land Q(x, y, z)$

Example: Schema Inclusion

```
Leave \triangle Buffer msg!: MSG items \neq \emptyset items = < msg! > ^items'
```

- ullet We can include the $\Delta Buffer$ schema to both the Join and Leave operations.
- Explanation for Leave Operation Predicate
 - The first line of the predicate is a pre-condition for the operation. It indicates that for the Leave operation to be possible, the buffer must not be empty.
 - The last line of the predicate specifies the relationship between the buffer contents before and after the operation. The output message is taken from the head of the sequence of messages in the buffer, leaving just the tail of the sequence of buffers.



Merging Schemas

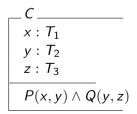
- Type Compatability is needed to merge schemas.
- In this case, the variable y is common between states A and B.
- We can simply merge the two types into a new state *C* without further specifying any new predicates.
- The full form of state *C* is also provided.

$$\begin{array}{c}
A \\
x: T_1 \\
y: T_2
\end{array}$$

$$P(x, y)$$



__C ____ A B



Extending Specifications: Slow Buffer and Slow Operations

- Applying concepts such as Schema Inclusion and Merging Schemas, we can extend specifications similar to inheritance or creating specialized classes in Object-Oriented Programming.
- To demonstrate this, we will attempt to model a Slow Buffer which has a constant delay: N to simulate that each new message can only join the buffer after delay seconds.

SlowBuffer $_$ Buffer
idle: \mathbb{N} Tick $_$ Δ SlowBuffer

idle' = idle + 1
items' = items

 $_SlowBuffer_{INIT}$ SlowBuffer $Buffer_{INIT}$ idle = 0

SlowJoin $\Delta SlowBuffer$ Join $idle \geq delay$ idle' = 0

 $_SlowLeave __$ $\Delta SlowBuffer$ Leave $idle \ge delay$ idle' = 0

Extending Specifications: Slow Buffer and Slow Operations (Full Form)

_SlowBuffer __ items : seq MSG

 $idle: \mathbb{N}$

 $\#items \leq max$

```
\_SlowBuffer_{INIT}
items: seq\ MSG
idle: \mathbb{N}
```

```
\#items \le max
items = <>
idle = 0
```

```
Slow Join \\ items, items' : seq MSG \\ idle, idle' : \mathbb{N} msg? : MSG \\ \\ \#items \leq max \land \#items' \leq max \\ \#items < max \land items' = items^{\land} < msg? > \\ idle > delay \land idle' = 0
```

```
SlowLeave _____items, items' : seq MSG
```

 $idle, idle' : \mathbb{N}$ msg! : MSG

```
#items \leq max \land #items' \leq max
items \neq \emptyset \land items = items' ^{\smallfrown} < msg? > idle \geq delay \land idle' = 0
```

Reasoning About The Specification

- Suppose, we want to verify that message buffer specified has the **FIFO property**.
- We want to show that the messages leave the buffer in the same order they arrive.
- In this case, we introduce **auxillary sequences** inhist and outhist to record the history of the flow of messages into and out of the buffer.
- Create a new schema which includes the original Buffer and Operation schemas and extra information about the auxillary variables.
- When a message **joins** the buffer, it is also added to the inhist sequence.
- When a message **leaves** the buffer, it is added to the outhist sequence.

Recorded Buffer and Operations

. RecordedBuffer _____

Buffer

inhist : seq MSG
outhist : seq MSG

. RecordedBuffer_{INIT} ______

Recorded Buffer

Buffer_{INIT}

 $\mathit{inhist} = <>$

outhist =<>

_ RecordedJoin _____

 $\Delta \textit{RecordedBuffer}$

Join

 $inhist' = inhist ^ < msg? >$

outhist' = outhist

RecordedLeave _____

 $\Delta Recorded Buffer$

Join

inhist' = inhist

 $outhist' = outhist \cap < msg! >$

Recorded Join: Expanded Schema

```
Recorded Join items, items': seq MSG inhist, inhist': seq MSG outhist, outhist': seq MSG msg?: MSG msg?: MSG msg: max \land \#items' \le max \land \#items' = items' < msg? > inhist' = inhist' < msg? > outhist' = outhist
```

Proving the FIFO Property

- How can we use the auxillary variables inhist and outhist to prove that the buffer satisifies the FIFO property?
- We can prove that the predicate \forall RecordedBuffer inhist = outhist \cap items is true.
- Prove using structural induction.
 - Initially inhist = outhist = items = <>, so the predicate is true for the initial state.
 - Suppose the predicate is true, and RecordedJoin occurs. After the operation, inhist' = inhist ∩ < msg? > ∧outhist' = outhist ∧ items' = items ∩ < msg? >
 - Hence, inhist^ < msg? >= (outhist^ items)^ < msg? >
 = outhist^ (items^ < msg? >) = outhist'^ items'
 - Therefore, the predicate remains true.
- We can construct a similar argument that the operation RecordedLeave also preserves the predicate.



Conjunction of Schemas

- When using the conjunction (\land) operator on two schemas, it is equivalent to merging the two schemas.
- Suppose A and B are schemas
 - The declaration of $A \wedge B$ is the **union** of the declarations of A and B
 - The predicate of $A \wedge B$ is the **conjunction** of the predicates of A and B
- Examples
 - **1** SlowRecordedBuffer $\hat{=}$ SlowBuffer \land RecordedBuffer
 - \bigcirc SlowRecordedBuffer_{INIT} $\widehat{=}$ SlowBuffer_{INIT} \land RecordedBuffer_{INIT}
- SlowRecordedBuffer Schema

Disjunction of Schemas

- Using the conjunction (\land) operator on two schemas yields a different result.
- \bullet Suppose A and B are schemas
 - The declaration of $A \vee B$ is the **union** of the declarations of A and B
 - The predicate of $A \vee B$ is the **disjunction** of the predicates of A and B

$$\begin{array}{cccc}
 -A & & & & & & & & \\
 x: T_1 & & & & & & & \\
 y: T_2 & & & & & & \\
 p(x,y) & & & & & & \\
\hline
 P(x,y) & & & & & & \\
\end{array}$$

$$\begin{array}{c}
A \wedge B \\
x : T_1 \\
y : T_2 \\
z : T_3 \\
\hline
P(x, y) \wedge Q(y, z)
\end{array}$$

A \lor B
$x:T_1$
$y:T_2$
$z:T_3$
$P(x,y) \vee Q(y,z)$

Disjunction of Schemas: Example

- Let Flag ::= ok | error (The Flag type can be either 'ok' or 'error')
- Ξ State is another special state that is used for operations that access information in the state without changing the state at all.
- Example: $CompleteJoin = JoinOk \lor JoinError$

```
loinOk
                                                              JoinFrror
loin
                                                              \Xi Buffer
                                                              flag! : Flag
flag! : Flag
flag! = ok
                                                             \#items = max \land flag! = error
 Complete Join
 \Delta Buffer
 msg?: MSG; flag!: Flag
 \#items < max \land items' = item ? < msg? > \land flag! = ok
 V
 \#items = max \land items' = items \land flag! = error
```

Composition of Schemas

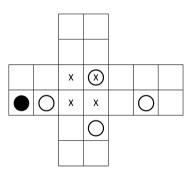
- Using the composition operator (g) on two schemas is typically used to combine the effects of two operations.
- Example: JoinLeave = Join § Leave
 - The pre-state of *Join* is the pre-state of *Join* § *Leave*.
 - The post-state of Join is identified with the pre-state of Leave hidden within Join § Leave.
 - The consequent post-state of *Leave* is the post-state of *Join* § *Leave*.
- Convention: Hidden state is denoted with double prime (").

Composition of Schemas in general

```
 \begin{array}{c} -AOP_1 \ {}_{\$} \ AOP_2 \\ \hline \delta A \\ t_3?: T_3; \ t_4!: T_4; \ t_5?: T_5; \ t_6!: T_6 \\ \hline \hline \exists \ x'': T_1; \ \ y'': T_2 \bullet Q_1(x,x',y,y',t_3?,t_4!) \land Q_2(x,x',y,y',t_5?,t_6!) \end{array}
```

Z Specification Case Study: Shunting Game

- The diagram illustrates the board and the starting state for a shunting game.
- The black piece is the shunter.
- A move consists of the black piece (the shunter) moving one position either vertically or horizontally provided either:
 - The position moved to is empty, or
 - The position moved to is occupied by a white piece but the position beyond the white piece is empty, in which case the white piece is pushed into the empty position.
- The shunter cannot push two white pieces at the same time.
- At each stage, a score is kept of the number of moves made so far.
- The game ends when the white pieces occupy the four positions marked with a cross.



Z Specification Case Study: Shunting Game (Writing the Specification)

- Board == $(1..7 \times 3..4) \cup (3..4 \times 1..6)$
- $over == \{(3,3), (4,3), (3,4), (4,4)\}$

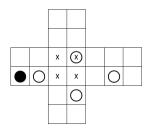
 $next: Board \leftrightarrow Board$

$$\forall (i,j), (k,l) : \textit{Board} \bullet (i,j) \ \underline{\text{next}} \ (k,l) \Leftrightarrow (i=k \land (j=l+1 \lor j=l-1)) \lor (j=1 \land (i=k+1 \lor i=k-1))$$

 $\textit{beyond}: \textit{Board} \times \textit{Board} \rightarrow \mathbb{N} \times \mathbb{N}$

dom beyond = b, w: Board | b next w

 $\forall b, w : \text{dom beyond} \bullet beyond(b, w) = 2w - b$





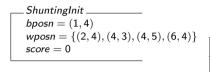
Z Specification Case Study: Shunting Game (Writing the Specification)

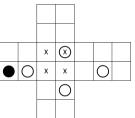
_Shunting _

bposn : Board wposn : ℙ Board

 $score: \mathbb{N}$

 $bposn \not\in wposn \land \#wposn = 4$





Over _____

 $\Xi Shunting$ score! : \mathbb{N}

wposn = over
score! = score

Move ____

 $\Delta Shunting$

 $wposn \neq over$ $bposn' \underbrace{next}_{} bposn$

 $bposn' \not\in wposn \Rightarrow wposn' = wposn$

 $bposn' \in wposn \Rightarrow wposn' = (wposn - bposn') \cup beyond(bposn, bposn')$

score' = score + 1

4 D > 4 B > 4 E > 4 E > E 9 Q C

CSP Prelude: Automatic Reasoning & Verification

- Z Specification is good at **capturing high level requirements**.
- However, if we want to answer the following questions, we need a formal language that can perform automatic reasoning and verification.
 - What is a valid trace of moves in the Shunting Game?
 - Is the goal state (a.k.a "over") reachable?
 - If the goal state is reachable, are there any bad moves preventing the goal?
 - What is the minimum number of moves to reach the goal?
- The answers to these questions would be answered in **process modelling using CSP** in the next section.

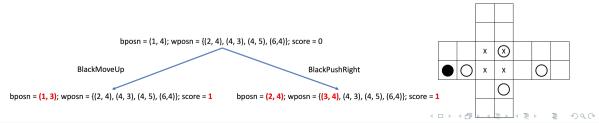


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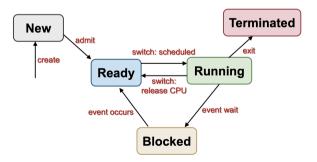
Introduction to CSP

- Communicating Sequential Processes (CSP) is a formal language to describe patterns interaction between concurrent processes in computer systems.
- CSP was created by **Tony Hoare** to reason about the behaviour of concurrent systems.
- CSP provides event based notation primarily aimed at describing the sequencing of behaviour within a process and the synchronisation of behaviour (or communication) between processes.
- In CSP, there are two main fundamental concepts
 - Process
 - 2 Event
- Events represent a co-operative synchronisation between process and environment.
- Both process and environment may control the behaviour of each other by enabling or refusing certain events or sequences of events.



Recap: Process State Model

- To gain a better understanding of Communicating Sequential Processes, revisit the first half of CS2106: Operating Systems where Processes, Concurrency, Synchronisation and Semaphores were covered.
- The Process State Model is shown here to help you visualize potential process behaviour that can be modelled using CSP.



Specifying a Process

- A process is determined or specified by what it can do.
- In other words, a process is defined by its behaviour, which are the events that we can observe.
- The perceived behaviour of a process will depend on the observer.
- In this course, we are mainly concerned with specifying the interaction between a system and its environment which is also the **external or visible behaviour**.

Concepts in CSP: Events

- A process engages in events.
- Each event is an atomic action.

Alphabet

The set of events a process can possibly engage in is the alphabet of the process.

- Example: Chocolate Vending Machine
- Events for a chocolate vending machine
 - oin insert a coin
 - 2 choc extract a chocolate
- The alphabet of a chocolate vending machine is {coin, choc}

Concepts in CSP: Trace

Trace

A finite sequence of events

- A deterministic process is specified by the set of processes denoting its possible behaviour.
- Any execution of the process will be one of these sequences.
- Example: The traces of the chocolate vending machine are:
 - ()
 - ⟨coin⟩
 - ⟨coin, choc⟩
 - ⟨coin, choc, coin⟩
 - . . .
- Any execution of the process will be one of the above sequences or traces.
- If $s \cap t$ is a trace of a process, then s is also a trace of a process. This means that set of traces is prefixed closed.
 - Example: Let $s = \langle coin, choc \rangle$, $t = \langle coin \rangle$, then $s \cap t = \langle coin, choc, coin \rangle$.

Notations and Conventions

- Events are denoted in lower case
 - Example: x, y, z are variables that denote events.
- Processes are denoted in upper case
 - Example: X, Y, Z are variables that denote processes.
- The **alphabet** of a process P is denoted by αP
- The set of traces of *P* is denoted by traces(P)
- Trace Notation
 - If A is a set of events, then seq A denotes the set of all finite sequences of events from A.
 - In this scenario, α P = A and trace(P) = seq A
 - Let $s, t : seq A, s \cap t$ be the concatenation of s with t.
 - We define the relation <= to be the **sequence prefix** of two sequences.

$$<=: \operatorname{seq} A \leftrightarrow \operatorname{seq} A$$

$$s <= t \Leftrightarrow \exists u : \operatorname{seq} A \bullet s \cap u = t$$

• $s^n = s \cap s \cap s \cap \ldots \cap s$ denotes the event s concatenated with itself n times.

Examples of Process Specification using CSP

- Let $STOP_A$ be the process with alphabet A that can do nothing.
 - $traces(STOP_A) = \{\langle \rangle \}$
- **②** Let CLOCK be the process with $\alpha CLOCK = tick$ which can 'tick' at any time.
 - $traces(Clock) = tick^*$ where $* \ge 0$
- **3** Let VM be the process with $\alpha VM = \{coin, choc\}$ which repeatedly supplies a chocolate after a coin has been inserted.
 - $traces(VM) = \{s : seq\{coin, choc\} \mid \exists n : \mathbb{N} \bullet s <= \langle coin, choc \rangle^n \}$
 - Note: <= is the sequence prefix relation from the previous slide.
- **1** Let WALK be a one-dimensional random walk process with $\alpha WALK = \{left, right\}$.
 - $traces(WALK) = (left \lor right)^*$ where $* \ge 0$
- **1** Let LIFE be the process with $\alpha LIFE = \{beat\}$ which can stop (die) at any time.
 - $traces(LIFE) = beat^*$ where $* \ge 0$



CSP Syntax

- **1** Prefix: $a \rightarrow P$
- Sequential Composition: P; Q
- **3** Parallel Composition (Synchronous): P[X] Q
- Interleaving (Asynchronous): P ||| Q
- **⑤** Choice: $a \rightarrow P \square b \rightarrow Q$
- **1** Interrupt Process: $P \nabla e \rightarrow Q$





Prefix

- A process which may participate in event a then act according to process description P is written as: a -> P.
- a is the event prefix to P.
- The **event a** is initially enabled by the process and occurs as soon as it is requested by its environment. All other events are refused initially.
- The **event a** is sometimes referred to as the guard of the process.
- Examples
 - **1** $VMU = coin \rightarrow STOP$
 - **2** SHORTLIFE = $(beat \rightarrow (beat \rightarrow STOP)) = beat \rightarrow beat \rightarrow STOP$
 - **3** $VMS = coin \rightarrow choc \rightarrow STOP$



Sequential Composition (P; Q)

- Let ✓ be the Termination event.
- The process which may only terminate is written as *SKIP*.
- Let $SKIP = \checkmark \rightarrow STOP$.
- The sequential composition of processes P and Q, written as P; Q, acts as P until P terminates by communicating ✓ and then proceeds to act as Q.

Parallel Composition

- The parallel composition of processes P and Q synchronised on the event set X is written as P |[X]| Q.
- No event from X may occur in P | [X] | Q unless **jointly enabled** by both P and Q.
- When events from X occur, they occur in both P and Q simultaneously, and are referred to as **synchronisations**.
- Events not from X may occur in either P or Q seperately but not jointly.
- Example: $(a \rightarrow P) | [a] | (c \rightarrow a \rightarrow Q)$
 - All a events must be Synchronous between the two processes.
- Often, it is simply written as $P \parallel Q$ where the common event set $X = \alpha P \cap \alpha Q$ is omitted.
 - When $P \parallel Q$ is given, we still know that all common events in $X = \alpha P \cap \alpha Q$ must be synchronous between P and Q.



Interleaving

- \bullet P ||| Q denotes an asynchronous parallel composition between two processes **P** and **Q**.
- Both components **P** and **Q** execute concurrently without any synchronisation.
- Example: $((a \rightarrow P) \mid \mid \mid (c \rightarrow a \rightarrow Q))$
 - One possible trace is $\langle c, a, a \rangle$, after which the process acts as $P \parallel \parallel Q$
 - **①** c from $c \rightarrow a \rightarrow Q$ is engaged, leaving us with $((a \rightarrow P) \mid\mid\mid (a \rightarrow Q))$
 - ② a from $a \to P$ is engaged, leaving us with $(P \mid || (a \to Q))$
 - **3** a from $a \rightarrow Q$ is engaged, leaving us with $P \parallel \mid Q$
 - Another possible trace is $\langle a, c, a \rangle$, after which the process acts as $P \parallel \parallel Q$
 - **1** a from $a \to P$ is engaged, leaving us with $(P \mid || (c \to a \to Q))$
 - 2 c from $c \rightarrow a \rightarrow Q$ is engaged, leaving us with $(P \mid || (a \rightarrow Q))$
 - **3** a from $a \rightarrow Q$ is engaged, leaving us with $P \mid \mid \mid Q$



Choice

- In a general choice, $(a \to P) \Box (b \to Q)$, the process begins with both events **a** and **b** enabled.
- The subsequent behaviour depends on the event which occured.
 - If the event which occured is **a**, the process will act as **P** afterwards.
 - If the event which occured is **b**, the process will act as **Q** afterwards.
- There are other types of choices, namely external or internal choice in the CSP syntax.
- For most cases, general choice is sufficient, hence in this module, we focus on general choice only.
- Example: $(a \rightarrow P) \Box (c \rightarrow a \rightarrow Q)$
 - If the first event is **a**, after which the process acts as *P*.
 - If the first event is **c**, after which the process acts as $a \rightarrow Q$.



Interrupt

- The interrupt process $P \nabla e \rightarrow Q$ behaves as **process P** until the first occurrence of **event e** which then the control passes to **process Q**.
- When coding the specification, the keyword **interrupt** is used instead of the symbol ∇ .
- For the System process, the first event can be a routine or an exception.
- After that, it still behaves as a System process.

```
Err() = exception -> Err();
Routine() = routine -> Routine();
ExceptionHandling() = Routine() interrupt exception -> ExceptionHandling();
System = Err() || ExceptionHandling();
```



⁰Note that in the zed-csp package, the interrupt symbol is \triangle .

Concurrency Example #1

We are given the following system specification in CSP

```
1 VMC = coin -> ((choc -> VMC)[](bisc -> VMC));
2 CHOCLOV = choc -> CHOCLOV [] coin -> choc -> CHOCLOV;
3 #alphabet VMC{coin, choc, bisc};
4 #alphabet VMC{coin, choc, bisc};
5 System = VMC || CHOCLOV;
```

- Semi-colon marks the end of each statement.
- When coding process specifications in CSP, we use the keyword **#alphabet**, instead of the symbol α .
- The only possible trace for this example is $\langle coin, choc \rangle^n$ for $n : \mathbb{N}_1$ after which the system acts as $VMC \parallel CHOCLOV$.
 - As defined in lines 3 and 4 of the specification, the common events are the alphabets of VMC and CHOCLOV which are coin, choc and bisc.
 - However, the process CHOCLOV does not have the event bisc.
 - ullet In VMC, the **coin** event has to be engaged first before we can engage choc o VMC
 - Hence, both VMC and CHOCLOV engages in the **coin** event first then the **choc** event before acting as $VMC \parallel CHOCLOV$ again.

Concurrency Example #2

We are given the same specification as the previous slide in CSP except that we now do not define the alphabet for the **VMC** and **CHOCLOV** processes.

```
1 VMC = coin -> ((choc -> VMC)[](bisc -> VMC));
2 CHOCLOV = choc -> CHOCLOV [] coin -> choc -> CHOCLOV;
3 System = VMC || CHOCLOV;
```

- If we did not explicity define the alphabet for each process, it can be auto inferred.
 - $\alpha VMC = \{coin, choc, bisc\}$
 - $\alpha CHOCLOV = \{coin, choc\}$
- In this specification the system may deadlock with the following trace $\langle coin, bisc \rangle$.
 - After (coin, bisc), no event is possible.
 - This is because now the **bisc** event is not common to both **VMC** and **CHOCLOV** processes.
 - Hence, the **bisc** event can occur seperately.
 - After $\langle coin, bisc \rangle$ has occurred, the system would be stuck at $VMC \parallel choc \rightarrow CHOCLOV$.
 - Since **coin** and **choc** are common events, neither events can be engaged synchronously as **coin** is a prefix for **choc**.

Concurrency Example #3

We are given the following system specification in CSP

```
1 VMH = on -> coin -> choc -> off -> VMH;
2 CUST = on -> ((coin -> bisc -> CUST) [] (curse -> coin -> choc -> CUST));
3 System = VMH || CUST;
```

- The common events between **VMH** and **CUST** are **on**, **coin** and **choc** and they must occur synchronously in the two processes.
- $\langle on, curse, coin, choc, off \rangle$ is a possible trace.
 - After the trace, the process will still behave as a System process.
- Deadlocks can occur with the following trace (on, coin, bisc)
 - The system will be stuck at $choc o off o VMH \parallel CUST$ and no event can be engaged.

Concurrency Example #4

We are given the following system specification in CSP

```
SLOWALK = left -> rest -> SLOWALK [] right -> rest -> SLOWALK;

SLOCLIMB = up -> rest -> SLOCLIMB [] down -> SLOCLIMB;

System = SLOWALK || SLOCLIMB;
```

Are the following traces possible?

- \bigcirc $\langle up, rest \rangle$
 - No. The common event between SLOWALK and SLOCLIMB is rest.
 - $\langle up, left, rest \rangle$ and $\langle up, right, rest \rangle$ are possible traces.
- - Yes. For example $\langle left, up, rest \rangle$ and $\langle right, up, rest \rangle$.

Laws for Concurrency

- Law 1: $P \parallel Q = Q \parallel P$
- Law 2: $P \parallel (Q \parallel R) = (P \parallel Q) \parallel R$
- Law 3: $P \parallel STOP_{\alpha P} = STOP_{\alpha P}$

Let...

- $\bullet \quad a \in (\alpha P \alpha Q)$
- $b \in (\alpha Q \alpha P)$
- Law 4A: $(c \rightarrow P) \parallel (c \rightarrow Q) = c \rightarrow (P \parallel Q)$
- Law 4B: $(c \rightarrow P) \parallel (d \rightarrow Q) = STOP$ if $c \neq d$
- Law 5A: $(a \rightarrow P) \parallel (c \rightarrow Q) = a \rightarrow (P \parallel (c \rightarrow Q))$
- Law 5A: $(c \rightarrow P) \parallel (b \rightarrow Q) = b \rightarrow ((c \rightarrow P) \parallel Q)$
- Law 6: $(a \rightarrow P) \parallel (b \rightarrow Q) = a \rightarrow (P \parallel (b \rightarrow Q)) \square b \rightarrow ((a \rightarrow P) \parallel Q)$

Channel

- Processes may communicate through channels.
- A channel is like a message buffer for one process to send a value to another process.
- A channel event is written as one of the following forms:

Form	Description
c!n	Channel Output. This event occurs when a process writes n (a value)
	to the tail of channel c's buffer
c?n	Channel Input. This event occurs when a process reads a value from
	the head of channel c's buffer to a local variable n
c.n	Channel output and its matching channel input are engaged together
	by two processes.

Channel Example

Suppose we are given the following specification in CSP.

```
channel c 1; // Channel with buffer size = 1

Sender(i) = c!i -> Sender(i);

Receiver() = c?x -> a.x -> Receiver();

System() = Sender(5) ||| Receiver();
```

- Note: A process can have optional parameters, eg: Sender(i)
- The first event must be c!5 since c's buffer is empty.
- The second event must be c?5 since c's buffer size is 1.
- The third event can either be c!5 or a.5

Channel Example: Synchronous Buffer

Suppose we are given the following specification in CSP.

```
channel c 0; // Synchronous Buffer
Sender(i) = c!i -> Sender(i);
Receiver() = c?x -> a.x -> Receiver();
System() = Sender(5) ||| Receiver();
```

- Note: A synchronous buffer is defined by setting the buffer size to 0.
- The first event must be c.5, since the sender must write to the c's buffer and the reciever must read from c's buffer simultaneously.
- The second event must be a.5

CSP Model Checkers: Automatic Reasoning

- A CSP Model Checker can check if a property is always satisfied.
- In the next part, we will explore how we can verify certain properties of our process model using PAT (CSP#).
- Suppose we are given the specification of the **Dining Philosophers Problem**.

- $\| x : \{1 ... n\} @P(x)$ is equivalent to $P(1) \| ... \| P(n)$
- #assert College() deadlockfree is the property we want to verify.
- If the property isn't always True, the Model Checker gives a counter example: $\langle get.1.0, get.0.1 \rangle$

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Introduction to Process Analysis Toolkit (PAT)

- The Process Analysis Toolkit is a self-contained framework to support composing, simulating and reasoning of concurrent, real-time systems and other possible domains.
- In PAT 3.5, there are 11 developed modules provided to model a variety of different systems ranging from Communicating Sequential Processes (CSP) Module, Real-Time System Module to the Web Service Module.
- In this module, we will be focusing on the Communicating Sequential Programs (CSP#) module

Introduction to PAT's CSP#

- PAT's CSP# module supports a rich modeling language named CSP#(pronounced 'CSP sharp', short for Communicating Sequential Programs)
- CSP# combines high-level modeling operators like (conditional or non-deterministic) choices, interrupt, (alphabetized) parallel composition, interleaving, hiding, asynchronous message passing channel, etc., with programmer-favored low-level constructs like variables, arrays, if-then-else, while, etc...
- CSP# offers great flexibility on how to model systems. For instance, communication among processes can be either based on shared memory (using global variables) or message passing (using asynchronous message passing or CSP-style multi-party barrier synchronization).
- The high-level operators are based on the classic process algebra Communicating Sequential Processes (CSP).
- The design principle for CSP# is to maximally keep the original CSP as a sub-language of CSP#, whilst offering a connection to the data states and executable data operations.

Operational Semantics

- Earlier, we want a way to automatically reason if a model satisfies a certain property.
- Hence, we will need to study **Operational Semantics** to understand how a process transitions from one state to another during the execution of CSP Specification.
- Operational Semantics tell us at given a system state, what are the possible actions the system can perform and what are the outcomes (next state)?
 - Example: $P \stackrel{a}{\rightarrow} Q$
 - The above is read as: If process P engages event a, it will become process Q.
 - In most cases the states are the process and actions are the events.
 - We will see in later examples what are states and actions.
- Operational Semantics can be presented using a set of inference rules in the following form similar to the philosophy of logic.

Premises

Conclusion



Operational Semantics: Primitives

- STOP (A process that does nothing)
- SKIP

$$SKIP \xrightarrow{\checkmark} STOP$$

SKIP can only engage the **termination event**, afterwards it becomes STOP.

Prefixing

 $a \rightarrow P$ can only engage event **a**, afterwards it becomes process **P**.



Operational Semantics

General Choice

If P is choosen

$$\frac{P \stackrel{a}{\rightarrow} P'}{(P \square Q) \stackrel{a}{\rightarrow} P'}$$

• If Q is chosen

$$\begin{array}{c|c} Q \xrightarrow{S} Q' \\ \hline (P \square Q) \xrightarrow{a} Q \end{array}$$

Sequential Composition

- In process P;Q, P takes control first and Q starts only when P has finished.
- Let ✓ be the termination event.

$$P \xrightarrow{a} P'$$

$$(P; Q) \xrightarrow{a} (P'; Q)$$

$$\begin{array}{c|c}
P \xrightarrow{\checkmark} P' \\
\hline
(P \square Q) \xrightarrow{\checkmark} Q
\end{array}$$

Interrupt

 In process P ∇ Q, whenever an event is engaged by Q, P is interrupted and the control is transferred to Q.

$$P \xrightarrow{a} P'$$

$$(P\nabla Q) \xrightarrow{a} (P'\nabla Q)$$

$$\frac{Q \xrightarrow{a} Q'}{(P\nabla Q) \xrightarrow{a} Q}$$



Operational Semantics: Example #1

• Let $VMS = coin \rightarrow (choc \rightarrow VMS \square bisc \rightarrow VMS)$

Operation Semantics

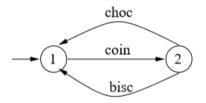
```
Step 1. VMS \xrightarrow{coin} (choc \rightarrow VMS \square bisc \rightarrow VMS)
```

Step 2.
$$(choc \rightarrow VMS \square bisc \rightarrow VMS) \xrightarrow{choc} VMS$$

Step 2.
$$(choc \rightarrow VMS \square bisc \rightarrow VMS) \xrightarrow{bisc} VMS$$

Labelled Transition System (LTS)

• A Labelled Transition System contains a set of states, an initial state (where the system starts from) and a labelled transition relation.



- Let $VMS = coin \rightarrow (choc \rightarrow VMS \square bisc \rightarrow VMS)$
- State 1 represents the process VMS.
- State 2 represents the process $(choc \rightarrow VMS \square bisc \rightarrow VMS)$



¹The Labelled Transition System is a directed graph

Operational Semantics

Interleaving

- In process P ||| Q, P and Q behaves independently.
- The exception is the termination, hence assume a is not √.

$$\begin{array}{c}
P \xrightarrow{a} P' \\
\hline
(P \mid \mid \mid Q) \xrightarrow{a} (P' \mid \mid \mid Q)
\end{array}$$

$$\begin{array}{c}
Q \xrightarrow{a} Q' \\
\hline
(P \mid\mid\mid Q) \xrightarrow{a} (P \mid\mid\mid Q')
\end{array}$$

Synchronization

- In process P | [X] | Q, no event from X may occur unless jointly by both P and Q.
- When events from X do occur, they occur in P and Q simultaneously.

$$P \xrightarrow{a} P', a \notin X$$

$$(P | [X] | Q) \xrightarrow{a} (P' | [X] | Q)$$

$$Q \xrightarrow{3} Q', a \notin X$$

$$(P | [X] | Q) \xrightarrow{3} (P | [X] | Q')$$

$$P \xrightarrow{a} P', Q \xrightarrow{a} Q', a \in X$$

$$(P \mid [X] \mid Q) \xrightarrow{a} (P' \mid [X] \mid Q')$$

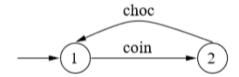
Operational Semantics: Example #2

Given the process $a \to P \mid [a] \mid (c \to a \to Q)$

- ① $(a \to P \mid [a] \mid (c \to a \to Q)) \xrightarrow{c} (a \to P \mid [a] \mid (a \to Q))$ Only event **c** can be engaged at first as **a** is a common event in both $a \to P$ and $c \to a \to Q$.
- ② $(a \to P \mid [a] \mid (a \to Q)) \xrightarrow{a} (P \mid [a] \mid Q)$ Engage the common event **a** on both $a \to P$ and $a \to Q$.

Operational Semantics: Example #3

- $VMC = coin \rightarrow (choc \rightarrow VMC \square bisc \rightarrow VMC)$
- $CHOCLOV = choc \rightarrow CHOCLOV \square coin \rightarrow choc \rightarrow CHOCLOV$
- **1** How would the process $VMC \mid [A] \mid CHOCLOV$ behave when $A = \{coin, choc, bisc\}$
 - Step 1: $VMC \mid [A] \mid CHOCLOV \xrightarrow{coin} (choc \rightarrow VMC \mid bisc \rightarrow VMC) \mid [A] \mid (choc \rightarrow CHOCLOV)$
 - Step 2: $(choc \rightarrow VMC \square bisc \rightarrow VMC) |[A]| (choc \rightarrow CHOCLOV) \xrightarrow{choc} VMC |[A]| CHOCLOV$

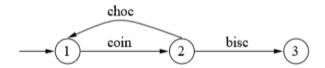


Operational Semantics Example: #4

- $VMC = coin \rightarrow (choc \rightarrow VMC \square bisc \rightarrow VMC)$
- $CHOCLOV = choc \rightarrow CHOCLOV \square coin \rightarrow choc \rightarrow CHOCLOV$
- **②** How would the process $VMC \mid [\{coin, choc\}] \mid CHOCLOV$ or equivalently $VMC \mid CHOCLOV$ behave?
 - Step 1: $VMC \parallel CHOCLOV \xrightarrow{coin} (choc \rightarrow VMC \square bisc \rightarrow VMC) \parallel (choc \rightarrow CHOCLOV)$
 - Step 2:

$$(\textit{choc} \rightarrow \textit{VMC} \; \Box \; \textit{bisc} \rightarrow \textit{VMC}) \, |[\, A\,]| \, (\textit{choc} \rightarrow \textit{CHOCLOV}) \xrightarrow{\textit{choc}} \textit{VMC} \, |[\, A\,]| \, \textit{CHOCLOV}$$

• Step 2: $(choc \rightarrow VMC \square bisc \rightarrow VMC) | [A] | (choc \rightarrow CHOCLOV) \xrightarrow{choc} VMC | [A] | (choc \rightarrow CHOCLOV)$



Case Study: Dining Philosophers

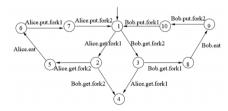
Specify the dining philosophers

- ② Get the alphabets of each process
 - $\alpha Alice = \{Alice.get.fork1, Alice.get.fork2, Alice.eat, Alice.put.fork1, Alice.put.fork2\}$
 - $\bullet \ \alpha Bob = \{Bob.get.fork1, Bob.get.fork2, Bob.eat, Bob.put.fork1, Bob.put.fork2\}$
 - αFork1 = {Alice.get.fork1, Alice.put.fork1, Bob.get.fork1, Bob.put.fork1}
 - α Fork2 = {Alice.get.fork2, Alice.put.fork2, Bob.get.fork2, Bob.put.fork2}

Case Study: Dining Philosophers

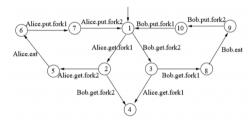
- Apply the operational semantics rule (one at a time) to build the Labelled Transition System.
 - Alice can perform Alice.get.fork1
 - Bob can perform Bob.get.Fork2
 - Fork1 can perform Alice.get.fork1 or Bob.get.fork1
 - Fork2 can perform Alice.get.fork2 or Bob.get.Fork2
 - By rule syn3, College can perform either Alice.get.fork1 or Bob.get.fork2, and then a state of the form.

$$\cdots \parallel \cdots \parallel \cdots \parallel \cdots$$



Case Study: Dining Philosophers

- Analyze the Labelled Transition system
 - Is the system deadlock-free?
 - Will Alice or Bob starve to death?



Safety

- Safety means something bad never happens.
- Examples
 - deadlock-freeness

```
#assert College() deadlockfree;
```

The system never deadlocks

invariant

```
# #assert Bank() |= [] Value >= Debit;
```

The savings of a bank account must always be non-negative.

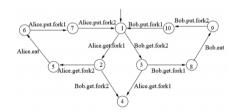


^{1&#}x27;[]' signifies 'always' in Linear Temporal Logic.

^{2&#}x27; = represents 'satisfisfaction' in Linear Temporal Logic.

Verifying Safety

- To verify safety, perform reachability analysis on the Labelled Transition System.
- A counterexample to the safety property is a finite execution which leads to a bad state.
- Perform either Depth First Search (DFS) or Breadth First Search (BFS) to search all reachable states for a 'bad' one.
- Example:



- **1** Depth First Search: $1 \rightarrow 2 \rightarrow 5 \rightarrow 7 \rightarrow 1 \rightarrow \text{backtrack} \rightarrow 4 \rightarrow \text{FOUND!}$
- **2** Breadth First Search: $1 \rightarrow 2 \rightarrow 3 \rightarrow 5 \rightarrow 4 \rightarrow \text{FOUND}!$



¹State 4 is the bad state as there is no outgoing edges.

Applications of Safety Verification

Many properties can be formulated as a safety property and solved using **reachability analysis**.

- Mutual Exclusion: []!(more than one process accessing the critical section)
 - There will never be more than one process accessing the critical section.
- Security: [](only the authorized user can access the information)
 - It is always the case that only the authorized user can access the information.
- Program Analysis
 - Arrays are always bounded.
 - Pointers are always non-null
 - etc...

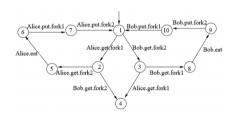


Liveness

- Liveness means something good eventually happens.
- Examples
 - **1** A program is eventually terminating.
 - A file writer is eventually closed.
 - Soth Alice and Bob eventually get to eat.

Verifying Liveness

- To verify liveness, perform loop searching on the Labelled Transition System.
- A counterexample to a liveness property is an infinite system execution during which the 'good' thing never happens.
 - Example: An infinite loop fails the property that the program is eventually terminating.
- We can search through the Labelled Transition System for a bad loop using Nested
 Depth First Search or Strongly Connected Component based Search
- Example



Assertion: Alice will always eventually eat. (False)

#assert College() |= Alice.eat

Counterexamples

- $\langle Alice.get.fork1, Bob.get.fork2 \rangle$
- < Bob.get.fork2 \rightarrow Bob.get.fork1 \rightarrow Bob.eat \rightarrow Bob.put.fork2, \rightarrow Bob.put.fork1 >*

CSP#: Extending CSP

- The original CSP has no shared variables, arrays, etc. . .
- PAT (CSP#) extends it with data operations.
- Hence, the operational semantics must be updated to support data operations.

Example

var x = 0;

$$P = (a\{x = 1\} \rightarrow P) \square (b \rightarrow Skip);$$

P (with valuation var x = 0) $\stackrel{a}{\rightarrow}$ P (with valuation var x = 1)

P (with valuation var x = 0) \xrightarrow{b} Skip (with valuation var x = 1)



Constants

```
1 #define max 5;
```

Enumerations

```
enum {red, blue, green};
// Syntactic Sugar for the following
define red 0;
#define blue 1;
#define green 2;
```

Note

- The constant value can only be of integer or boolean value.
- #define is a keyword used for multiple purposes. Here it defines a global constant.
- (;) semi-colon marks the end of the 'sentence'.

Variables

```
var knight = 0;
```

Arrays

```
// A fixed sized array may be defined as follows:
var board = [3, 5, 6, 0, 2, 7, 8, 4, 1];
// If we do not specify the elements in an array,
// all elements in array are initialized to 0
var leader[3]; // Array of size 3.
var matrix[3][2] // Multi-dimensional array (internally an array of 6)
```

Note

- Multi-dimensional arrays are internally converted to one-dimension.
- The var keyword is used to defined variables.
- The scope of these variables are global if they are not within an event or process.

• Often, it is desirable to provide the range of the variables / arrays explicity by giving the lower bound or upper bound or both.

```
var knight:{0..} = 0;
var board:{0..10} = [3, 5, 6, 0, 2, 7, 8, 4, 1];
```

• Array Initialization: To ease modelling, PAT supports fast array initialization using the following syntax

```
1 #define N 2;
2 // Initialized array with syntax shortcuts
3 var array = [1(2), 3..6, 7(N * 2), 12..10];
4 // The above is the same as the following
5 var array = [1, 1, 3, 4, 5, 6, 7, 7, 7, 7, 12, 11, 10];
```

Macro

• The keyword **#define** may be used to define macros.

```
1 #define goal x == 0;
```

Explanation

- goal is the name of the macro
- \bullet x == 0 is what the goal means.
 - We can use macro to define system properties.

```
#assert System() reaches goal;
```

We can also use macro to define processes

```
if (goal) { P } else { Q };
```

Explanation

If the value of x is 0 then do P else do Q.

- PAT only supports integer, boolean and integer arrays for the purpose of efficient verification.
- However, advanced data structures (eg: Stack, Queue, Hashtable, etc...) are necessary for some models.
- To support arbitrary data structures, PAT provides an interface to create user defined data types by inheriting an **abstract class** ExpressionValue using the **C# library**.
- For more details:
 - PAT User Manual: Section 2.5.2 User Defined Data Types

- Event Prefixing
 - Basic Form

```
1 e -> p;
2 VM() = coin -> coffee -> VM();
```

- Compound Form
 - For example in x.exp1.exp2, x is the event name and exp1 and exp2 are expressions.
 - Each expression corresponds to a variable (eg: process parameters, channel input variables or global variables).

```
#define N 2;
// Dining Philosophers Example
Phil(i) = get.i.(i + 1) % N -> Rest();
```

- Statement Block inside Events (aka Data Operations)
 - An event can be attached with assignments which update global or local variables.
 - Process arguments and channel inputs can only used without being updated.
 - Semi-colons (;) mark the end of a statement in C# or end of a sentence in CSP#.

```
var array = [0, 2, 4, 7, 1, 3];
var maxi = -1;
P() = findmax {
   var index = 0;
   while (index < 6) {
        if (maxi < array[index]) { maxi = array[index]; }
        index = index + 1;
};
};
</pre>
```

Note

 \bullet P() = ... is equivalent to defining process P without any process parameters.

• Conditional Choice: A choice may depend on a Boolean expression which in turn depends on the valuation of the variables.

```
var x = 1;
Init = []i{1,2}@set.i{x = i} -> Skip;
P = if (x == 1) { a -> Stop } else { b -> Stop };
System = Init;P; // Sequential composition of two processes
```

• Guarded Process: A guarded process only executes when its guard condition is satisfied.

```
var x = 1;
Init = []i{1,2}@set.i{x = i} -> Skip;
P = [x == 1] a -> Stop [] [x != 1] b -> Stop;
System = Init;P; // Sequential composition of two processes
```

Note

- []i:{1, 2} means choice for variable i which can be either 1 or 2.
- In both examples, Process P behaves differently depending of the value of variable x.
- Both conditional choice and guarded process can produce the same effect.

- Guarded Process: A guarded process only executes when its guard condition is satisfied.
- In the example, the trace $\langle b, b, a \rangle$ is possible but $\langle b, b, b, a \rangle$ is not possible.

```
var x = 0;
P = [x < 4] b{x = x + 1;} -> P;
aSys = [x == 2] a -> Stop ||| P;
```

CSP#: Process Definition

• Atomic Process:

- The keyword atomic is used to indicate that a process is of higher priority.
- This means if the atomic process has an enabled event, the event will execute before any events from non-atomic processes.

```
channel ch 0;
P = atomic { a -> ch!0 -> b -> Skip };
Q = atomic { d -> ch?0 -> e -> f -> Skip };
W = g -> Skip;
System = P | | | Q | | | W;
```

- In the example above, processes P and Q are both atomic processes, while process W is not.
- The expected behaviour is that processes P and Q will interleave each other (only synchronised on ch.0), whereas W will execute only after event b and f have occured.

Note

Since the channel size is 0, processes P and Q have to synchronise at the channel events.

CSP#: Assertions

- Deadlock-freeness: The following assertion asks if process P() is deadlock-free or not.
 - A deadlock state is a state with no further move, except for successfully terminated state.

```
P = a -> Skip;
#assert P deadlockfree; // True
```

- **Reachability:** The following assertion asks whether process P() can reach a state at which *some given condition is satisified.*
 - In this example, the assertion is True because it can reach a state where x < 0.

```
1 var x = 0;
2 P() = add{x = x + 1;} -> P() [] minus{x = x - 1;} -> P();
3 #define goal x < 0;
4 #assert P() reaches goal; // Note: goal condition must be a macro</pre>
```

CSP#: Assertions

The following coin exchanging example shows how to minimize the number of coins during reachability search.

```
var x = 0;
var weight = 0;
P() = if (x <= 14) {
    coin1{x = x + 1; weight = weight + 1;} -> P();
    [] coin2{x = x + 2; weight = weight + 1;} -> P();
    [] coin5{x = x + 5; weight = weight + 1;} -> P();
};
#define goal x == 14;
#assert P() reaches goal with min(weight);
```

- Linear Temporal Logic (LTL) is a formalism used for specifying and reasoning about the behavior of systems over time.
- It extends propositional logic by introducing temporal operators that describe how properties of a system evolve over time, making it suitable for reasoning about sequences of states in a system.
- In LTL, time is viewed as a linear sequence of discrete points, and temporal operators allow the expression of future and current behaviors in a system.

Let ϕ and ψ be LTL formulaes.

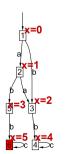
Operator	Usage	Name	Explanation
X	$\mathbf{X} \phi$	Next	ϕ has to hold at the next state
G	G ϕ	Globally	ϕ has to hold on the entire subsequent path
F	F ϕ	Finally	ϕ eventually has to hold somewhere on the subsequent path
U	ψ U ϕ	Until	ϕ holds at the current or a future position, and ψ has to hold
			until that position. At that position ψ does not have to hold
			anymore.
R	ψ R ϕ	Release	ϕ is true until the first position in which ψ is true, or forever
			if such a position does not exist.

- The LTL assertion is true if and only if every execution (trace) of the system satisfies the formula F, where F is an LTL formula whose syntax is defined as the following rules.
 - $F = e \mid prop \mid [] F \mid <> F \mid X F \mid F1 U F2 \mid F1 R F2$
 - Notations
 - e is an event
 - 2 prop is a predefined propositional
 - 1 reads as "always" (or 'G' in CSP#)
 - 4 <> reads as "eventually" (or 'F' in CSP#)
 - X reads as "next"
 - O U reads as "until"
 - R reads as "release"

```
# assert P() |= F;
```



```
1 \text{ var } x = 0:
                                     # #assert P deadlockfree; // Valid
P = [x < 2] a\{x = x + 1\} \rightarrow P
[][x < 4] b \{x = x + 2\} -> P  #assert P = 1t2; // First state, <math>x < 2?
[][x >= 4] c -> P;
                                                          // Yes
5 \# define ge2 x >= 2;
6 #define lt2 x < 2;
                                     6 #assert P |= !c; // Init event is not c?
                                                         // Yes
                                     9 #assert P |= X (a || b);
                                    10 // Next event a or b? Yes!
                                    11
                                    12 #assert P |= [] ge2; // Always x >= 2? No
                                    13 // Counter Example: Initial State (x = 0)
                                    #assert P \mid = <> ge2; // Eventually x >= 2?
                                    16 // Yes. Traces: <a, b>, <a, a>, <b>
```



```
1 #assert P |= [] (1t2 -> X ge2);
2 // It is always true that if the current
     state is x < 2, it implies the next
     state is x \ge 2?
_3 // No. For example, _4, init state _x = 0
     (1t2), next state x = 1(!ge2)
5 #assert P |= [] (1t2 -> X(Xge2));
6 // It is always true that if the current
     state is x < 2, then the next next
     state is x \ge 2?
7 // Yes. Traces: <a. a>, <a. b>, <b>
```

```
1 \text{ var } x = 0;
                                     #assert P |= (lt2 U ge2);
_{2} P = [x < 2] a\{x = x + 1\} \rightarrow P _{2} // Is it always x < 2 until x >= 2? Yes
[][x < 4] b \{x = x + 2\} \rightarrow P 
[][x >= 4] c -> P;
                                    4 #assert P |= (ge2 R le3);
5 \# define ge2 x >= 2;
                                     5 // x <= 3 until the first position
                                     6 // where x >= 2? Yes
6 #define lt2 x < 2;
                                     8 #assert P |= (ge2 R lt2);
                                     9 // x < 2 until the first position
                                     10 // where x >= 2? No
                                     11 // Counter Examples: <a, b>, <b>
```

CSP# Example: Patterson's Algorithm

Patterson's Algorithm is a concurrent programming algorithm for mutual exclusion that allows two or more processes to share a single-use resource without conflict, using only shared memory for communication.

```
bool flag[2] = {false, false};
int turn;
```

```
1 PO:
          flag[0] = true;
                                                      flag[1] = true;
                                            1 P1:
PO_gate: turn = 1;
                                            2 P1_gate: turn = 0;
           while (flag[1] && turn == 1) {
                                                       while (flag[0] && turn == 0) {
              // busy wait
                                                           // busy wait
             critical section
                                                       // critical section
           // end of critical section
                                                       // end of critical section
           flag[0] = false;
                                                      flag[1] = false;
```

CSP# Example: Patterson's Algorithm

```
1 #define N 2:
var pos[N]; // pos[N] is the flag[N]
3 var step[N]; // step[1] is the !turn
4 var counter = 0; // how many are in CS
6 ProcessO() = RepeatO(1); cs.O{counter = counter + 1;} -> reset{pos[0] = 0; counter =
                      counter - 1;} -> Process0();
7 Repeat 0(j) = [j < N] update 0.1 \{ pos[0] = j; \} -> update . 0.2 \{ step[j] = 0; \} -> ([step[j] = 0; ] -> ([step[j] = 0; 
                 ] != 0 || (pos[1] < j)]idle.j -> Repeat0(j + 1))
                  [] [i == N] Skip;
8
10 Process1() = Repeat1(1); cs.1{counter = counter + 1;} -> reset{pos[1] = 0; counter =
                     counter - 1:} -> Process1():
11 Repeat1(j) = [j < N] update.1.1{pos[1] = j;} -> update.1.2{step[j] = 1;} -> ([step[j]
                 ] != 1 || (pos[0] < i)]idle.i -> Repeat1(i + 1))
           [] [i == N] Skip:
12
14 Peterson() = ProcessO() ||| ProcessI();
15 #define goal counter > 1:
16 #assert Peterson() reaches goal; // Should be not valid
17 #assert Peterson() |= <> cs.0: // Should be not valid
18 #assert Peterson() |= [] (update.0.1 -> <> cs.0) // Should be valid
```

CSP# Example: Alternating Bit Protocol

- We are trying to model a simple network protocol that retransmits lost messages between a Sender (A) and Receiver (B).
- A sends a bit to B and message may get lost. Using an internal timer, A should retransmit if there is no ACK when the time is out. A should continue to listen for the correct ACK if it receives a wrong one.
- B will send ACK if and only if it receives a correct bit. We assume the ACK never gets lost. B should ignore the wrong bit received.
- After finishing one bit, A and B should move on to send and ACK the alternating bit.

Note

ifa (cond) { P } else { Q }; performs the condition checking and the first operation of P or Q together.



CSP# Example: Alternating Bit Protocol

```
1 channel c 1: // unreliable channel.
channel d 1; // perfect channel.
3 channel tmr 0; // sender's internal timer.
5 Sender(alterbit) = (c!alterbit -> Skip [] lost -> Skip); tmr!1 -> Wait4Response(
     alterbit);
7 Wait4Response(alterbit) = (d?x -> ifa (x == alterbit) {
8 tmr!0 -> Sender(1 - alterbit)
9 } else { Wait4Response(alterbit) }) // Time not out, wait
10 [] tmr?2 -> Sender(alterbit): // Time is out, retransmit
12 Timer = tmr?1 -> (tmr?0 -> Timer [] tmr!2 -> Timer);
14 Receiver(alterbit) = c?x -> ifa (x == alterbit) {
d!alterbit -> Receiver(1 - alterbit)
16 } else { Receiver(alterbit) }; // Wait to receive
18 ABP = (Sender(0) | | Receiver(0) | | Timer):
```

CSP# Example: Alternating Bit Protocol

```
1 #assert ABP |= [](c!0 -> <>d?0);
```

Is it always true, that when a Sender process writes 0 into the unreliable channel c, a Receiver process will read the value 0 from the perfect channel d?

- Not valid
- Counter Example: $\langle c!0, c?0, d!0, tmr.1, tmr.2, c!0, c?0, tmr.1, tmr.2, c!0, (c!0, c?0, tmr.1, tmr.2, c!0)^* \rangle$

CSP# Example: Shunting Game

Modelling the game board using a 1-D array

```
1 #define M 7;
2 #define N 6:
3 #define o -1:
4 #define a 1;
5 #define w 0:
6 // col number: 0 1 2 3 4 5 6
7 var board[N][M] = [0,0,a,a,0,0,0, // O row number starting from O
                    o.o.a.a.o.o.o. // 1
                     a,a,a,w,a,a,a, // 2
                     a.w.a.a.a.w.a. // 3
                     a, w, a, a, a, w, a, // 4
                     o,o,a,w,o,o,o, // 5
                     o.o.a.a.o.o.o]:// 6
15 // Black Position
16 var r:\{0..N-1\} = 3: // row
var c: \{0..M-1\} = 0; // column
```

CSP# Example: Shunting Game

Modelling the moves

Modelling goal and trouble state.

```
1 #define goal board[2][2] == w && board[2][3] == w && board[3][2] == w && board
        [3][3] == w;
2 #assert Game reaches goal;
3 #define trouble board[0][3] == w;
4 #assert Game reaches trouble;
5 #assert Game |= [](trouble -> ! <> goal); // trouble prevents reaching goal
```

- One of the latest automotive technologies, a push-button keyless system, allows you to start your car's engine without the hassle of key insertion and offers great convenience.
- These systems are designed so it is possible to start the engine without the owner's key-fob and it cannot lock your key fob inside the car because the system will sense it and prevent the user from locking them in.
- However, the keyless system can also suprise you as it may allow you to drive the car without a key-fob, meaning you can drive without physically having the key.
- In this example, we will model such a Keyless System and use assertions to check if one can drive the car without having a key with them.

Constants and variables

```
1 #define N 2: // number of owners
2 #define far 0; // owner is out and far away from the car
3 #define near 1; // owner is close enough to open / lock the car if he has the keyfob
4 #define in 2; // owner is in the car
5 #define off 0; // engine is off
6 #define on 1; // engine is on
7 #define unlock 0; // door is unlocked but closed
8 #define lock 1: // door is locked (and must be closed)
9 #define open 2; // door is open
10 #define incar -1: // kevfob is put inside car
11 #define faralone -2; // keyfob is put outside and far
13 var owner[N]: // owners' position, initially all users are far away from the car
14 var engine = off; // engine status, intially off
15 var door = lock; // door status, initally locked
16 var key = 0; // key fob position, initially with its first owner
17 var moving = 0; // car moving status, 0 for stop and 1 for moving
18 var fuel = 10: // energy costs, say 1 for a short drive and 5 for long driving
```

Owner positions

Key-fob positions

Door Operation

```
door_op(i) =
    [key == i && owner[i] == near && door == lock && moving == 0]
    unlockopen.i{door = open} -> door_op(i)

[] [owner[i] == near && door == unlock && moving == 0] justopen.i{door = open} -> door_op(i)

[] [door != open && owner[i] == in] insideopen.i{door = open} -> door_op(i)

[] [door == open] close.i{door = unlock} -> door_op(i)

[] [door == unlock && owner[i] == in] insidelock.i{door = lock} -> door_op(i)

[] [door == unlock && owner[i] == near && key == i] outsidelock.i{door = lock} -> door_op(i)
```

Motor

```
1 \text{ motor(i)} =
      [owner[i] == in && (key == i || key == incar) && engine == off && fuel != 0]
     turnon.i{engine = on} -> motor(i)
   [] [engine == on && owner[i] == in && moving == 0] startdrive.i{
          moving = 1;
   } -> motor(i)
5
   [] [moving == 1 && fuel != 0] shortdrive.i{
6
          fuel = fuel - 1:
          if (fuel == 0) {engine = off; moving = 0;}
9
     } -> motor(i)
   [] [moving == 1 && fuel > 5] longdrive.i{
10
11
          fuel = fuel - 5:
          if (fuel == 0) { engine = off; moving = 0; }
13
      } -> motor(i)
   [] [engine == on && moving == 1 && owner[i] == in] stop.i{moving = 0;} -> motor(i)
14
   [] [fuel == 0 && engine == off] refill{fuel = 10} -> motor(i)
15
   [] [engine == on && moving == 0 && owner[i] == in] turnoff.i{
16
          engine = off;
     } -> motor(i);
18
```

Reasoning

CSP# Example: Multi-lift System (using C#)

In this example, we are modelling a multiple lift system in a building with multiple floors.

```
1 #define NoOfFloors 2: // floor-0, floor-1
2 #define NoOfLifts 2: // lift-0, lift-1
3 #define NoOfUsers 2: // user-0, user-1
5 // this array models the external requests; extrequestsUP[i] = 1 denotes there is an
       upward floor request at floor-i; 0 for no request;
o var extrequestsUP[NoOfFloors];
7 var extrequestsDOWN[NoOfFloors]:
8 // this array models the internal requests;
9 // intrequests[i][j] = 1 if there is a request for floor-i for lift-i; 0 for no
     request:
var intrequests[NoOfLifts][NoOfFloors]; // [-1, -1]
12 // the level of the lift door opens at. -1 means the door is closed
13 var door = [-1(NoOfLifts)];
```

CSP# Example: Multi-lift System (using C#)

Data Operations to clear internal and external requests when the door of the lift-i is open at each level.

```
door[i] = level;
intrequests[i][level] = 0;
if (direction > 0) {
    extrequestsUP[level] = 0;
} else {
    extrequestsDOWN[level] = 0;
}
```

CSP# Example: Multi-lift System (using C#)

Data Operations (using a function defined in an external C# library) to check whether to continue travelling on the same direction or to change direction.

```
public static bool CheckIfToMove(int level, int direction, int i, int NoOfFloors,
    int[] intrequests, int[] extrequestsUP, int[] extrequestsDOWN) {
    int Counter = level + direction;
    while (Counter >= 0 && Counter < NoOfFloors) {
        if (extrequestsUP[Counter] != 0 || extrequestsDOWN[Counter] != 0 ||
        intrequests[i * NoOfFloors + Counter] != 0) {
            return true;
        } else {
                Counter = Counter + direction;
        }
    }
    return false;
}</pre>
```

CSP# Example: Multi-lift System (using C#) Modelling the Lift

```
1 Lift(i, level, direction) = ifa(intrequests[i][level] != 0 || (direction == 1 &&
      extrequestsUP[level] == 1) || (direction == -1 && extrequestsDOWN[level] == 1)) {
      opendoor.i.level{ *data operations to clear request* } -> close.i.level{door[i]
      = -1:} -> Lift(i, level direction)
3 } else {
      checkIfToMove.i.level -> ifa(call(CheckIfToMove, level, direction, i,
4
      NoOfFloors, intrequests, extrequestsUP, extrequestsDOWN)) {
5
          moving.i.level.direction ->
6
          ifa (level + direction == 0 || level + direction == NoOfFloors - 1) {
              Lift(i, level + direction, -1 * direction)
          } else {
9
              Lift(i, level + direction, direction)
      } else {
13
          ifa ((level == 0 && direction == 1) ||
               (level == NoOfFloors - 1 && direction == -1)) {
              Lift(i, level, direction)
          } else {
16
              changedir.i.level -> Lift(i, level, -1 * direction)
20 }
```

CSP# Example: Multi-lift System (using C#) Modelling the Users

```
User() = []pos:{0..NoOfFloors - 1}@ (ExternalPush(pos); UserWaiting(pos));

// The following models the behaviours of the user pushing external buttons
ExternalPush(pos) = ifa(pos != 0) {pushdown.pos{extrequestsDOWN[pos] = 1;} -> Skip}

else {pushup.pos{extrequestsUP[pos] = 1;} -> Skip}

[] ifa(pos != NoOfFloors - 1) {pushup.pos{extrequestsUP[pos] = 1;} -> Skip}

else {pushdown.pos{extrequestsUP[pos] = 1;} -> Skip};

// The following models the behaviours of the user waiting and entering the lift
UserWaiting(pos) = []i:{0..NoOfLifts - 1}@([door[i] == pos]enter.i ->

([]y:{0..NoOfFloors - 1}@push.y{intrequests[i][y] = 1;} ->

([door[i] == y]exit.i -> User()));
```

CSP# Example: Multi-lift System (using C#) Questioning the System

CSP# Example: 2-phase Commit Protocol

```
1 #define N 2: // number of participants
2 enum {Yes, No, Commit, Abort}; // constants
3 channel vote 0:
4 var hasNo = false;
6 // The following models the coordinator
7 \text{ Coord}() = (|||{N}@ \text{ request -> Skip});
            (|||\{N\}@ \text{ vote?vo}\{if \text{ (vo == No) } \{hasNo = true;}\} \rightarrow Skip);
8
            decide -> (([hasNo == false] (|||{N}@inform.Commit -> Skip);
9
            CoordPhaseTwo(Commit))
10
             [] ([hasNo == true] (|||{N}@inform.Abort -> Skip); CoordPhaseTwo(Abort)));
11
12 CoordPhaseTwo(decC) = |||{N}@acknowledge -> Skip;
14 // The following models a participant
15 Part() = request -> execute -> (vote!Yes -> PhaseTwo() [] vote!No -> PhaseTwo()):
16 PhaseTwo() = inform.Commit -> complete -> result.Commit -> acknowledge -> Skip
17
              [] inform.Abort -> undo -> result.Abort -> acknowledge -> Skip;
19 #alphabet Coord {request, inform.Commit, inform.Abort, acknowledge};
20 #alphabet Part {request, inform.Commit, inform.Abort, acknowledge};
21 System = Coord() || (|||{N}@Part()); // Note: request is a common event between
      Coord and Part and has to be synchronised.
```

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- **5** Timed and Probability CSP

Real-Time System (RTS) Module

- The Real-Time System (RTS) module is an extension of the CSP module with operators which captures quantitative **timing requirements**.
- Let P and Q be processes, while d represents a duration of d time units.

```
1 P = Wait[t] // delay
2 P = P timeout[t] Q // timeout
3 P = P interrupt[t] Q // timed interrupt
4 P = P deadline [t] // deadline
5 P = P within[t] // within
```

Timed Process Definition: Wait

- A wait process Wait[t] delays the system execution for a period of t time units then terminates.
- In the subsequent slides, a set of inference rules using premises and conclusions will be used to define the behaviour of each timed process.
- Recall that each (V, P) is an ordered pair of values and processes.

Definition 1

$$\frac{t \leq d}{(V, Wait[d])} \xrightarrow{t} (V, Wait[d-t])$$

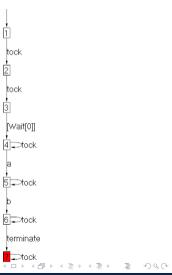
Definition 2

- The starting time of process P is delayed by exactly t time units.
- If the amount of time elapsed is less than the specified duration, then the Wait process will be still active.

Timed Process Definition: Wait (Example)

```
1 P = Wait[2]; (a -> b -> Skip);
```

- The Wait process is active for exactly 2 time units.
- In this example, the first event **a** begins only at least after 2 time units.



Timed Process Definition: Timeout

- The process P timeout[t] Q passes control to process Q if no event has occured in process P before t time units have elapsed.
- For instance if process a -> P timeout [t] Q engages in event a before t time units have elapsed since the process is enabled, then the process is transformed to P.
- If event a has not occured by time t, the process transforms to Q (by silent tau-transition).

Invisible Events

- Let τ (tau) be an invisible event. Example: tau{pv = x}
- { pv = x } (Event with no name is also an invisible event)

Note: Invisible events are not observable.



Timed Process Definition: Timeout

• The timeout constraint is removed if an event in P is engaged before d time units has elapsed.

$$\frac{(V,P) \xrightarrow{\times} (V',P')}{(V,P \text{ timeout}[d] \ Q) \xrightarrow{\times} (V',P')}$$

 If P engages in an invisible event τ, the timeout constraint is not removed, but the process becomes P' timeout[d] Q

$$\frac{(V,P) \xrightarrow{\tau} (V,P')}{(V,P \text{ timeout[d] } Q) \xrightarrow{\tau} (V',P' \text{ timeout[d] } Q)}$$

• If time has not passed d units, the timeout constraint remains.

$$(V, P) \xrightarrow{t} (V, P'), t \le d$$

$$(V, P \text{ timeout[d] } Q) \xrightarrow{t} (V, P' \text{ timeout[d - t] } Q)$$

• If no event has occured by timeout, the process transforms to Q by silent-tau transition.

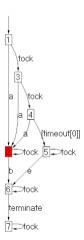
$$(V, P \text{ timeout}[0] \ Q) \xrightarrow{\tau} (V, Q)$$



Timed Process Definition: Timeout (Example)

```
P = (a \rightarrow b \rightarrow Skip) timeout[2] (e \rightarrow Skip);
```

- As long as process P engages event a within 2 time units and before timeout[0] is engaged, the timeout constrained is removed and (e -> Skip) will never be engaged.
- (e -> Skip) can only be engaged after timeout[0] is engaged.



Timed Process Definition: Timed Interrupt

- Process P interrupt[t] Q behaves as P until t time unit elapse and then switches to Q
- For example process (a -> b -> c -> ...) interrupt[t] Q may engage in events a, b, c, ... as long as t time units has not elapsed.
- Once t time units have elapse, then the process transforms to Q by silent-tau transition.

Definition 1

$$\frac{(V,P) \xrightarrow{\times} (V',P')}{(V,P \text{ interrupt[d] } Q) \xrightarrow{\times} (V',P' \text{ interrupt[d] } Q)}$$

Definition 2

$$(V, P) \xrightarrow{t} (V', P'), t \leq d$$

$$(V, P \text{ interrupt[d] } Q) \xrightarrow{t} (V', P' \text{ interrupt[d - t] } Q)$$

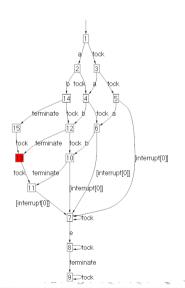
Definition 3

 $\overline{(V,P \text{ interrupt}[0] } Q) \xrightarrow{\tau} (V',Q)$



Timed Process Definition: Timed Interrupt (Example)

- Process P can engage in the events in (a -> b -> Skip) until 2 time unit has elapsed.
- After 2 time units has elapsed, Process P can only engage in events in (e -> Skip).



Timed Process Definition: Deadline

Process P deadline[t] is constrained to terminate within t time units.

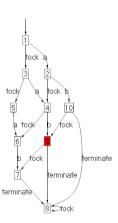
Definition 1

$$\frac{(V,P) \xrightarrow{x} (V',P')}{(V,P \text{ deadline[d]}) \xrightarrow{x} (V',P' \text{ deadline[d]})}$$

Definition 2

$$\frac{(V,P) \xrightarrow{t} (V',P'), t \leq d}{(V,P \text{ deadline[d]}) \xrightarrow{t} (V',P' \text{ deadline[d - t]})}$$

Definition 3

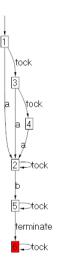


 $P = (a \rightarrow b \rightarrow Skip) deadline[2];$

Timed Process Definition: Within

- The within operator forces the process to make an observable move within the given time frame.
- For example P within[t] says the first event of P must engage within t time units.
- In the example below, event a has to be engaged by latest t = 2 which is at **State 4**.

```
P = (a \rightarrow b \rightarrow Skip) within[2];
```



CSP# Example: Fischer's Protocol

Mutual exclusion in Fischer's Protocol is guaranteed by carefully placing bounds on the execution times of the instructions, leading to a protocol which is very simple, and relies heavily on time aspects.

```
1 #define N 2;
2 #define Delta 3;
3 #define Epsilon 4;
4 #define Idle -1;
5 \text{ var } x = Idle;
6 var counter;
8 P(i) = ifb(x == Idle) {
      ((update.i{x = i} -> Wait[Epsilon]) within[Delta]);
      ([x == i](cs.i{counter++} -> exit.i{counter--; x=Idle} -> P(i))
      [] [x != i]P(i))
11
12 };
13
FischersProtocol = | | | i : \{0..N-1\} @P(i) :
```

CSP# Example: Railway Crossing System

Control trains passing a critical point (a bridge)

```
#import "PAT.Lib.Queue";
2 #define N 2:
3 channel appr 0;
4 channel go 0;
5 channel leave 0:
6 channel stop 0;
7 var < Queue > queue;
9 Train(i) = appr!i -> ((stop?i -> StopS(i) within[10]) timeout[10] Cross(i));
10 Cross(i) = Wait[4]; (leave!i -> Train(i)) within[11];
11 StopS(i) = go?i -> Start(i);
12 Start(i) = Wait[10]: (leave!i -> Train(i)) within[10]:
13 Gate = if (queue.Count() ==0) {
      appr?i -> atomic{tau{queue.Enqueue(i)} -> Skip}; Occ
14
15 } else {
16
      (go!queue.First() -> Occ) within[10]
17 };
19 Occ = (leave?[i == queue.First()]i -> atomic{tau{queue.Dequeue()} -> Skip}; Gate) []
       (appr?i -> atomic{tau{queue.Enqueue(i)} -> stop!queue.Last() -> Skip}; Occ);
20 System = (||| x: \{0...N-1\} @Train(x)) ||| Gate;
            Kevin Toh
                                 CS4211: Formal Methods for Software Engineering
                                                                                          150 / 162
```

CSP# Example: Light Control System (Part 1 of 2)

```
1 var dim : {0..100}:
var on = false:
3 channel button 0:
4 channel dimmer 0:
5 channel motion 0:
7 TurningOn = turnOn{ on = true; dim = 100;} -> Skip;
8 TurningOff = turnOff{ on = false; dim = 0; } -> Skip;
10 ButtonPushing = button?1 -> atomic{if (dim > 0) { TurningOff } else { TurningOn }};
11 DimChange = dimmer?n -> atomic{setdim{dim = n} -> Skip};
12 ControlledLight = (ButtonPushing [] DimChange); ControlledLight;
14 // The motion detector
15 NoUser = move -> motion!1 -> User [] nomove -> Wait[1]; NoUser;
16 User = nomove -> motion!O -> NoUser [] move -> Wait[1]: User:
17 MotionDetector = NoUser:
19 // Continued on the next slide...
```

CSP# Example: Light Control System (Part 2 of 2)

```
// The room controller
Ready = motion?1 -> button!1 -> On;
Regular = adjust -> dimmer!50 -> Regular;
On = Regular interrupt motion?0 -> OnAgain;
OnAgain = (motion?1 -> On) timeout[20] Off;
Off = button!1 ->> Ready; // Note: ->> is shortcut for atomic
Controller = Ready;
System = MotionDetector ||| ControlledLight ||| Controller:
```

PCSP Module: Probability Processes

- The PCSP module adds probability processes to existing process definitions in the CSP# module.
- **Probability processes** are a special kind of process with probabilistic characteristic defined using the keyword pcase.
- It is a compositional process made up of probabilistic branches.

- prob1 and prob2 are floating point probability values.
- default = 1 prob1 prob2 ...

- PAT will add up and normalize the weights.
- For example, the probability of P to Q1 is $\frac{weight1}{weight1+weight2+\cdots+weightn}$

Probabilty Processes Assertion

```
#assert P reaches cond with prob/pmin/pmax
```

This assertion asks the (min/max/both) probability that the process P() can reach a state at which some given condition is satisifed.

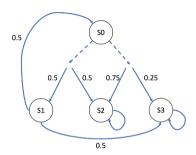
Keyword: prob

The keyword prob provides both the minimum and maximum probability a process P() can reach a certain state. It provides a range of probabilities.

PCSP Example: Simple pcase

```
var current = 0:
2 aSystem = State0;
4 State0 = pcase{
     [0.5] : e05{current = 1} -> State1
default : e05{current = 2} -> State2
7 } [] pcase{
[0.25] : e025\{current = 3\} \rightarrow State3
default : e075{current = 2} -> State2
10 };
12 State1 = pcase{
[0.5] : e05{current = 0} -> State0
     default : e05{current = 3} -> State3
14
15 }
17 State2 = e -> State2:
18 State3 = e -> State3:
20 #define predicate current == 2;
^{21} #assert aSystem reaches predicate with pmax; // 0.75
22 #assert aSystem reaches predicate with pmin; // 0.67
```

PCSP Example: Calculating Max Reachability for Simple pcase



Let x_i be the max reachability from S_i to S_2 :

•
$$x_0 = max(0.5x_1 + 0.5x_2, 0.75x_2 + 0.25x_3)$$

$$x_1 = 0.5x_0 + 0.5x_3$$

•
$$x_2 = 1$$

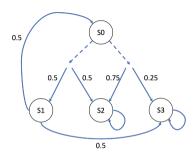
•
$$x_2 = 0$$

- x_0, x_1 dependent on other reachability values.
- Initially, assume $x_0, x_1 = 0$.
- When calculating each current iteration, use the previous iteration's x_0 value when calculating x_1 . That is why in the table below, we start from iteration 0.
- Stop iterating only when the values are stabilized.

Iteration	<i>x</i> ₀	<i>x</i> ₁
0	0	0
1	0.75	0
2	0.75	0.375
3	0.75	0.375



PCSP Example: Calculating Min Reachability for Simple pcase



Let x_i be the min reachability from S_i to S_2 :

$$\bullet \ x_0 = min(0.5x_1 + 0.5x_2, 0.75x_2 + 0.25x_3)$$

$$x_1 = 0.5x_0 + 0.5x_3$$

- $x_2 = 1$
- $x_2 = 0$

- x_0, x_1 dependent on other reachability values.
- Initially, assume $x_0, x_1 = 1$.
- Use the previous iteration's x_0 value when calculating x_1 .
- Stop iterating only when the values are stabilized.

Iteration	<i>x</i> ₀	<i>x</i> ₁
0	1	1
1	0.75	0.5
2	0.75	0.375
3	0.6875	0.375
4	0.6875	0.3438
5	0.6719	0.3438
	0.6667	0.3333

PCSP Example: Monty Hall

```
1 enum{Door1, Door2, Door3};;
2 \text{ var car} = -1:
3 \text{ var guess} = -1:
4 \text{ var goat} = -1;
5 var final = false:
7 #define goal guess == car && final;
9 PlaceCar = []i:{Door1, Door2, Door3}@ placecar.i{car = i} -> Skip;
10 Goat = []i:{Door1, Door2, Door3}@
      ifb (i != car && i != guess) { hostopen.i{goat = i} -> Skip };
11
TakeOffer = []i:{Door1, Door2, Door3}@
      ifb (i != guess && i != goat) { changeguess {guess = i; final = true} -> Stop };
14
15 NotTakeOffer = keepguess{final = true} -> Stop:
17 Sys_Take_Offer = PlaceCar; Guest; Goat; TakeOffer;
18 #assert Sys_Take_Offer reaches goal with prob; // Max Prob = 2/3
20 Sys_Not_Take_Offer = PlaceCar; Guest; Goat; NotTakeOffer;
21 #assert Sys_Not_Take_Offer reaches goal with prob; // Max Prob = 1/3
```

PCSP Example: Monty Hall (Explanation)

• What happens if we changed **line 14** to

```
if (i != car && i != guess) { hostopen.i{goat = i} -> Skip };
```

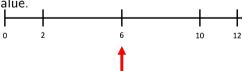
- goat will remain as −1
- In the original code, ifb was used. The process will wait at the ifb block when the condition is not true.
- However for if, the process will terminate if the condition is not true.
- Alternatively, we can change ifb to a guard condition.
- The code in the previous slide can deadlock.
 - The TakeOffer and NotTakeOffer processes have a STOP process inside its definition.

PCSP Example: Consensus

```
1 #define N 2:
2 #define K 2;
3 #define range 12; // Range
4 #define counter_init 6; // Middle
5 #define left 2; // Left Target
6 #define right 10; // Right Target
7 var counter : {0..range} = counter_init; // shared coin
8 var Pcounter; // record the process number
9 var coin0counter = N; // number of coints which are 0;
10 var coin1counter: // number of coints which are 1;
11 // local variable: 0 - flip, 1 - write, 2 - check, 3 - finished
12 // processij means this process's coin is i and its local variable is j
process00 = pcase{ [0.5] : process01}
                      default : tau {coin0counter--; coin1counter++;} -> process11 }:
15 process01 = [counter > 0] tau {counter--;} -> process02;
process02 = [(counter <= left)] tau {Pcounter++;} -> process03
[] [(counter >= right)] {Pcounter++; coin0counter--; coin1counter++;} -> process13
[] [(counter > left) && (counter < right)] process00;</pre>
19 process03 = [Pcounter==N] done -> process03:
20 process13 = [Pcounter==N] done -> process13;
System = ||\{N\}@| process00;
```

PCSP Example: Consensus (Explanation)

- ullet In this example, N processes come to a consensus by deciding whether the agreed value should be 0 or 1.
- In process00, there is a 50% chance that the process gets coin 0 in the coin flip which goes to process01. In the remaining 50% chance, the process gets coin 1 in the coin flip and proceeds to process11.
- If a process flips coin 0, the counter's value is reduced by 1.
- If a process flips coin 1, the counter's value is increased by 1.
- If the counter's value is below the left boundary of 2, then the agreed value is 0.
- If the counter's value is below the above boundary of 10, then the agreed value is 1.
- Note that in this system, all *N* processes are **executing concurrently** without synchronisation. Hence, it is possible that the majority decision on the coin value, may differ from the counter value.



PCSP Example: Consensus (Simplified)

```
1 #define N 2:
2 #define K 2;
3 #define range 12;
4 #define counter init 6:
5 #define left 2;
6 #define right 6;
7 var counter: {0..range} = counter_init;
8 Var Pcounter:
10 process00 = pcase{
[0.5] : tau{counter--;} -> process02
   default: tau{counter++;} -> process02
13 }:
process02 = [(counter <= left)] tau{Pcounter++} -> process03;
             [] [(counter >= right)] tau{Pcounter++} -> process13;
             [] [(counter > left) && (counter < right)] process00;
17 process03 = [Pcounter == N] done -> process03;
18 Process13 = [Pcounter == N] done -> process13;
19 System = |||{N}@ process00;
```