CS M51A/ EE M16 LOGIC DESIGN OF DIGITAL SYSTEMS

- INSTRUCTOR: Prof. Miloš Ercegovac
- TAs: Yang Lu, Teng Xu and Hyun Kim
- TEXTBOOK: *Introduction to Digital Systems* by Ercegovac, Lang and Moreno, Wiley 1999.
- Digital version of the textbook available at CourseSmart site (http://www.coursesmart.com/9780471527992)
- GRADING
  - Homeworks: 10%
  - Quizzes (4): 20% (in recitation)
  - Midterm: 30% (in class, Nov. 4) Final: 40%

COMMENTS ON COURSE ORGANIZATION

- PLEASE READ THE TEXTBOOK BEFORE LECTURES and THINK OF QUESTIONS
- LECTURES FOLLOW THE TEXTBOOK
- QUESTIONS DURING LECTURES ARE WELCOME; characteristic problems solved and discussed during lectures
- LECTURE VIEWGRAPHS POSTED ON THE CLASS WEB PAGE
- COME TO OFFICE HOURS - BEST WITH SPECIFIC QUESTIONS

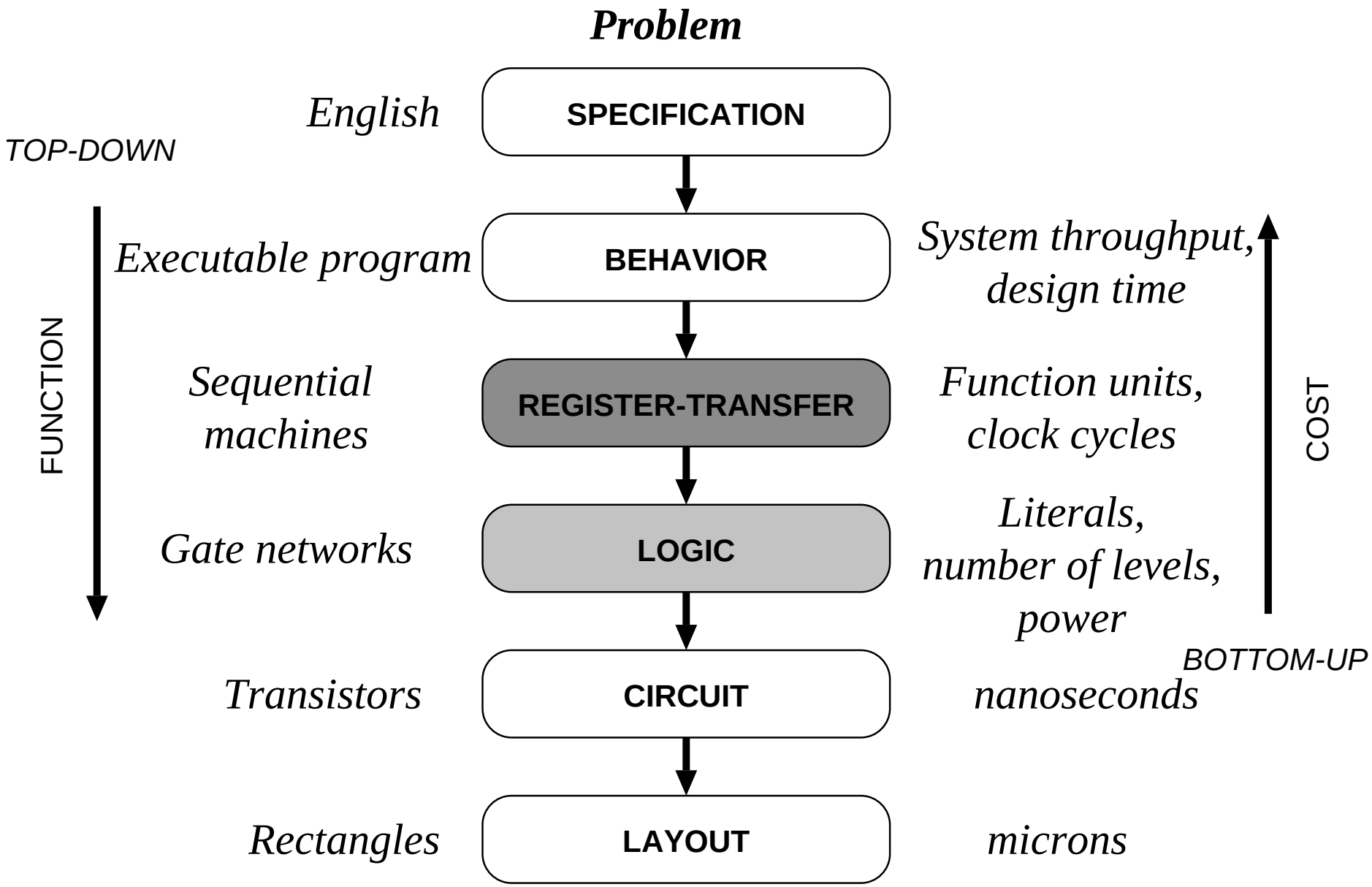
- DISCUSSIONS: HOMEWORKS, PROBLEM SOLVING, LogiSim DESIGNS, Q & A, QUIZZES

- MIDTERM and FINAL EXAMS (closed book and notes; 2 cheat sheets OK)

- CHECK REGULARLY THE CLASS WEB PAGE Lecture viewgraphs, homeworks/solutions, solutions to all odd-numbered exercises from the book, sample exams, announcements, and more ...

- VHDL PARTS WILL NOT BE COVERED; INSTEAD WE WILL USE A SIMPLE DESIGN ENTRY AND SIMULATION TOOL LogiSim (free download)

- SOLUTIONS TO ODD-NUMBERED EXERCISES POSTED ON THE CLASS WEB PAGE. WORK IN GROUPS ON THESE PROBLEMS.
- WORK INDEPENDENTLY ON THE GRADED MATERIAL: YOUR HIGHEST ETHICAL CONDUCT IS EXPECTED.



(Adapted from "Modern VLSI Design" by Wayne Wolf Prentice-Hall 1998)

Fabrication, testing and packaging --> IC s
-> System implementation & use

Figure 1: Big picture.