CSMSIA FI4 MDE

REGISTER FILE: STORES OPERANDS & TEMPORX RESULTS FASTER ACCESS THAN TO CACHE INPUTS: JEAD AND WRITE ADDRESSES RELATIVELY SMALL SIZE · READ 4UD CLK MULTIPLE READS XND WRITES ON DIFF. REGISTER WRITE OPS · DATA IN OUTPUTS : DATA OUTPUTS (LEFT, DIGHT) RAL LDO LDI LD2 1D3 RAR DIN PARAMETERS: 2" REGISTERS X 16 12-BIT ADDRESSES RO LD-LDO M-BIT PRECISION + 16 716 EYAMPLE: 12 = 2, 4 REGISTERS RI CD-LD1 n = 16+ 16 2 READS, IWRITE 1-6 MUX MUX R2 10-102 (DOUTL, DOUTR, DIN) + 16 RAL -LEFTREAD ADDRESS 716 RAR - RIGHT READ 73 LD -LD3 ADD ZESS 703 116 WA - WRITE ADR, W - WRITE OP +16 CLK DourL DOUTR