CSM51A F14 MDE

REGISTER FILE: STORES OPERANDS & TEMPORX RESULTS

FASTER ACCESS THAN TO CACHE

INPUTS: • READ AND
WRITE ADDRESSES RELATIVELY SMALL SIZE

CLK • READ AND
WRITE OPS MULTIPLE READS AND WRITES ON DIFF. REGISTER

• DATA IN

OUTPUTS: DATA OUTPUTS
(LEFT, RIGHT)

PARAMETERS: 2^k REGISTERS

k-BIT ADDRESSES

n-BIT PRECISION

EXAMPLE: $k=2$, 4 REGISTERS
$n=16$

2 READS, 1 WRITE

$(D_{OUT}L, D_{OUT}R, D_{IN})$

RAL — LEFT READ
ADDRESS
RAR — RIGHT READ
ADDRESS

WA — WRITE ADR,

W — WRITE OP