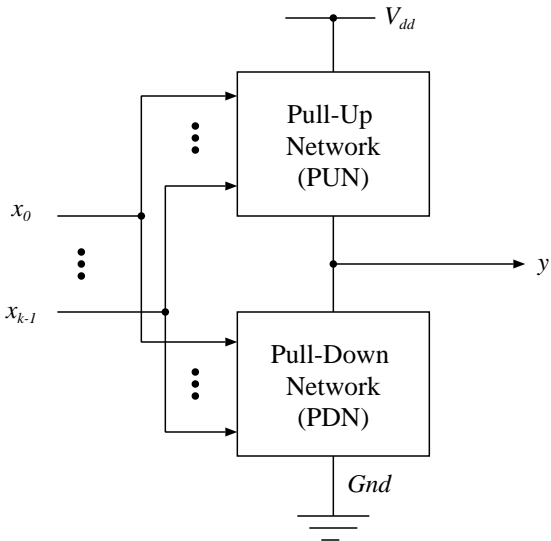
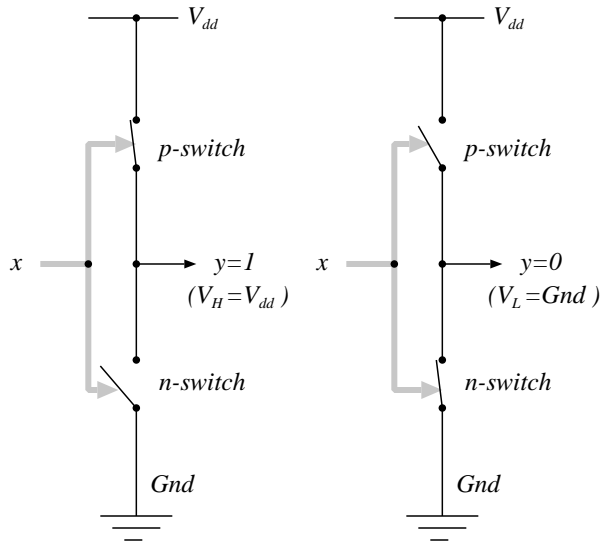


COMPLEMENTARY LOGIC



STABLE SITUATIONS



- no static current in stable state
- \Rightarrow low power dissipation
- faster transitions