

Bytecode: VI

Handlers Set 0:

Handlers Set 1:

00 03:	load_r 03
10 :	add32
93 :	switch_HAT
7a 05:	load_r 05
17 :	add32
52 :	switch_HAT
	...

402300:	movzx eax, byte[vpc]
	add vpc, 1
	push[VMcontext+eax*4]
	jmp VMloop
...	
4023e0:	mov eax, [esp]
	add esp, 4
	add dword[esp], eax
	jmp VMloop
...	
4024a6:	add HAT, 250h
	jmp VMloop

4026a4:	pop eax
	add dword[esp], eax
	jmp VMloop
...	
4026f8:	add HAT, 250h
	mov ecx, ecx
	jmp VMloop
...	
402740:	push [vpc]
	pop eax
	add vpc, 1
	push[VMcontext+eax*4]
	jmp VMloop

VMloop:

Handler Address Tables:

movzx eax, byte[vpc]
add vpc, 1
jmp HAT[eax]

HAT0:	402300	...	4023e0	...	4024a6	...
	HAT0[00]		HAT0[10]		HAT0[93]	
		HAT1[17]		HAT1[52]		HAT1[7a]
HAT1:	...	4026a4	...	4026f8	...	402740