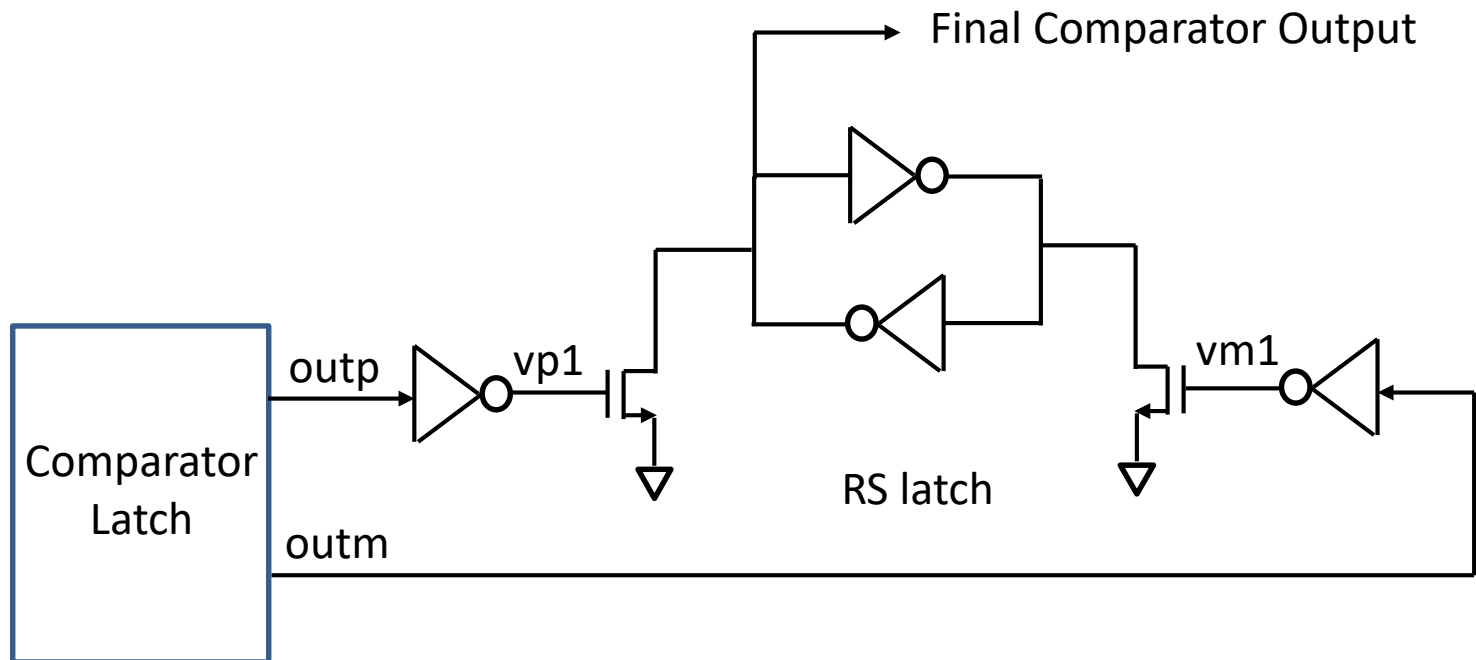# HW4 Comparator RS latch implementation

During ph2, comparator latch makes a decision.
During ph1, both comparator latch outputs (outp, outm) reset to VDD.
Then both vp1 & vm1 are low, thereby retaining previous outputs.