VERIFICATION OF DIGITAL CIRCUITS
Mikroelektronika w Technice i Medycynie

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Test cases for DUT - MULT

A. **Operations and sequences**

1. Valid data are given at the input - arg_a_parity, arg_b_parity relative to arg_a, arg_b.
2. Invalid data are given at the input - arg_a_parity, arg_b_parity are invalid for arg_a and arg_b.
3. Invalid data **a**, valid data **b** are given at the input - arg_b_parity is invalid for arg_b, arg_a_parity is valid for arg_a.
4. Invalid data **b**, valid data **a** are given at the input - arg_a_parity is invalid for arg_a, arg_b_parity is valid for arg_b.

B. **Check corners values**

1. Simulate maximum values for signed data.
2. Simulate minimum values for signed data.
3. Simulate one value for signed data (1).
4. Simulate minus one value for signed data (-1).
5. Simulate zeros values.