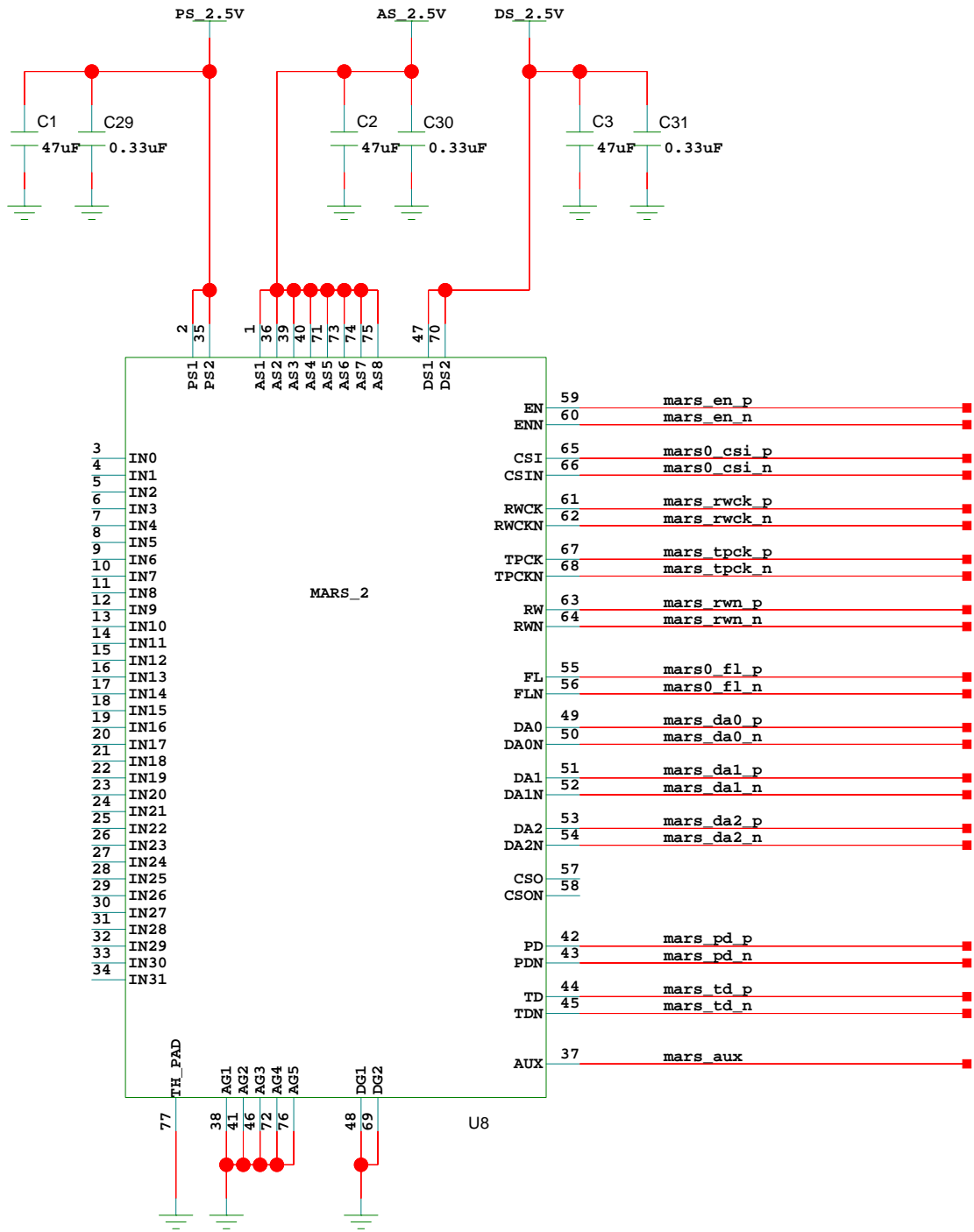
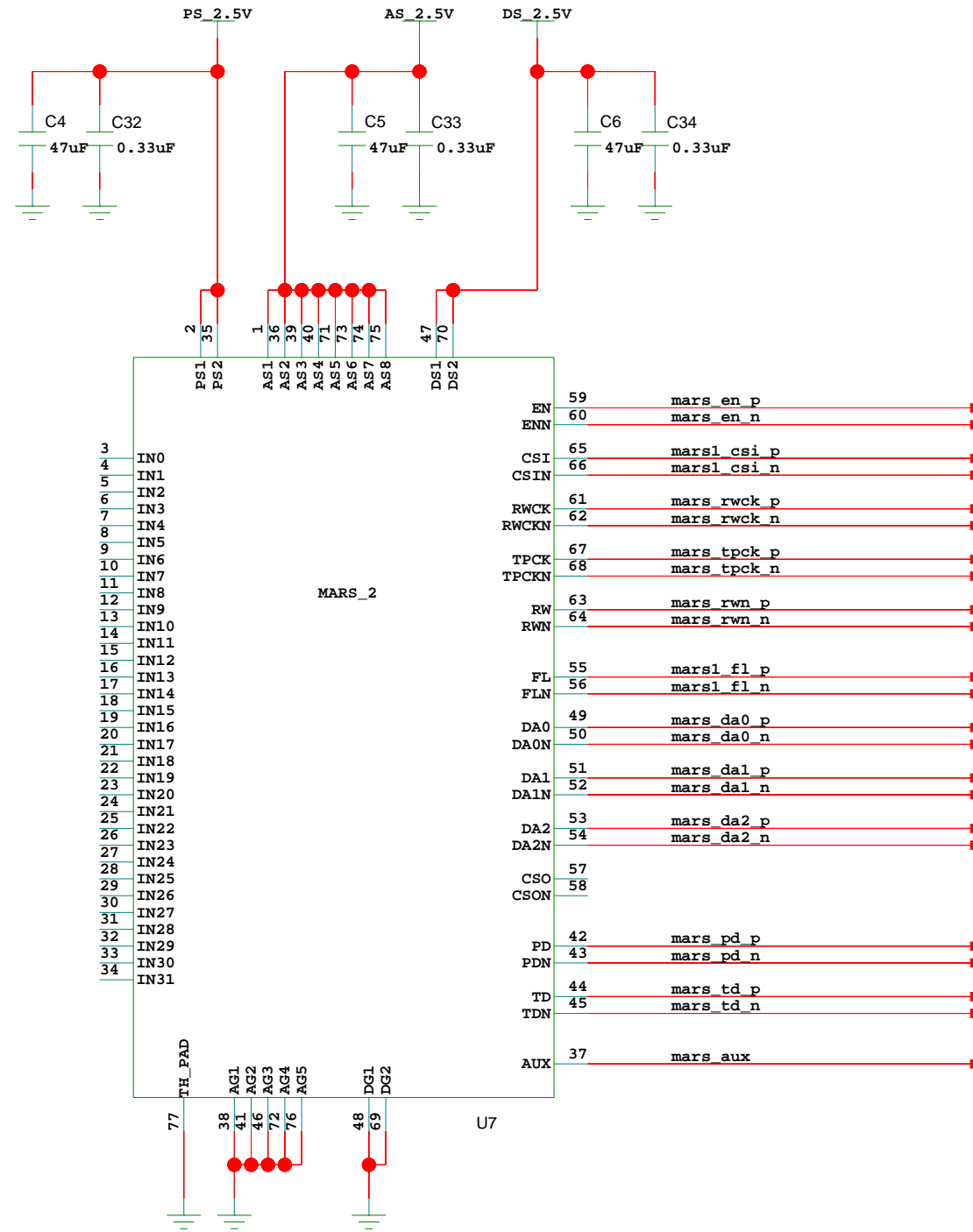


MARS ASIC 0

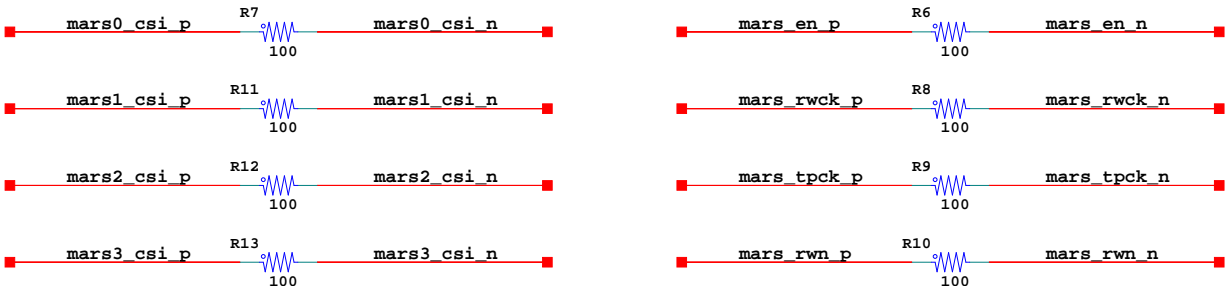
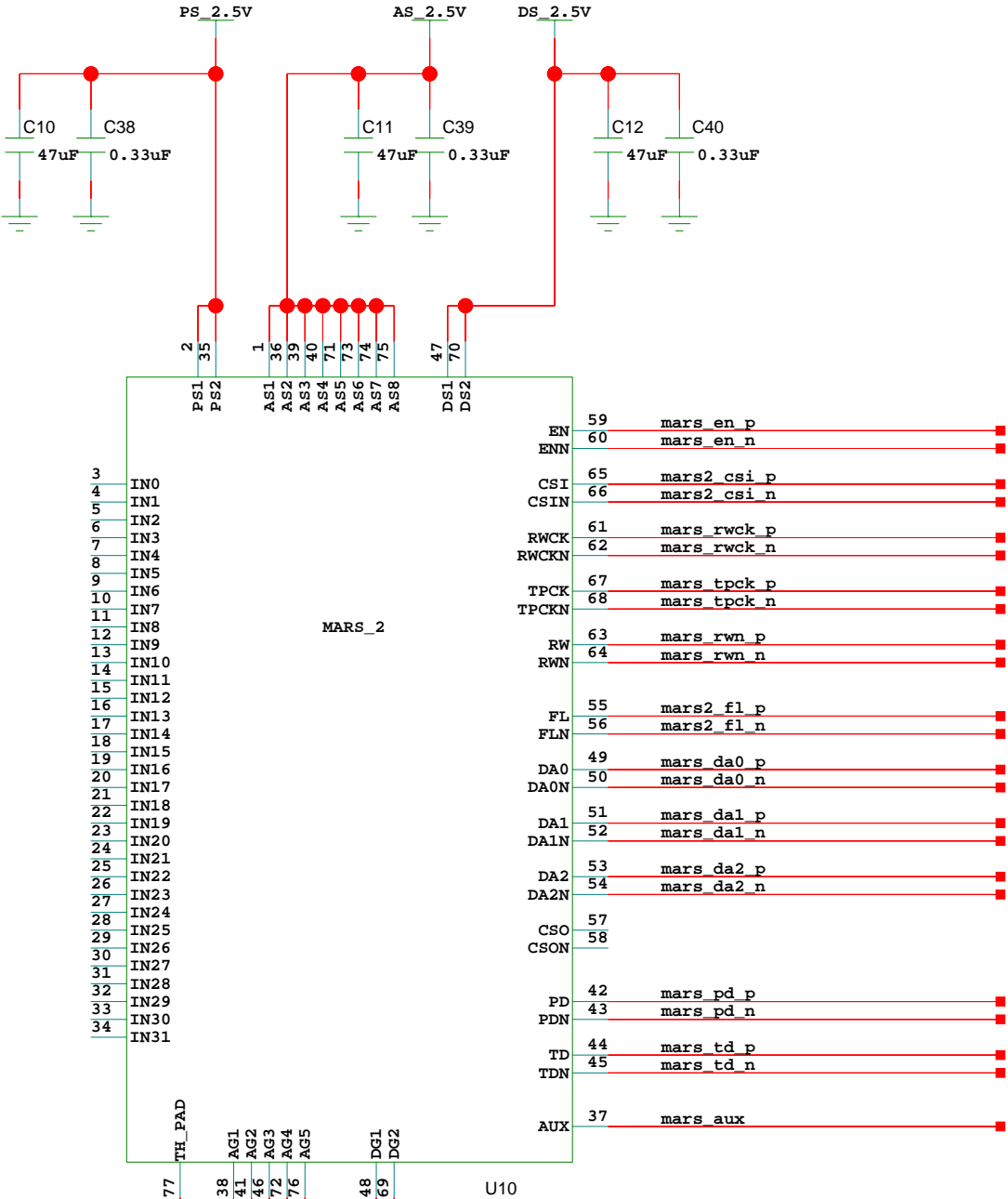


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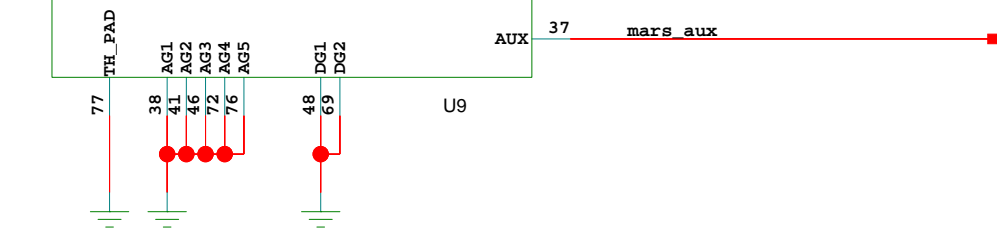
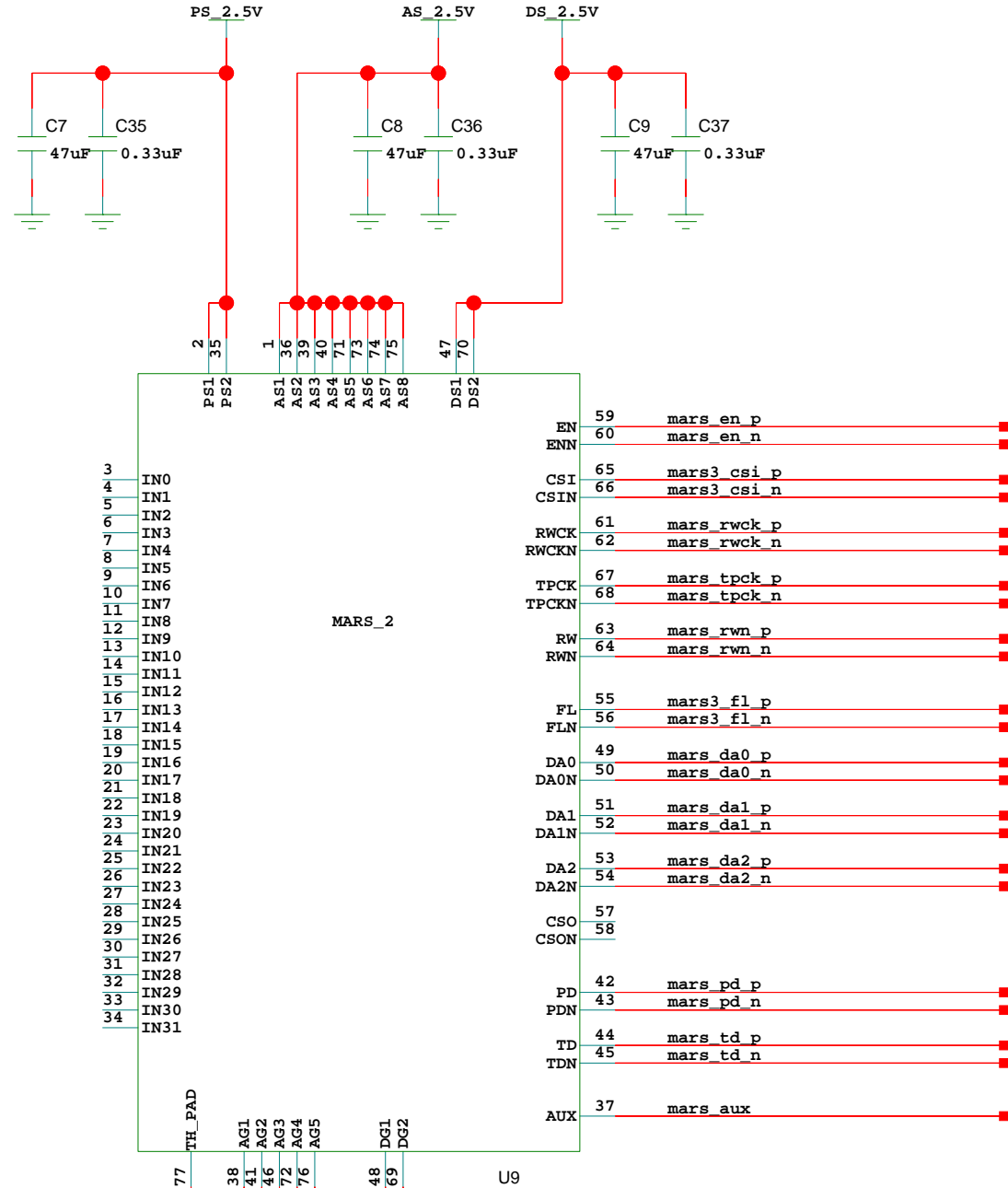


Title:	
Silicon Strip Detector Board	
Date: 1-5-17	Ver: -
Sheet Size: B	Rev: -
Sheet 1 of 6	Drawn By J. Mead

MARS ASIC 2

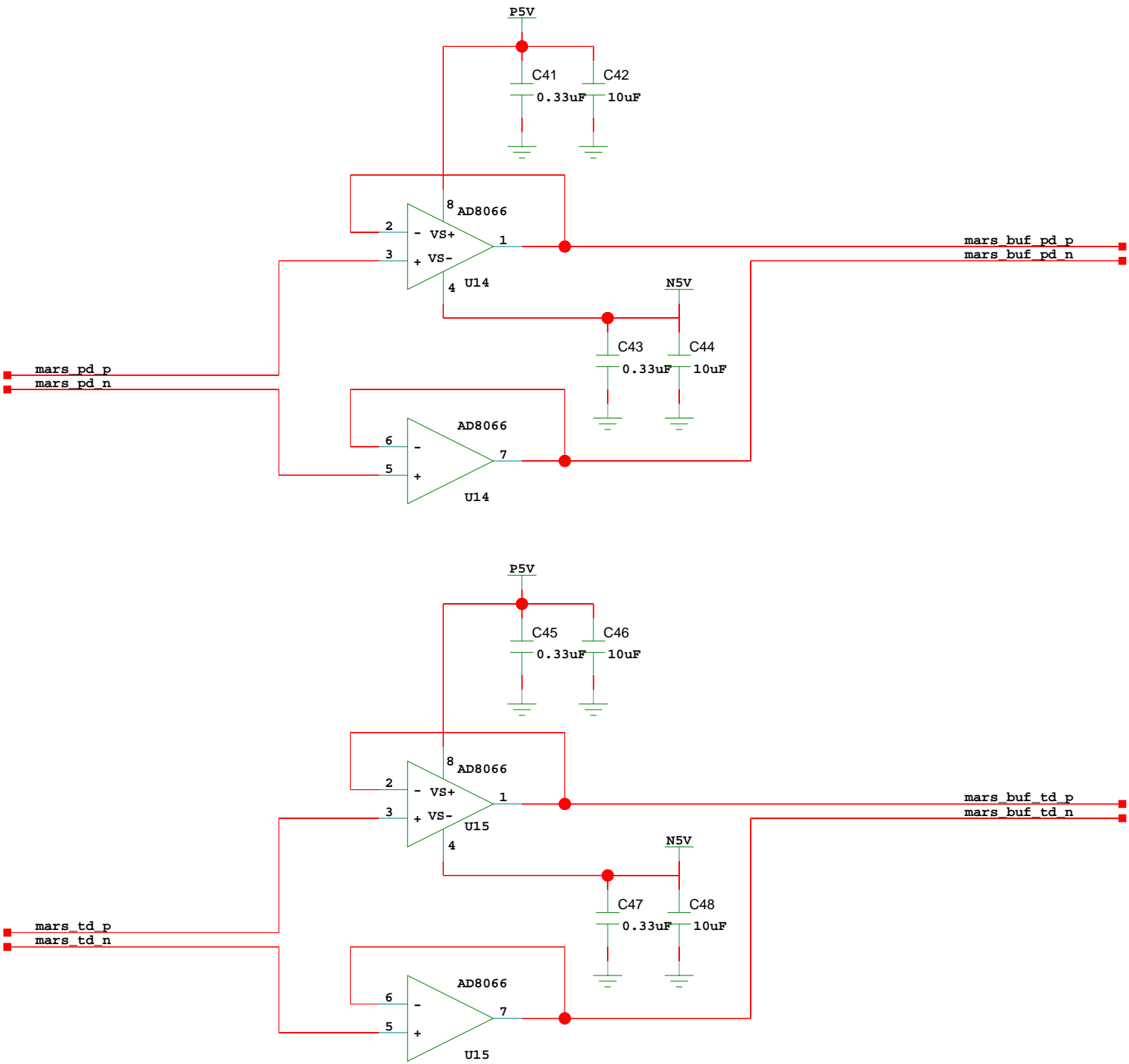


MARS ASIC 3



Title:		Silicon Strip Detector Board	
Date:	1-5-17	Ver:	
Sheet Size:	B	Rev:	-
Sheet	2	of	6
Drawn By		J. Mead	

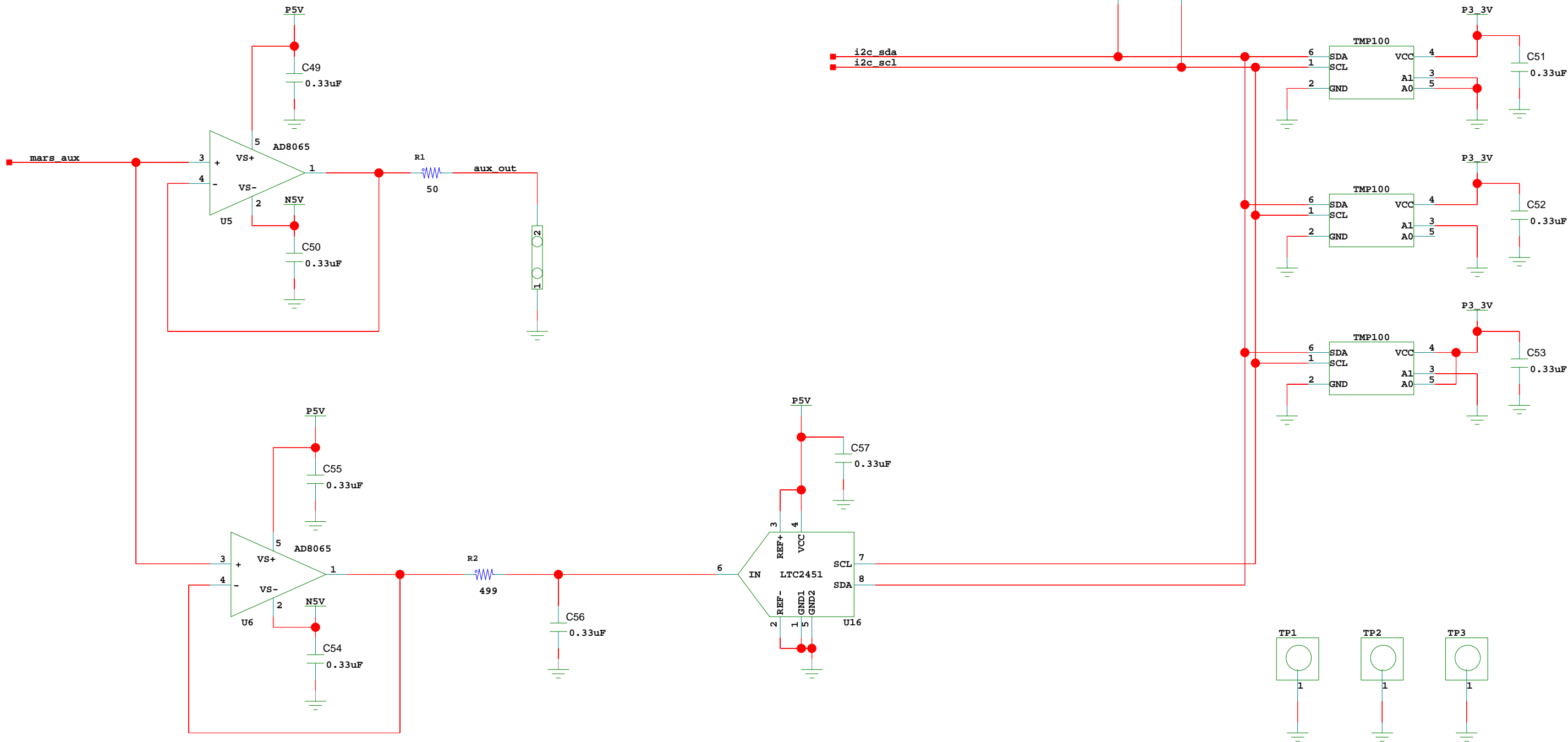
MARS0 PD/TD JFET Buffers



Title:		Silicon Strip Detector Board	
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Sheet	3	of	6
Drawn By		J. Mead	

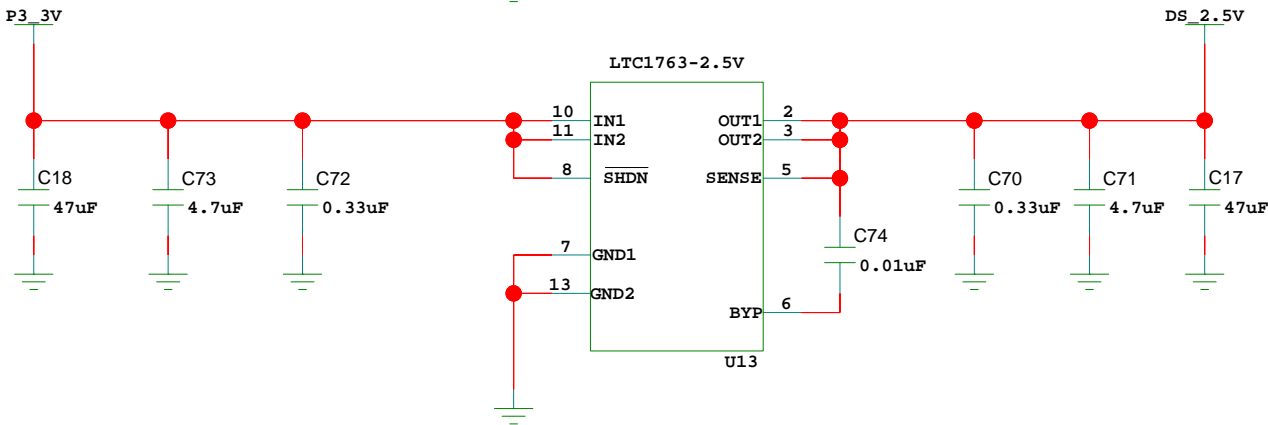
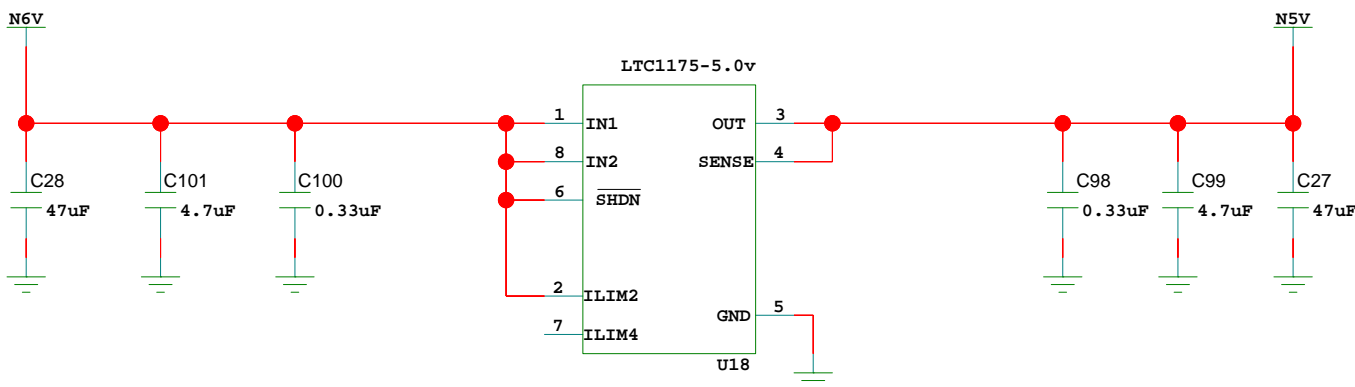
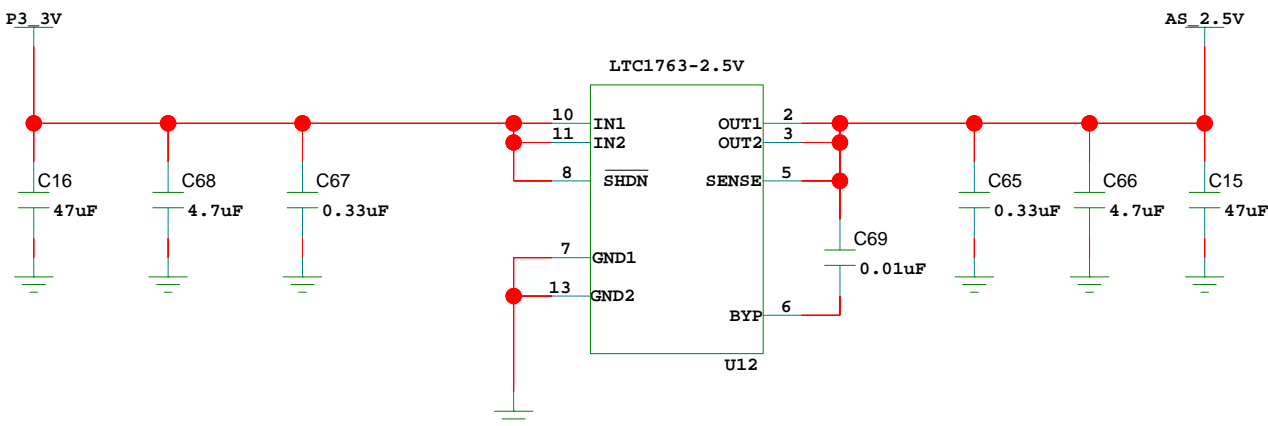
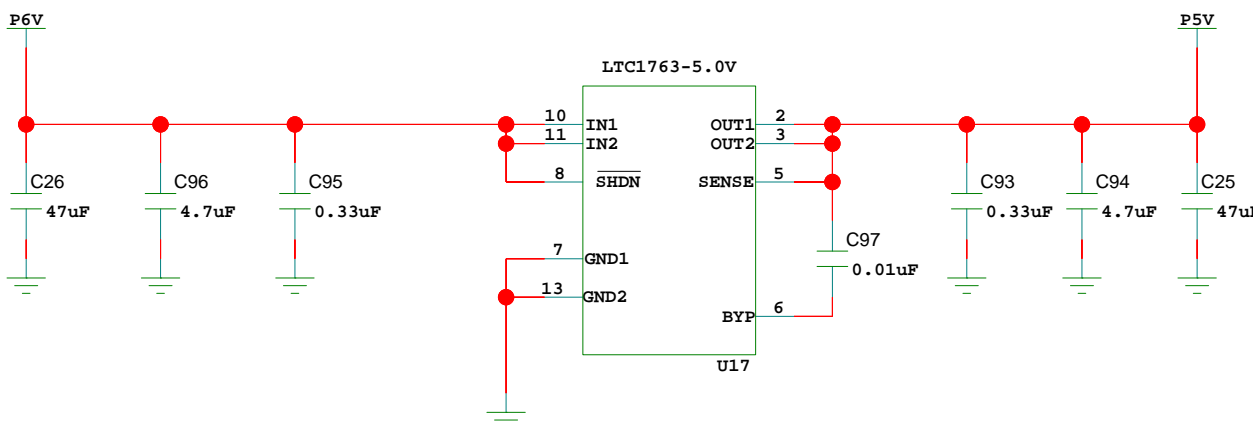
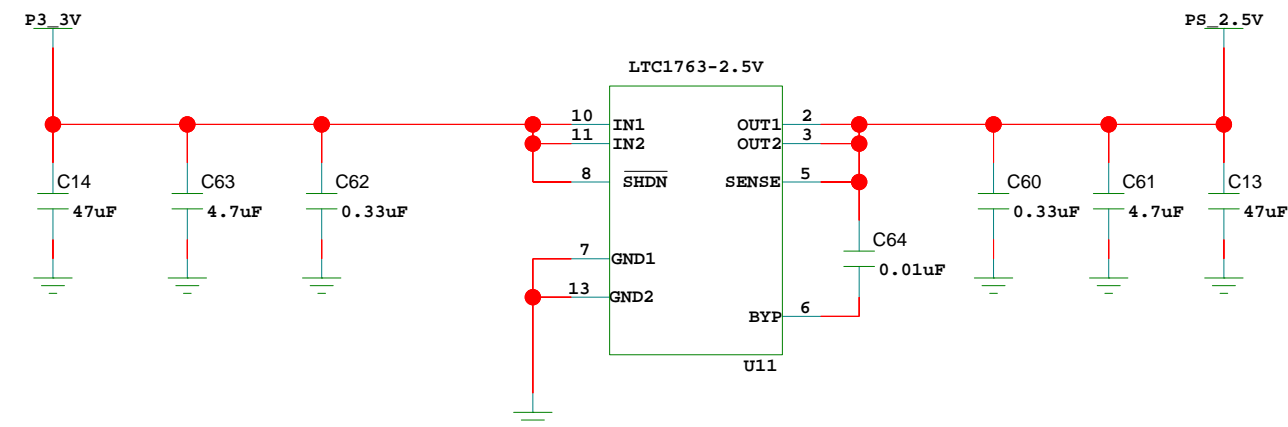
MARS Aux Buffer

Temperature Sensors



Title:		Silicon Strip Detector Board	
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Drawn By		J. Mead	

Regulators

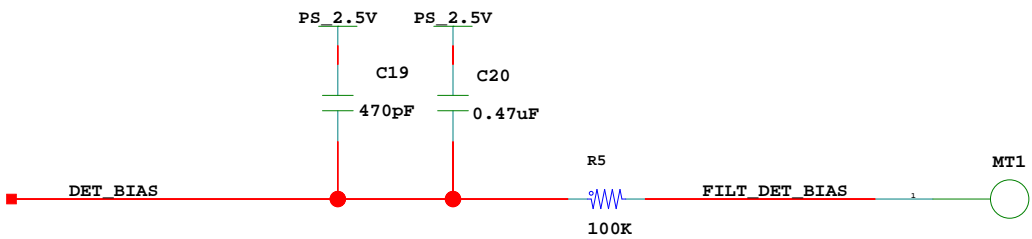
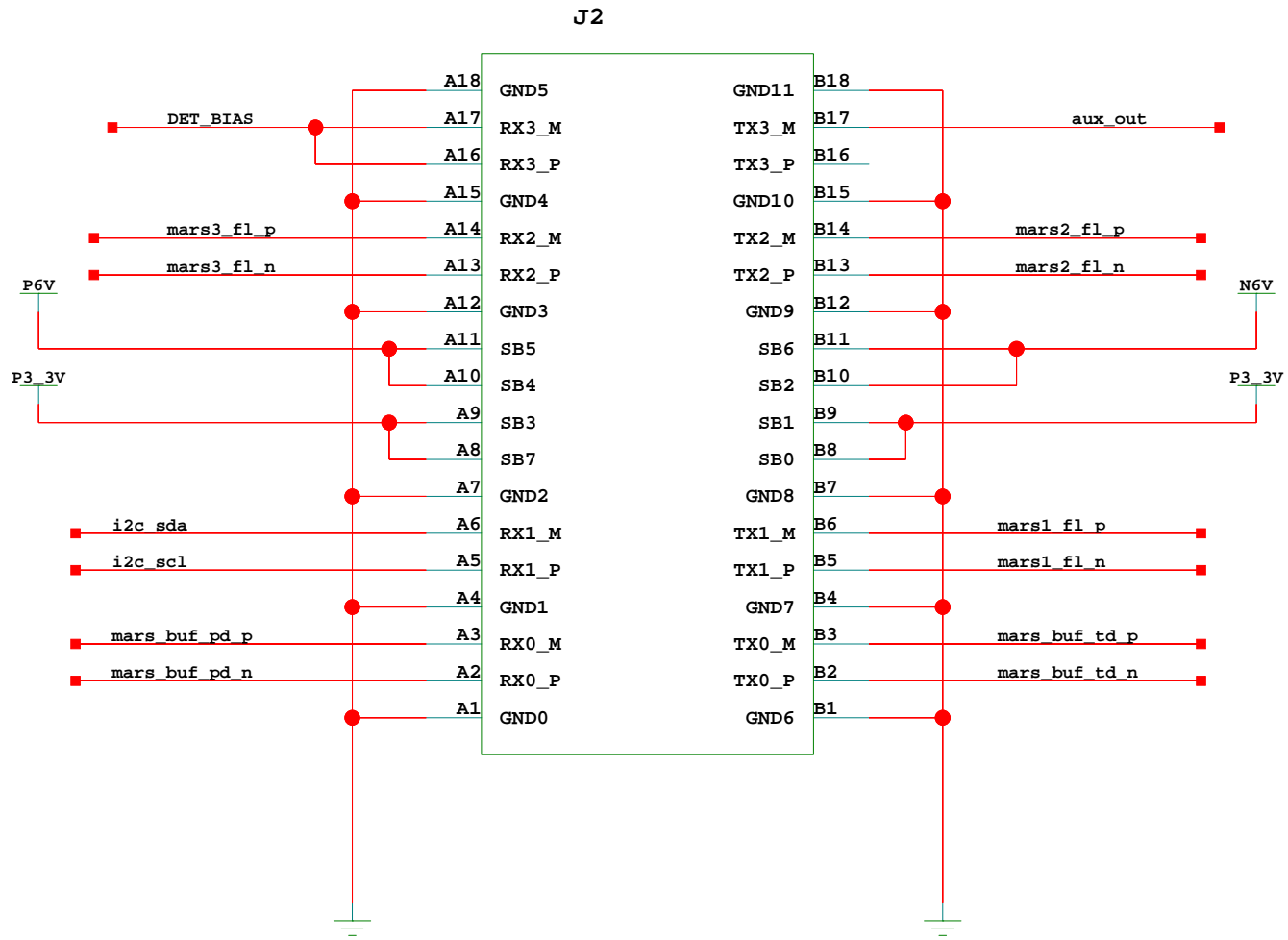
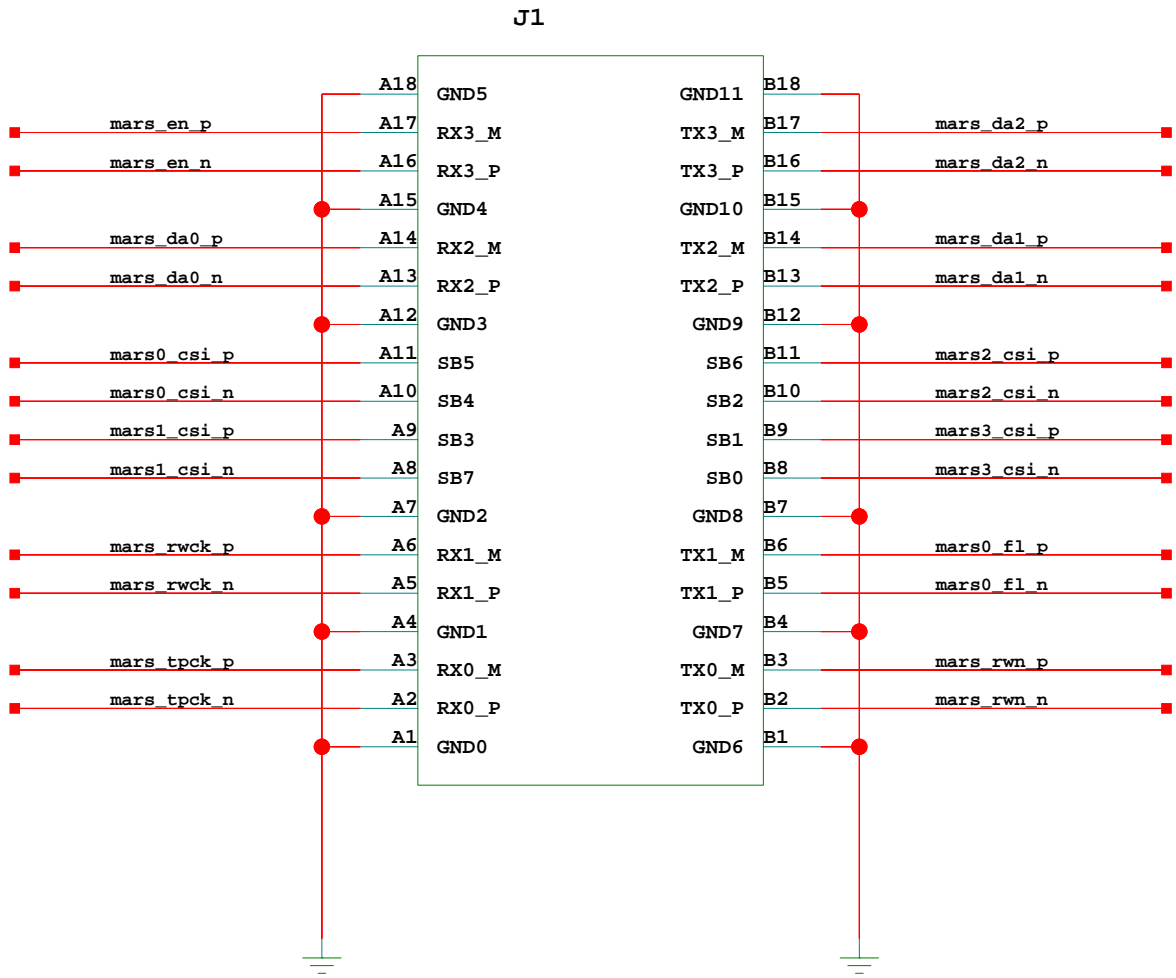


Power :

MARS chips :  $197\text{mW /chip} \times 4 \text{ chips} = 0.8 \text{ W}$  (with 100 ohm load)  
MARS regulators :  $3.3\text{v}-2.5\text{v} \times (0.8 \text{ W}/2.5\text{V}) = 0.26 \text{ W}$   
JFET buffers:  $6.4\text{mA/chip} \times 2 \text{ chips} \times 10\text{v} = .13 \text{ W}$   
JFET regulators :  $2 \text{ regulators} \times (6\text{v}-5\text{v}) \times (6.4\text{mA} \times 2 \text{ chips}) = 0.01 \text{ W}$   
Total Power Estimate = 1.2 W

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miniSAS IPASS Connector  
Input Voltage Requirements : +5v Analog, -5v Analog, +5v Digital



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