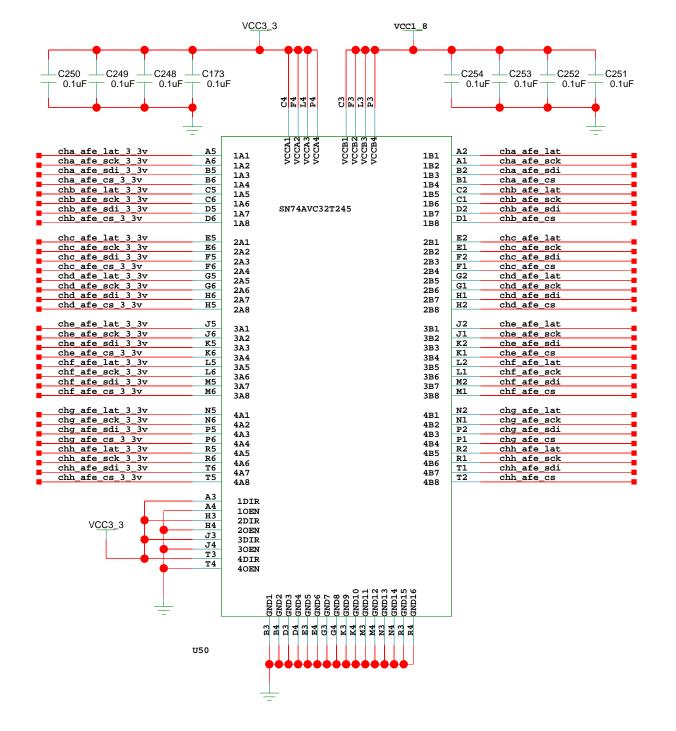
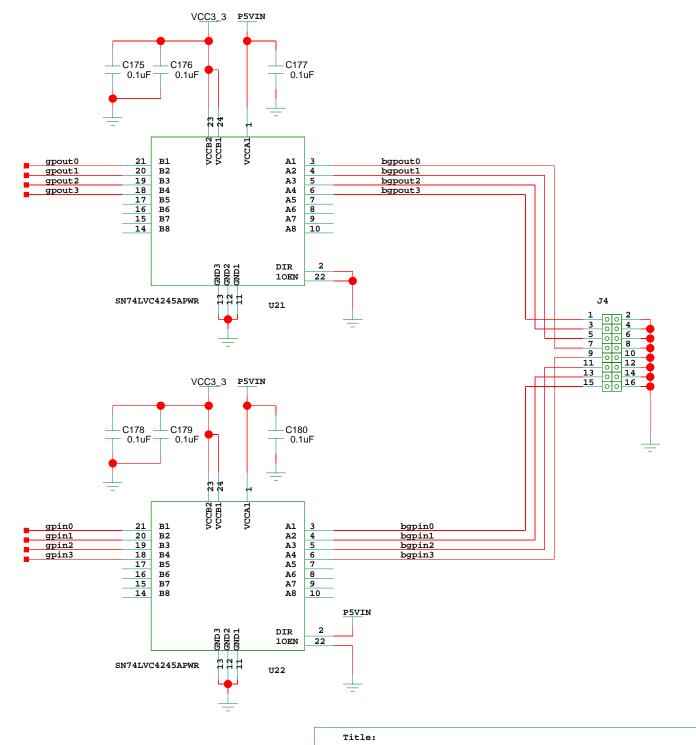


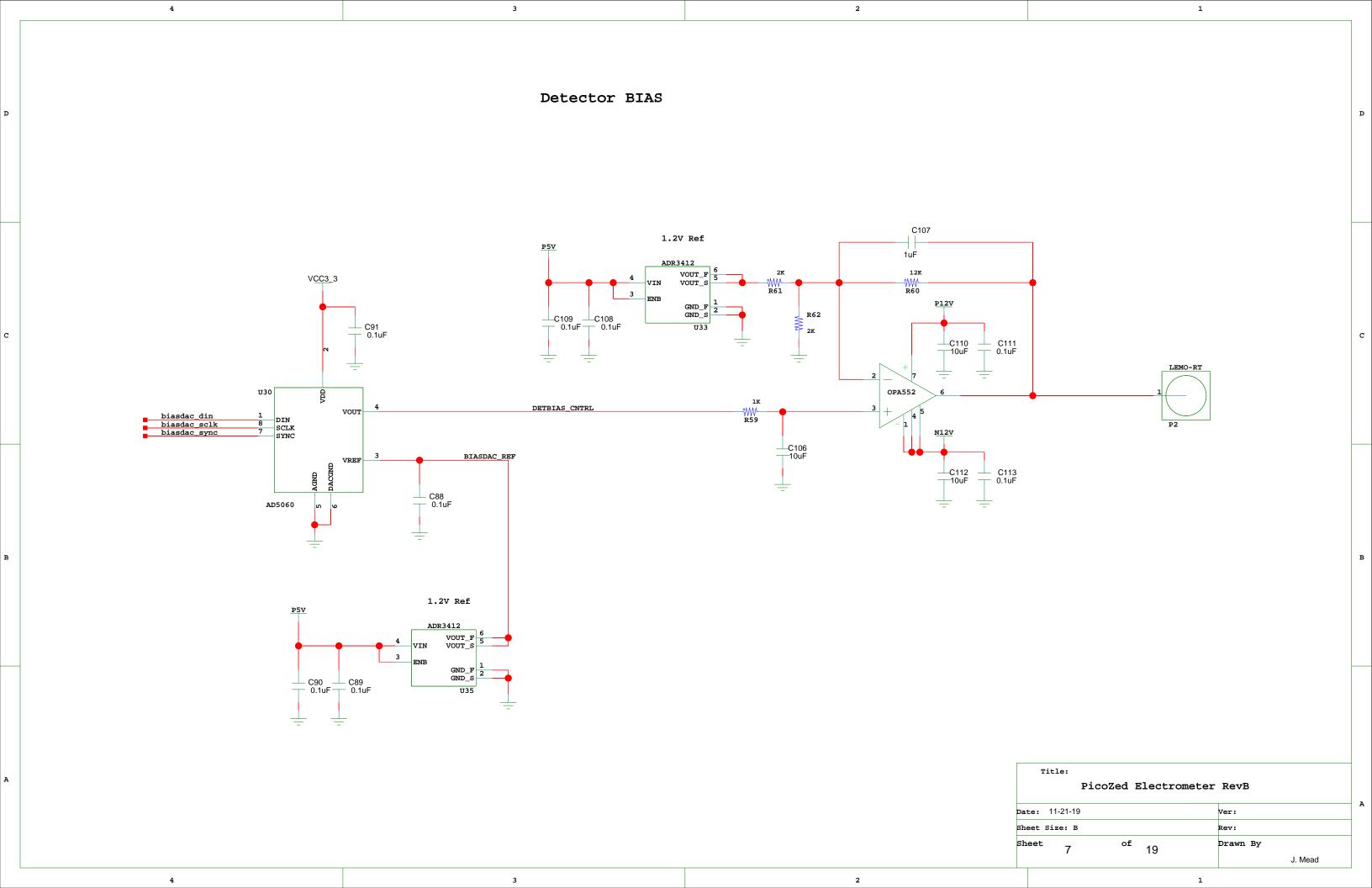
Front End to picoZed

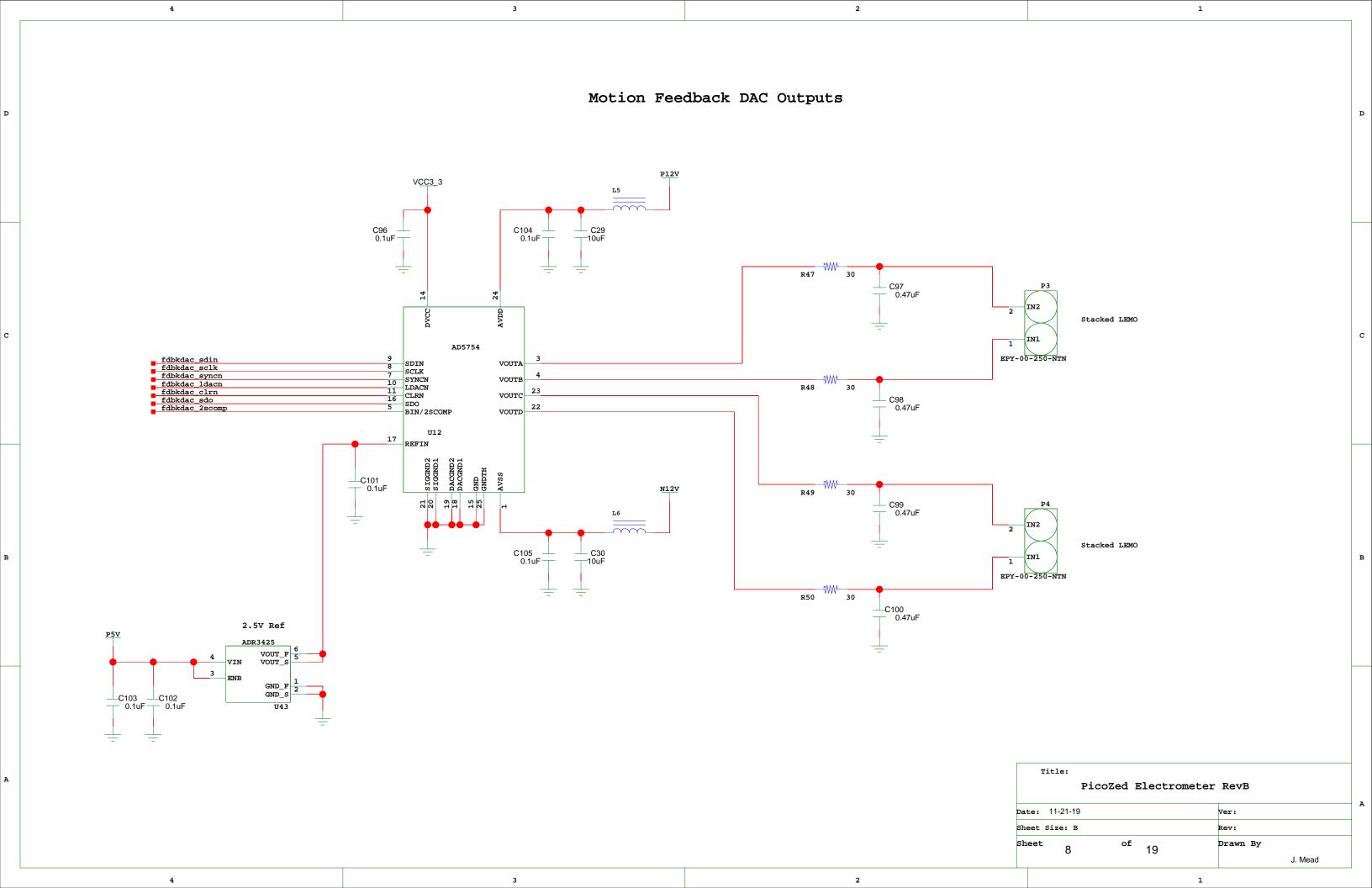


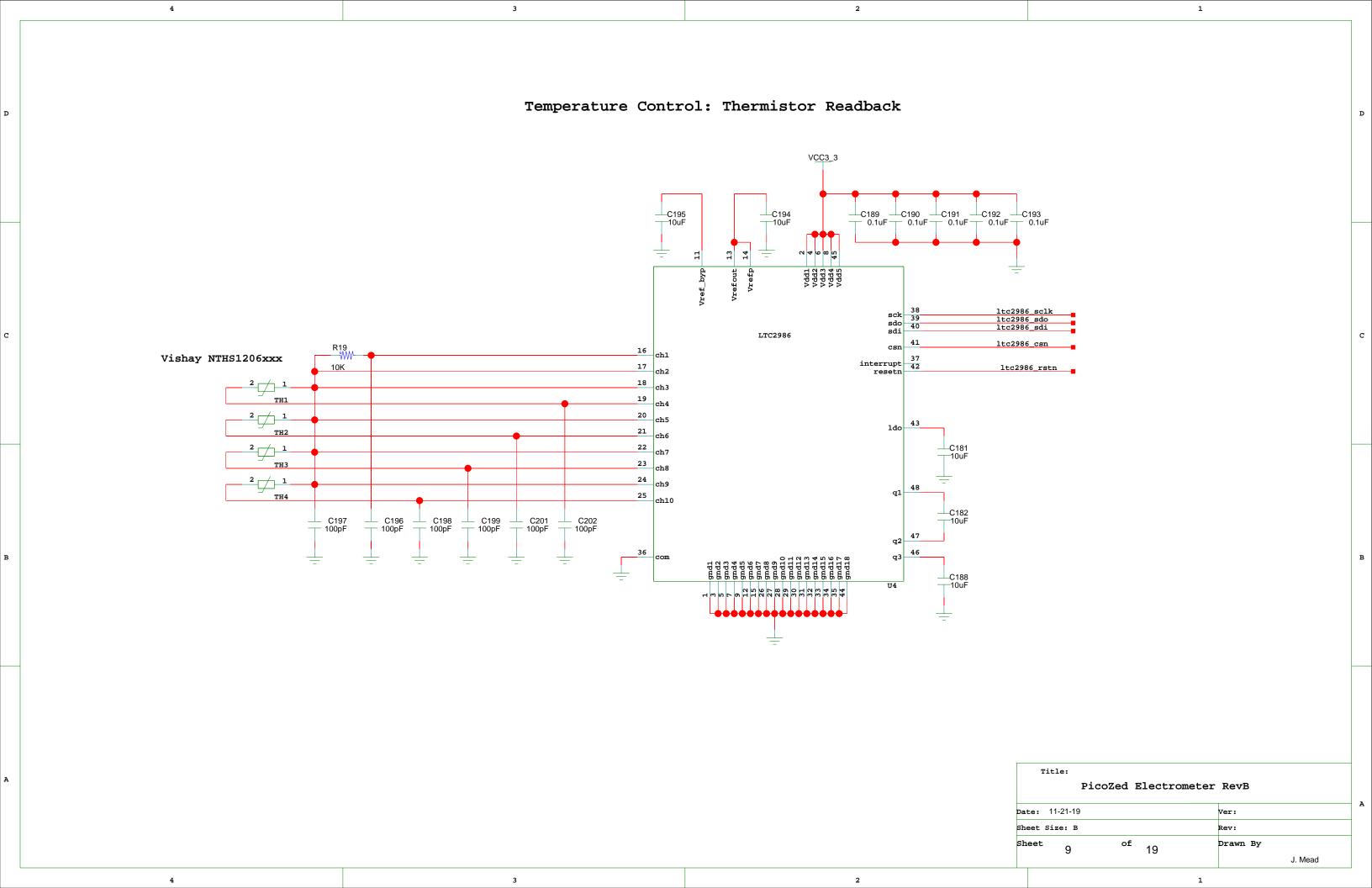
Front panel GPIO

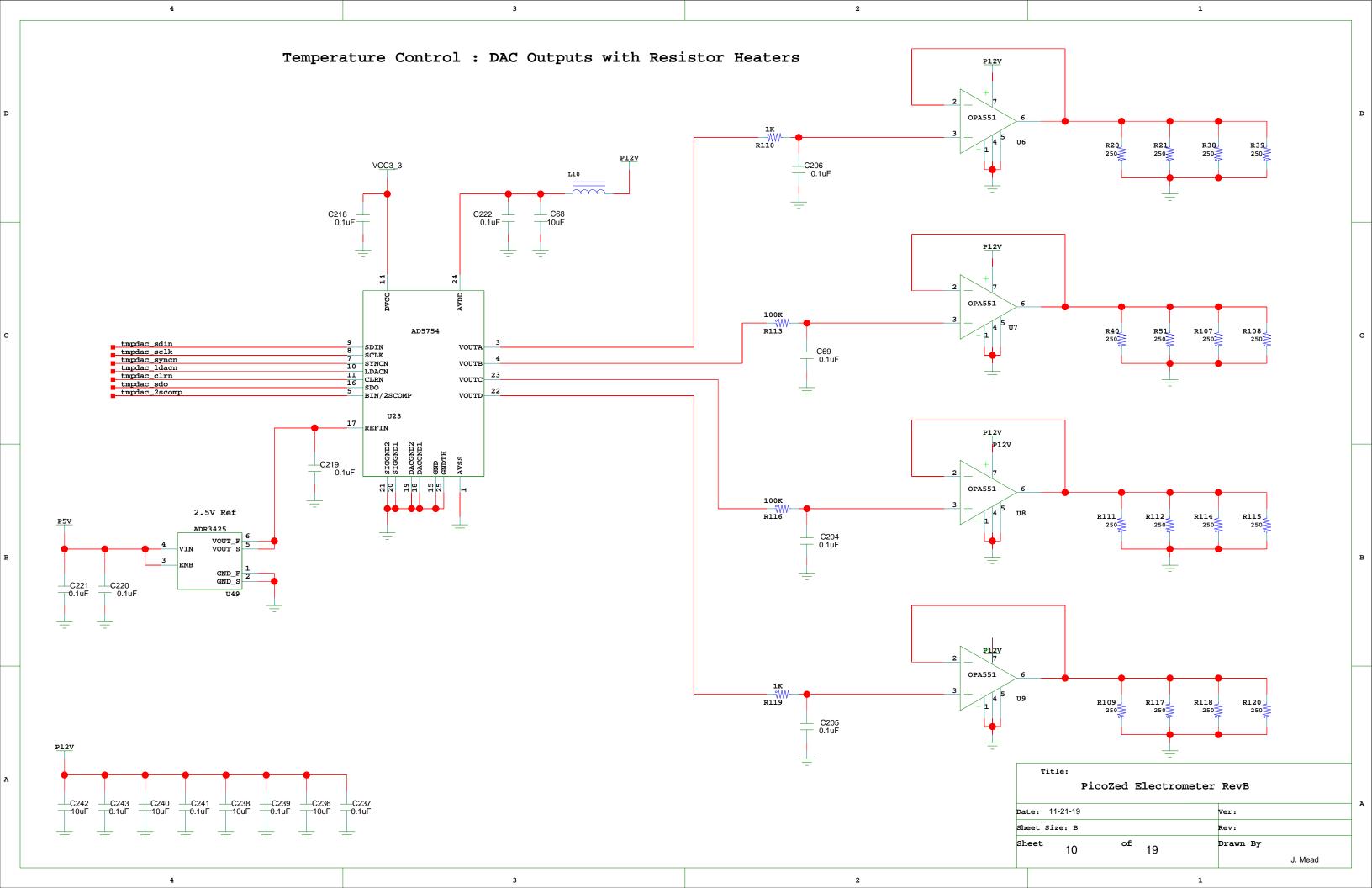
3.3v to 5v level translator











1 picoZED Connectors JX1 & JX2 TP1 61083-101400LF 61083-101400LF jtag_tck jtag_tms JTAG TCK PMOD DO JTAG TMS PMOD D1 jtag_tdi jtag_tdo JTAG_TDO JTAG_TDI PMOD_D2 PMOD_D3 P5VIN PWR_ENABLE CARRIER_SRST# PMOD_D4 PMOD_D5 FPGA_VBATT FPGA_DONE 10 PMOD_D6 PMOD D7 1.0 vcco_enb 11 SE_0 SE_1 12 LVDS_1_P 14 INIT# PG 1V8 12 13 LVDS_0_P cha_adc_cnv cha_adc_bsy F5 PG_MODULE cha adc sck 13 LVDS_0_N 15 LVDS_0_N 17 GND_15 19 LVDS_2_P LVDS_2_N cha adc sdo **E**5 LVDS_1_N SE_0 GND 15 SE 15 17 16 18 GND GND_16 F2 che_adc_cnv J2 cha_afe_lat cha_afe_cs che_adc_bsy LVDS_3_P LVDS_0_P LVDS_1_P 19 cha_afe_sck che_adc_sck che_adc_sdo cha_afe_sdi F1 LVDS_3_N LVDS_0_N LVDS_1_N GND_22 24 LVDS_5_P 26 LVDS_5_N 28 GND_28 30 LVDS_7_P 32 23 GND_21 GND_21 GND_22 __chb_adc_cnv E4 K7 J3 chb_adc_bsy che_afe_lat LVDS_3_P LVDS_3_N che_afe_cs 25 LVDS_4_P 27 LVDS_4_N 29 GND_27 LVDS_2_P LVDS_2_N GND_27 25 26 chb_adc_sck chb_adc_sdo che_afe_sdi che_afe_sck E3 27 28 GND_28 В2 L2 __chb_afe_lat 31 LVDS_6_P chb_afe_cs Р7 chf_adc_cnv chf_adc_bsy LVDS_4_P LVDS_5_P chb afe sdi chb_afe_sck chf_adc_sck chf_adc_sdo 31 33 LVDS_6_N GND_33 LVDS_4_N GND_33 LVDS_5_N 34

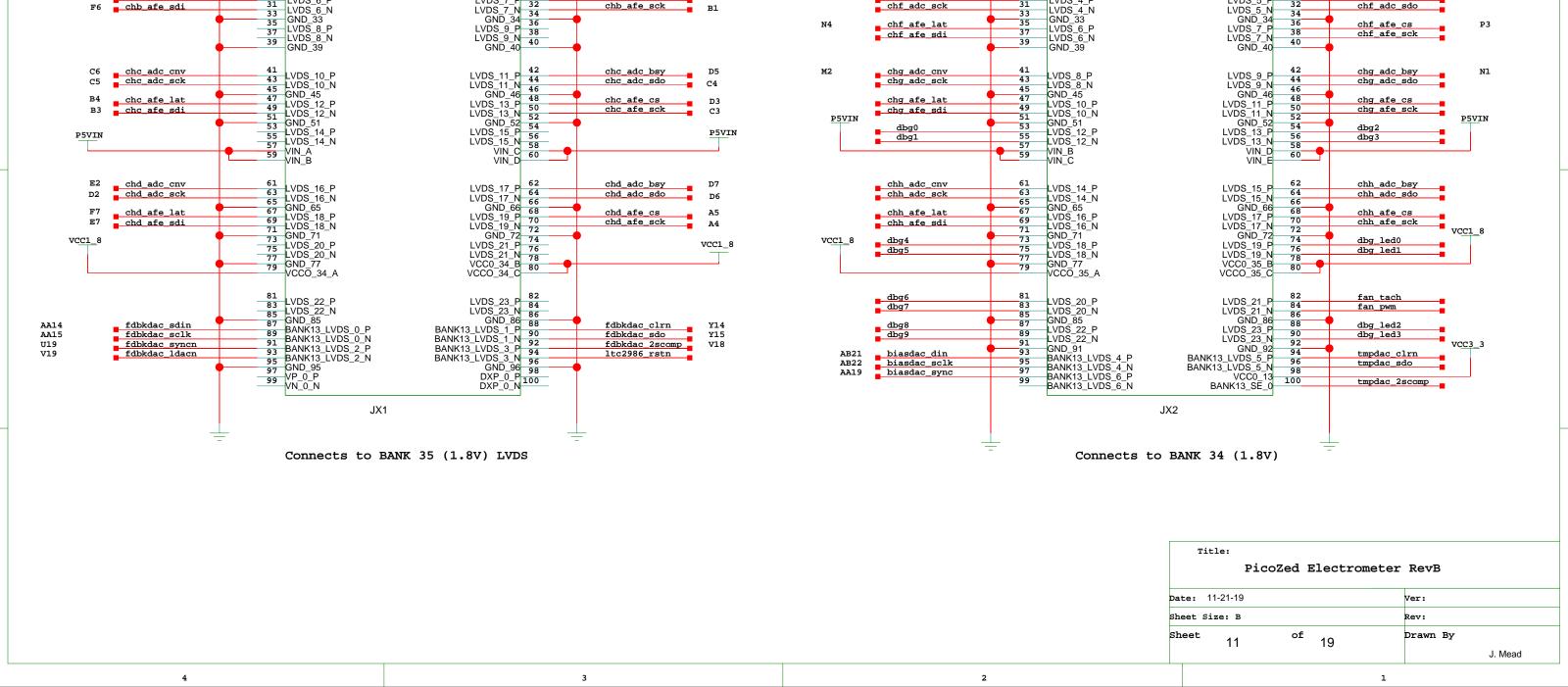
G3

G2

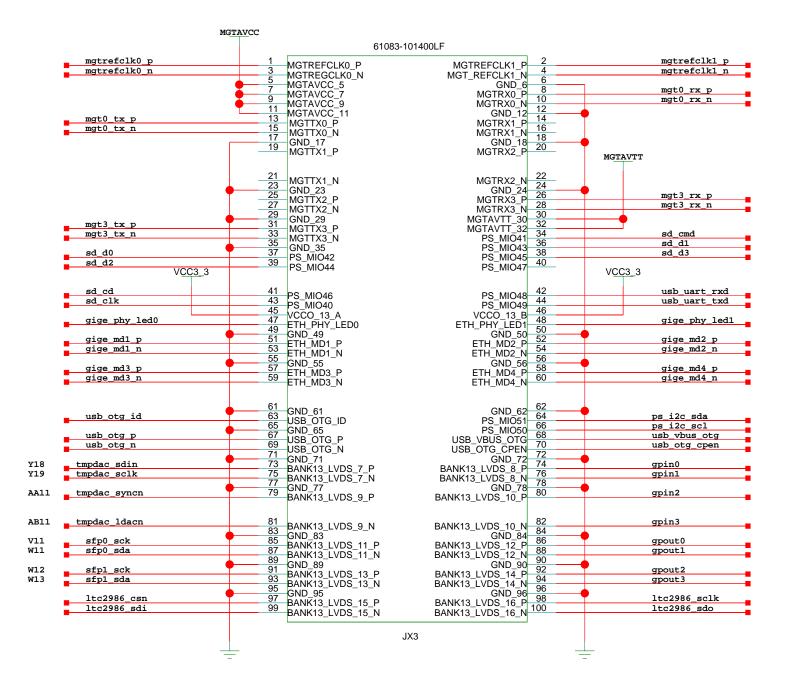
G4

F4

D



picoZED Connector JX3



Т	itle:				
PicoZed Electrometer RevB					
Date: 11-21-19				Ver:	
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	14			J. Mead	

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