

Paper Title*

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4th Given Name Surname

dept. name of organization (of Aff.)

name of organization (of Aff.)

City, Country

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5th Given Name Surname

dept. name of organization (of Aff.)

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Abstract—This document is a model and instructions for \LaTeX . This and the IEEEtran.cls file define the components of your paper [title, text, heads, etc.]. ***CRITICAL: Do Not Use Symbols, Special Characters, Footnotes, or Math in Paper Title or Abstract.**

Index Terms—component, formatting, style, styling, insert

I. INTRODUCTION

This document is a model and instructions for \LaTeX . Please observe the conference page limits.

II. BACKGROUND AND MOTIVATION

A. Address mapping

NAND Flash는기기의 특성으로 인해 Logical Block Address(LBA)와 Physical Page Address(PPA)에 대한 사상이 필요하다.

asdf

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