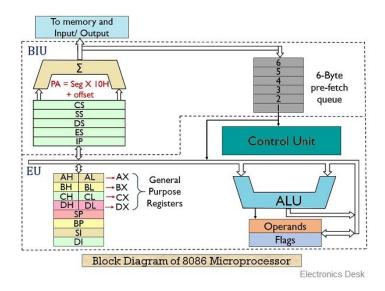
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EE2003

Intel 8086



Execution Unit

- Flag register used to store some result about an operation. Example result of a comparision or overflow bit etc. Used by the ALU
- E(xtended)SP: Stack pointer
- EA/B/C/D: General registers. Do whatever you want
- ESI/ EDI: For string instructions. strlen() or memcpy()

The Extended registers **extend** the A/B/C/DX registers to 32 bit

Bus Interface Unit

CS, SS, DS store the starting address of code, stack and data segments. The IP(instruction pointer) or the A,B,C,D registers are used to store the offset from the start address. Formula is given by:

$$Address = Segment << 4 + offset$$

BP and SP point to SS, IP points to CS, A/B/C/D point to DS (or ES, prof did not remember)

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x86 Ops

```
; EAX <- EBX, register direct
   MOV EAX, EBX
   MOV EAX, [ECX]
                        ; Load from memory, register indirect
2
   MOV [ECX], EAX
                        ; store to memory
3
    MOV EAX, [EBX - N] ; register indirect with offset, requires the EDS as well
4
   MOV EAX, 0x1602
                     ; immediate mode, no address calculation required
5
    MOV EAX, EBX - ECX ; THIS IS WRONG
6
7
8
    ; Load Effective Addr:
9
    ; Essentially you can do math in the right operand.
10
    ; Used in compilers for things like
11
    ; int *p = &point[i].ycoord;
12
    LEA EAX, [EBP+8*EAX+4]
13
    ; ALU Ops
15
    ADD EAX, EBX
16
                        ; EAX <- EAX + EBX
    SUB EAX, DWORD PTR[EBX]
17
    SUB EAX, WORD PTR[EBX]
18
    SUB EAX, BYTE PTR[EBX]
19
20
    ; Logical Ops
21
   AND EAX, DWORD PTR [EBP + 4]
```

1. Write assembly to do the following:

```
EAX = x * y + a - b

EBX = (x \oplus y)|(a \land b)
```

```
; first part
    MOV EAX, X
2
    MUL y
3
    MOV EBX, a
4
    SUB EBX, b
    ADD EAX, EBX
6
    ;second part
8
    MOV EBX, x
9
    XOR EBX, y
10
    MOV ECX, a
11
    AND ECX, b
12
    OR EBX, ECX
```

2. Write a program to evaluate z = x * y using repeated addition

```
XOR EAX, EAX
    MOV EBX, y
2
    CMP EBX, 0
3
    JZ done
4
    add:
5
    ADD EAX, x
6
    DEC EBX
7
    JNZ add
    done:
10
    NOP
11
```

3. Write a program to calculate the string length of a constant string

```
; assuming the data is little endian
1
    XOR EAX, EAX
2
    loop:
    CMP [EBX], 0
    JZ done
    INC EBX
6
    INC EAX
7
    JMP loop
8
9
    done:
10
    NOP
11
```

4. Write a program to swap two integers x and y

```
Swap(int *pX, int *pY){
1
             \__{asm}{}
2
                      MOV EAX, pX
3
                      MOV EBX, pY
4
                      PUSH DWORD PTR [EAX]
6
                      PUSH DWORD PTR [EBX]
7
                      POP DWORD PTR [EAX]
8
                      POP DWORD PTR [EBX]
9
             }
10
    }
11
```

5. Look at these swaps and realize the differences

```
Swap(int *pX, int *pY){
              \__{asm}{}
2
                       ; this thing is like gae
3
                       ; the most useless thing ever
4
                       PUSH pX
5
                       PUSH pY
6
                       POP pX
7
                       POP pY
8
             }
9
10
11
    Swap2(int x, int y){
12
              \__{asm}{}
13
                       ; this thing is like gae
14
                       ; the most useless thing ever
15
                       PUSH x
16
                       PUSH y
                       POP x
18
                       POP y
19
             }
20
21
22
    Swap3(int *pX, int *pY){
23
              _{asm}{
24
                       ; this thing is not gae
25
                       MOV EAX, pX
26
                       MOV EBX, pY
27
                       PUSH DWORD PTR [EAX]
28
                       PUSH DWORD PTR [EBX]
29
                       POP DWORD PTR [EAX]
30
                       POP DWORD PTR [EBX]
31
             }
32
33
```

Compiling a C program

Optimization

You can optimize your *.c code by using the \01 flag if using MSVC. It removes dead code and can optimize to reduce intstructions. However one should be very careful and not trust the compiler to do their job perfectly all the time.

```
int __stdcall Fn(int x, int y){
    /*
    * Prologue
    * PUSH EBP ; store the previous base
```

```
* MOV EBP, ESP
                                     ; set the top as the new base
5
             * SUB ESP, Ox40; leave some space out for local variables(?)
6
7
             // MOV [EBP-4], 0
8
             int z = 0;
9
             z = x+y;
10
             /* MOV EAX, 8[EBP]
11
             * MOV EBX, 12[EBP]
12
               ADD EAX, EBX
             */
14
             MOV [EBP-4], EAX
15
             return z;
16
             /* Epilogue
17
                ADD ESP, 0x40
18
                POP EBP
19
                RET 8
                               ; doing __stdcall forces compiler to do the
20
                                ; stack cleanup
21
22
23
    int main(){
24
             int z=0;
25
             z = Fn(2,4);
26
27
```

Optimization

__stdcall forces the callee to do the stack cleanup. Normal behaviour is RET 0 in the callee function and ADD ESP, N in the caller function. Directly doing RET N is faster(by one clock cycle) but you cannot do variable arguments.

Pushing parameters

The parameters passed to a function are pushed from right to left. For a call like:

the stack will look like:

a	b	c	 ${f z}$
ESP			EBP

Optimization

__fastcall is a calling convention(specific to MSVC) that asks the compiler to use registers to store parameters(wherever possible). This will speed up memory access.

Recursion

```
int fact(int n)
    {
2
             int ans = 1;
3
             if(0 != n){
4
                      ans = n*fact(n-1);
5
             }
6
             return ans;
8
9
    int main()
10
11
             int ans = 0;
12
             ans = fact(3);
13
14
```

This gives the assembly output as:

```
; Prologue
1
             PUSH EBP
2
             MOV EBP, ESP
3
             PUSH ECX
4
5
             MOV EAX, 1
6
             MOV EBX, [EBP+8]
7
             MOV [EBP-4], EAX
8
             CMP EBX, 0
9
             JZ DONE
10
             DEC EBX
11
             PUSH EBX
12
             CALL fact
13
             ADD ESP, 4
14
             MUL [EBP+8]
15
16
    DONE:
17
             ; Epilogue
18
             ADD ESP, 4
19
             POP EBP
20
             RET 0
21
```

The stack will look like (NOTE it grows downwards):

ans-main				
3				
ret-addr-main				
EBP-main				
$\operatorname{ans-fact} 1$				
2				
ret-addr1(0xC200)				
EBP-fact1				
ans-fact2				
1				
ret-addr2(0xC200)				
EBP-fact2				
ans-fact3				
0				
ret-addr3(0xC200)				